

1 NET LIST

The listing on the following pages provides "Net List" information for the connectors on both sides of the station backplane board. The example in Figure 1 describes how to interpret the information.

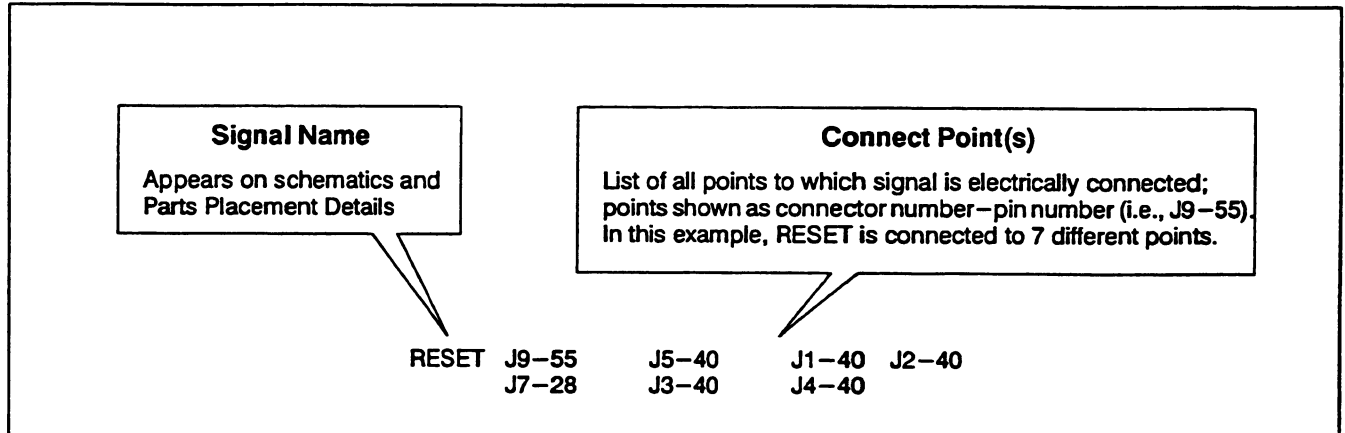


Figure 1. Interpreting Net List Information Example

Net List

RCLK3	J8	42	J15	17	PTT	J14	11	J8	60
LINE1--	J6	2J17	26		MonDet(GCC)	J14	8	J8	20
GenTxData--	J6	26			DataRx(GCC)	J14	7	J8	22
	J17	34			RSSI(GCC)	J14	6	J8	24
AuxOut3	J6	30	J17	38	CTS3	J8	36	J15	5
AuxOut1	J6	28	J17	36	RxD3	J8	34	J15	3
LINE4--	J6	20	J17	29	RemoteLpback3	J8	44	J15	21
LINE2--	J6	8	J17	27	ExtSpare#12	J31	4	J4	16
AuxIn2(TxInh	J6	58	J17	12		J2	16	J5	16
AuxIn6	J6	54	J17	16		J3	16	J1	16
AuxOut7rla+	J6	52	J17	18	ExtSpare#10	J31	2	J4	14
AuxOut9Rla+	J6	50	J17	20		J2	14	J5	14
AuxIn9--opto+	J6	48	J17	22		J3	14	J1	14
AuxIn11--opto+	J6	46	J17	24	AuxOut8--Rel--	J6	36	J17	44
AuxIn4(Rxinh	J6	56	J17	14	AuxOut5	J6	32	J17	40
RxD1	J8	50	J20	2	GenTxData+	J6	64	J17	9
CTS1	J8	52	J20	8	AuxIn12--opto--	J6	44	J17	50
LINE3--	J6	14	J17	28	AuxIn1Siteoft	J6	60	J17	11
Modem--	J8	1	J17	30	ExtSpare#16	J32	7	J6	72
LinePTTDtGCC	J14	10	J8	16	ExtSpare#14	J32	9	J6	70
ExtSpare#8	J31	3	J4	12	AuxIn8	J6	34	J17	42
	J2	12	J5	12	AuxOut10--Rel--	J6	40	J17	46
	J3	12	J1	12	CCI/Monitor	J14	24	J8	64
ExtSpare#2	J31	9	J4	6	Mute/PLStrip	J14	23	J8	62
	J2	6	J5	6	SerialID	U1	2	J8	58
	J3	6	J1	6	AuxIn10--opto--	J6	42	J17	48
DATA*1	J2	78	J7	78	PL+In	J6	62	J17	10
DATA*2	J4	78	J7	70	TDMCLOCK	J4	46	J2	46
ODC2	J4	75	J7	68		J5	46	J7	34
SBI1	J2	76	J7	76	RN1	8J3	46	J1	46
5MHZREF	J4	70	J5	70	SPARE#4	J4	50	J2	50
	J7	64	J30	1		J5	50	J7	38
Modem+	J8	2	J17	5		J9	60	J3	50
AGC1	J2	74	J7	74		J1	50		
1PPS	J8	74	J21	1	SPARE#6	J4	52	J2	52
DLAN2--	J8	12	J18	5		J5	52	J7	40
	J18	9				J9	62	J3	52
DLAN1--	J8	10	J19	5		J1	52		
	J19	9			TDMFRAMESYN CJ4	J4	44	J2	44
ETHERNET	J8	78	J22	1		J5	44	J7	32
WFI--	J8	8	J18	3	RN16	J3	44	J1	44
	J18	7	J19	3	HDL CBUSY	J4	42	J2	42
	J19	7				J5	42	J7	30
ExtSpare#4	J31	7	J4	8	RN13	J3	42	J1	42
	J2	8	J5	8	A5	J4	56	J2	56
	J3	8	J1	8		J5	56	J7	44
ExtSpare#6	J31	5	J4	10		J3	56	J1	56
	J2	10	J5	10		J10	69		
	J3	10	J1	10	A3	J4	60	J2	60
Async--	J8	56	J20	6		J5	60	J7	46
DCD1	J8	54	J20	1		J3	60	J1	60
						J10	73		

A4	J4	58	J2	58
	J5	58	J7	45
	J3	58	J1	58
	J10	71		
A2	J4	62	J2	62
	J5	62	J7	47
	J3	62	J1	62
	J10	75		
SPIMOSI	J4	38	J2	38
	J5	38	J7	26
	J9	53	J3	38
	J1	38	J10	61
RESET	J4	40	J2	40
	J5	40	J7	28
	J9	55	J3	40
	J1	40	J10	63
SPARE#2	J4	48	J2	48
	J5	48	J7	36
	J9	57	J3	48
	J1	48	J10	65
	J11	61		
A1 (CS2)	J4	54	J2	54
	J5	54	J7	42
	J3	54	J1	54
	J10	68		
SPARE#5	J4	51	J2	51
	J5	51	J7	39
	J9	61	J3	51
	J1	51		
XMITSPIREQ	J7	48	J9	65
HST_REQ	J7	50	J9	67
SPARE#1	J4	47	J2	47
	J5	47	J7	35
	J9	56	J3	47
	J1	47	J10	64
	J11	60		
SPARE#3	J4	49	J2	49
	J5	49	J7	37
	J9	58	J3	49
	J1	49	J10	66
	J11	62		
PTTREQ	J7	49	J9	66
SPICLK	J4	39	J2	39
	J5	39	J7	27
	J9	54	J3	39
	J1	39	J10	62
SPIMISO	J4	37	J2	37
	J5	37	J7	25
	J9	52	J3	37
	J1	37	J10	60
AMUXCTRL	J9	48	J11	58
X3MUX	J9	46	J11	56
X2MUX	J9	44	J11	54
X1MUX	J9	42	J11	52
FAN_ON	J9	38	J11	48
FINALA_TUNE1	J9	36	J11	46
DRIVERA_TUNE1	J9	34	J11	44

VfinalREFLEC	J9	22	J11	39
PATEMP	J9	20	J11	37
TX16.8MHZREF	J4	66	J5	66
	J7	56	J9	70
VCOAUDIO	J7	60	J9	78
28V	J9	4	J10	4
	J10	5	J10	6
	J10	7	J10	8
	J10	9	J10	10
	J10	11	J10	12
	J10	13	J10	14
	J10	15	J11	4
	J11	5		
28V	J11	6	J11	7
	J11	8	J11	9
	J11	10	J11	11
	J11	12	J11	13
	J11	14	J11	15
Vcontrol	J9	15	J9	16
	J9	17	J9	18
	J11	32	J11	33
	J11	34	J11	35
ANTRYKEYEDA+	J9	31	J23	2
	J27	8		
EXTWMVr	J9	26	J27	3
EXTCircTemp	J9	28	J27	5
EXTI/O1	J9	32	J27	6
RxAudio	J14	22	J4	17
	J2	17	J5	17
	J8	68	J3	17
	J1	17		
ETHERNETISOGDJ8	J8	76	J8	80
	J22	2	J22	3
	J22	4	J22	5
5V	J31	8	J4	27
	J4	28	J4	29
	J4	30	J4	31
	J4	32	J4	33
	J4	34	J2	27
	J2	28	J2	29
	J2	30	J2	31
	J2	32		
5V	J2	33	J2	34
	J5	27	J5	28
	J5	29	J5	30
	J5	31	J5	32
	J5	33	J5	34
	J7	9	J7	10
	J7	11	J7	12
	J7	13		
5V	J7	14	J7	15
	J7	16	J7	17
	J7	18	J7	19
	J7	20	J7	21
	J7	22	J9	11
	J9	12	J17	8
	J3	27	J3	28
	J3	29		

5V	J3	30	J3	31	GND	J7	61	J7	62
	J3	32	J3	33		J7	65	J7	66
	J3	34	J1	27		J7	72	J7	73
	J1	28	J1	29		J7	79	J7	80
	J1	30	J1	31		J6	38	J6	53
	J1	32	J1	33		J6	79	J6	80
	J1	34	J10	24		J8	5	J8	6
	J10	25				J8	13		
5V	J10	26	J10	27	GND	J8	14	J8	26
	J10	28	J10	29		J8	27	J8	28
	J10	30	J10	31		J8	29	J8	30
	J11	24				J8	31	J8	38
BATTERYTEMP	J24	2	J10	59		J8	45	J8	46
						J8	47	J8	48
GND	J14	9	J14	16		J8	55	J8	65
	J14	17	J14	18		J8	66		
	J14	19	J14	20	GND	J8	70	J8	71
	J31	1	J31	10		J8	72	J8	73
						J8	75	J8	77
GND	J4	1	J4	2		J8	79	J18	1
	J4	3	J4	4		J15	1	J15	7
	J4	18	J4	19		J9	1	J9	2
	J4	20	J4	21	GND	J9	13	J9	14
	J4	35	J4	36		J9	23	J9	24
	J4	57	J4	61		J9	29	J9	30
	J4	63	J4	64		J9	39	J9	40
	J4	67				J9	41	J9	50
						J9	51	J9	68
GND	J4	68	J4	71		J9	69	J9	71
	J4	72	J4	73		J9	72		
	J4	79	J4	80	GND	J9	73	J9	74
	J2	1	J2	2		J9	75	J9	76
	J2	3	J2	4		J9	79	J9	80
	J2	18	J2	19		J19	1	J21	2
	J2	20	J2	21		J21	3	J21	4
	J2	35				J21	5	J30	2
						J30	3	J30	4
GND	J2	36	J2	59		J30	5		
	J2	61	J2	63	GND	J23	1	J23	3
	J2	64	J2	67		J17	7	J17	32
	J2	68	J2	71		J27	1	J27	9
	J2	72	J2	73		J24	1	J24	3
	J2	79	J2	80		J20	5	J3	1
	J5	1	J5	2		J3	2	J3	3
	J5	3				J3	4	J3	18
						J3	19		
GND	J5	4	J5	18		J3	20	J3	21
	J5	19	J5	20	GND	J3	35	J3	36
	J5	21	J5	35		J3	57	J3	63
	J5	36	J5	57		J3	64	J3	67
	J5	59	J5	63		J3	68	J3	71
	J5	64	J5	67		J3	72	J3	73
	J5	68	J5	71		J3	79	J3	80
	J5	72				J1	1		
GND	J5	73	J5	79		J1	2	J1	3
	J5	80	J7	1	GND	J1	4	J1	18
	J7	2	J7	3		J1	19	J1	20
	J7	4	J7	23		J1	21	J1	35
	J7	24	J7	51		J1	36	J1	59
	J7	52	J7	53		J1	63	J1	64
	J7	54	J7	57		J1	67	J1	68
	J7	58				J1	71		

GND	J1	72	J1	73	13.8V	J10	22	J10	23
	J1	79	J1	80		J11	16	J11	17
	J10	1	J10	2		J11	18	J11	19
	J10	3	J10	32		J11	20	J11	21
	J10	33	J10	34		J11	22	J11	23
	J10	35	J10	36	Locallpback3	J8	40	J15	18
	J10	37	J10	38	AuxIn10-opto+	J6	49	J17	23
	J10	39			AuxOut10R+	J6	51	J17	21
GND	J10	40	J10	41	TxPLInhitGCC	J14	5	J8	15
	J10	42	J10	43	ODC1	J2	75	J7	75
	J10	44	J10	45	SBI2	J4	76	J7	69
	J10	46	J10	47	ExtSpare#1	J32	5	J4	5
	J10	48	J10	49		J2	5	J5	5
	J10	50	J10	51		J3	5	J1	5
	J10	52	J10	53	ExtSpare#5	J32	1	J4	9
	J10	74				J2	9	J5	9
GND	J10	76	J10	77		J3	9	J1	9
	J10	78	J11	1	ExtSpare#7	J32	2	J4	11
	J11	2	J11	3		J2	11	J5	11
	J11	25	J11	26		J3	11	J1	11
	J11	27	J11	28	ExtSpare#15	J32	10	J6	71
	J11	29	J11	30	ExtSpare#13	J32	8	J6	69
	J11	31	J11	40	DATA2	J4	77	J7	71
	J11	41			AGC2***	J4	74	J7	67
GND	J11	42	J11	43	DLAN1+	J8	9	J19	4
	J11	49	J11	50		J19	8		
	J11	51	J11	63	ExtSpare#3	J32	3	J4	7
	J11	64	J11	65		J2	7	J5	7
	J11	66	J11	67		J3	7	J1	7
	J11	68	J11	69	LINE4+	J6	19	J17	4
	J11	70	J11	71	LINE3+	J6	13	J17	3
	J11	72			LINE2+	J6	7	J17	2
GND	J11	73	J11	74	LINE1+	J6	1	J17	1
	J11	75	J11	76	WFI+	J8	7	J18	2
	J11	77	J11	78		J18	6	J19	2
13.8V	J31	6	J4	22		J19	6		
	J4	23	J4	24	DLAN2+	J8	11	J18	4
	J4	25	J4	26		J18	8		
	J2	22	J2	23	RingIndicator	J8	49	J20	9
	J2	24	J2	25	TxD1	J8	51	J20	3
	J2	26	J5	22	RTS1	J8	53	J20	7
	J5	23	J5	24	Async+	J8	57	J20	4
	J5	25			AUXCarrier	J14	14	J8	61
13.8V	J5	26	J7	5	SeizeRelseGC	J14	4	J8	17
	J7	6	J7	7	RxMute(GCC)	J14	3	J8	19
	J7	8	J9	3	DataPTT(GCC)	J14	2	J8	21
	J9	5	J9	6	DataTx(GCC)	J14	1	J8	23
	J9	7	J9	8	RXPLDET	J14	15	J8	25
	J9	9	J9	10	TXData-	J14	21	J8	69
	J17	33	J27	10	TSTAT/RXCaie	J14	12	J8	59
	J3	22			TXData+/TXAio	J14	13	J8	67
13.8V	J3	23	J3	24	TxD3	J8	33	J15	2
	J3	25	J3	26	RTS3	J8	35	J15	4
	J1	22	J1	23	DCD3	J8	39	J15	8
	J1	24	J1	25	TCLK3	J8	41	J15	15
	J1	26	J10	16					
	J10	17	J10	18					
	J10	19	J10	20					
	J10	21							

DTR3	J8	43	J15	20	TDMDATA	J4	45	J2	45
ExtSpare#9	J32	4	J4	13		J5	45	J7	33
	J2	13	J5	13	RN17	J3	45	J1	45
	J3	13	J1	13	HDLCCCLK	J4	43	J2	43
ExtSpare#11	J32	6	J4	15		J5	43	J7	31
	J2	15	J5	15	RN1	4J3	43	J1	43
	J3	15	J1	15	HDLCDATA	J4	41	J2	41
PL-In	J6	27	J17	35		J5	41	J7	29
AuxOut2(RxCo	J6	29	J17	37	RN1	2J3	41	J1	41
AuxOut4	J6	31	J17	39	SPIGRANT	J7	43	J9	59
AuxOut6	J6	33	J17	41	A0(CS1)	J4	53	J2	53
TXWIDEBANDAIO	J4	69	J2	69		J5	53	J7	41
	J5	69	J7	63		J3	53	J1	53
	J3	69	J1	69		J10	67		
RX16.8MHZREF	J4	65	J2	65	REFAUDIO	J7	59	J9	77
	J5	65	J7	55	BMUXCTRL	J9	49	J11	59
	J3	65	J1	65	Y3MUX	J9	47	J11	57
AuxOut8Rla+	J6	57	J17	19	Y2MUX	J9	45	J11	55
AuxIn7	J6	59	J17	17	Y1MUX	J9	43	J11	53
AuxIn5	J6	63	J17	15	FINALA_TUNE2	J9	37	J11	47
AuxIn3(ExtTx	J6	65	J17	13	DRIVERA_TUNE2	J9	35	J11	45
AuxIn9-opto-	J6	39	J17	47	VfinalFORWARD	J9	19	J11	36
AuxOut7-Rela-	J6	35	J17	43	VdriverFOARD	J9	21	J11	38
AuxOut9-Rela-	J6	37	J17	45	EXTWVMf	J9	25	J27	2
DATA1	J2	77	J7	77	EXTWMLRef	J9	27	J27	4
RSTAT/PahInh	J14	25	J8	63	EXTI/O2	J9	33	J27	7
DSR3	J8	37	J15	6	AuxIn11-opto-	J6	41	J17	49
					AuxIn12-opto+	J6	43	J17	25

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2 CONNECTOR PINOUT INFORMATION

Figure 2 shows the backplane board viewed from the rear with all connectors labeled. The tables on the facing page provide pinout information for each corresponding connector.

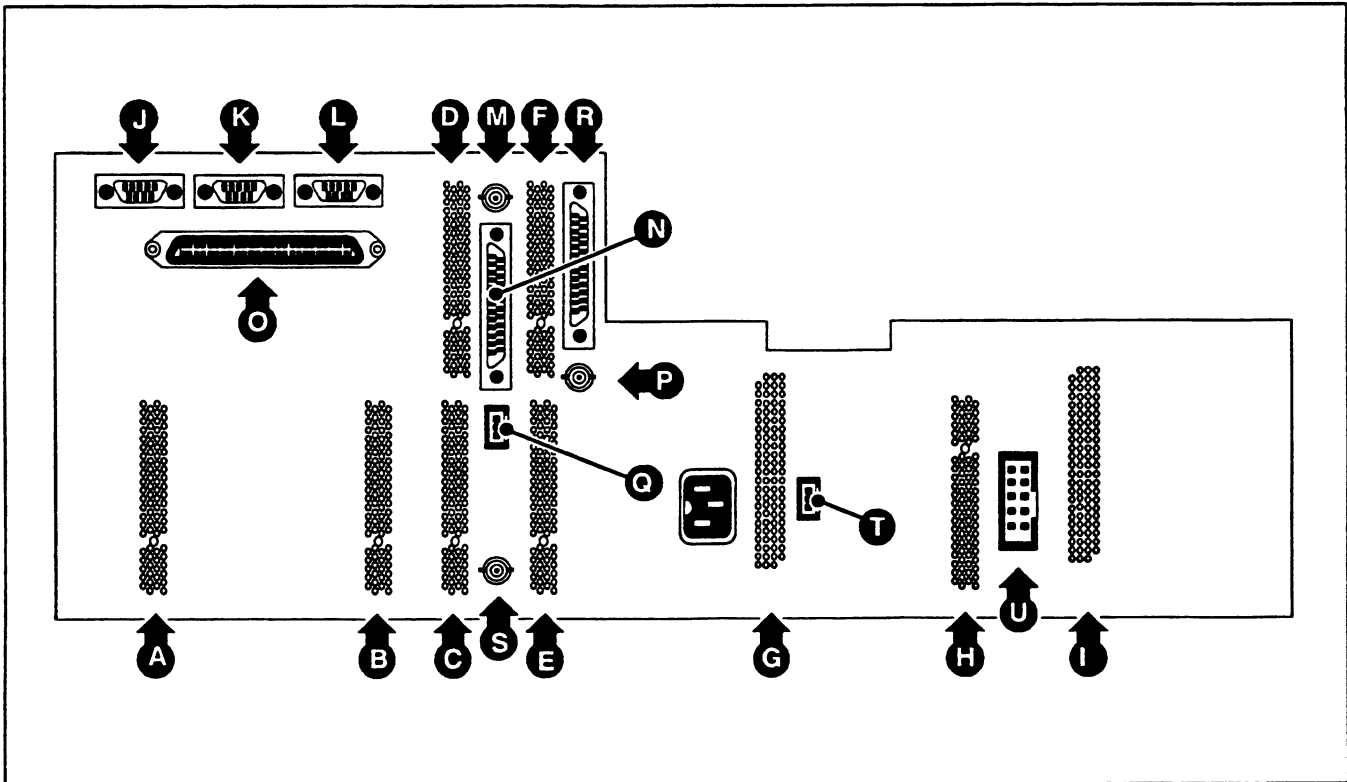


Figure 2. Quantar Backplane Connectors

A J2 RECEIVER #1

Table with 2 columns: Pin #, Signal Name. Contains 80 rows of pin assignments for J2 RECEIVER #1.

B J4 RECEIVER #2/UHSO

Table with 2 columns: Pin #, Signal Name. Contains 80 rows of pin assignments for J4 RECEIVER #2/UHSO.

C J5 WIRELINE INTERFACE BOARD

Table with 2 columns: Pin #, Signal Name. Contains 80 rows of pin assignments for J5 WIRELINE INTERFACE BOARD.

D J6 WIRELINE INTERFACE BOARD

Table with 2 columns: Pin #, Signal Name. Contains 80 rows of pin assignments for J6 WIRELINE INTERFACE BOARD.

E J7 STATION CONTROL

Table with 2 columns: Pin #, Signal Name. Contains 80 rows of pin assignments for J7 STATION CONTROL.

F J8 STATION CONTROL

Table with 2 columns: Pin #, Signal Name. Contains 80 rows of pin assignments for J8 STATION CONTROL.

G J10 POWER SUPPLY

Table with 2 columns: Pin #, Signal Name. Contains 80 rows of pin assignments for J10 POWER SUPPLY.

H J9 Exciter

Table with 2 columns: Pin #, Signal Name. Contains 80 rows of pin assignments for J9 Exciter.

I J11 POWER AMPLIFIER

Table with 2 columns: Pin #, Signal Name. Contains 80 rows of pin assignments for J11 POWER AMPLIFIER.

J J20 Async RS232/485

Table with 2 columns: Pin #, Signal Name. Contains 10 rows of pin assignments for J20 Async RS232/485.

K J18 DLAN1

Table with 2 columns: Pin #, Signal Name. Contains 10 rows of pin assignments for J18 DLAN1.

L J19 DLAN2

Table with 2 columns: Pin #, Signal Name. Contains 10 rows of pin assignments for J19 DLAN2.

M J21 1PPS INPUT

Table with 2 columns: Pin #, Signal Name. Contains 5 rows of pin assignments for J21 1PPS INPUT.

N J14 6809/MRTI

Table with 2 columns: Pin #, Signal Name. Contains 25 rows of pin assignments for J14 6809/MRTI.

O J17 System 50-Pin Telco

Table with 2 columns: Pin #, Signal Name. Contains 25 rows of pin assignments for J17 System 50-Pin Telco.

R J15 SYNC RS232

Table with 2 columns: Pin #, Signal Name. Contains 25 rows of pin assignments for J15 SYNC RS232.

S J30 5 MHz REF INPUT

Table with 2 columns: Pin #, Signal Name. Contains 5 rows of pin assignments for J30 5 MHz REF INPUT.

T J24 BATTERY TEMPERATURE

Table with 2 columns: Pin #, Signal Name. Contains 3 rows of pin assignments for J24 BATTERY TEMPERATURE.

U J27 PERIPHERAL TRAY

Table with 2 columns: Pin #, Signal Name. Contains 10 rows of pin assignments for J27 PERIPHERAL TRAY.

P J22 ETHERNET In/Out

Table with 2 columns: Pin #, Signal Name. Contains 5 rows of pin assignments for J22 ETHERNET In/Out.

Q J23 ANTENNA RELAY

Table with 2 columns: Pin #, Signal Name. Contains 3 rows of pin assignments for J23 ANTENNA RELAY.

* Isolated from chassis ground