Who's yo DAHDI

An examination of HamVoIP theft

- Bryan Fields, W9CR
- 30-DEC-2019

Introductions

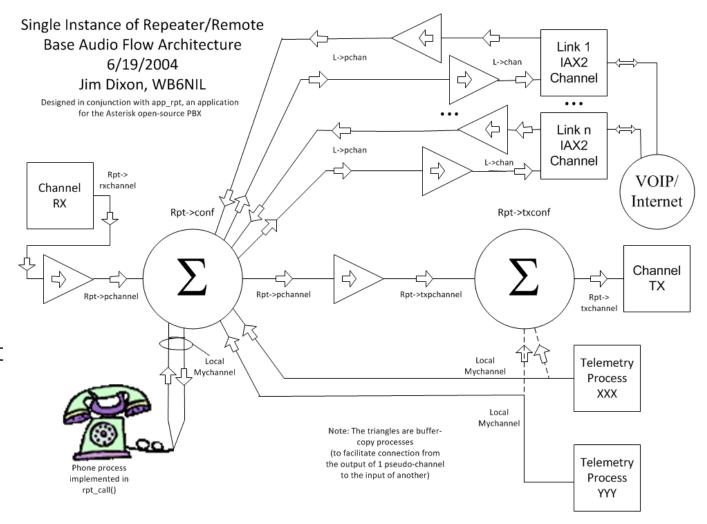
- app_rpt is a plug-in for Asterisk
- Zaptel does hardware i
- Combine with radio = A
- Zapata became DAHDI
- Jim designed it all



app_rpt flow diagram Circa 2004

Timing Alignment is important

Older hardware included this as the kernel lacked it Newer kernels on Intel don't have a problem



Issues on ARM

- ARM Acorn RISC Machine
- It's the Raspberry Pi!
- Asterisk (newer asterisk) runs well, Older no so much (1.4 is allstar)
- Why?

Timing!

- Intel has this fixed, and this can cause stuttering and such in the conference calls that app_rpt uses
- Pickle Linux for the BeagleBoard-xM 2011 or so by Jim and N7PKT with the LoX interface
- The first ports of code to it RPi
 - 2013 RPi 1 B+ 700 MHz single core <u>ARMv6</u>, 512mb ram (runs like ass)
 - 2013 Beagle Bone Black 1GHz ARMv7, 512mb ram, works well as IO is not a factor
 - 2015 RPi 2 900 MHz quad-core ARMv7, 1gb ram first serious contender, USB issues
 - 2106 RPi 3 1200 MHz quad-core ARMv7, 1gb ram worked well

Porting to Arch Linux

Anthony, VK2ACP ports it and gets Zaptel/DAHDI running

[App_rpt-users] Arch Linux version of allstar has been stable

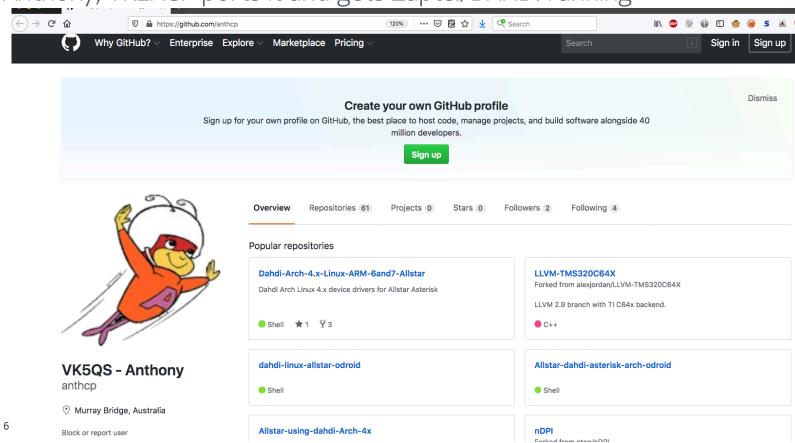
Anthony Percy anthcp at gmail.com

Thu Jan 17 06:10:18 UTC 2013

- Previous message (by thread): [App_rpt-users] Echolink
- Next message (by thread): [App_rpt-users] Arch Linux version of allstar has been stable for 2 weeks now....
- Messages sorted by: [date] [thread] [subject] [author]

Porting to Arch Linux

Anthony, VK2ACP ports it and gets Zaptel/DAHDI running



Porting to ARM

First inquiries about Pi and BBB from Doug

[App_rpt-users] Asterisk and Allstar on the Raspberry Pi

Doug Crompton doug at crompton.com

Sat Apr 20 17:35:54 UTC 2013

Previous message (by thread): [<u>App_rpt-users</u>] <u>Voter Question</u>

Next message (by thread): [App_rpt-users] Asterisk and Allstar on the Raspberry Pi

• Messages sorted by: [date] [thread] [subject] [author]

I have been using the Pi for awhile on IRLP with great success. It runs without a hitch. I was curious if anyone is using or has explored the use of the Pi with Asterisk on Allstar? It seems like the requirements would be similar. I use a USB sound FOB and the standard IRLP controller board. The Pi GPIO drives the IRLP controller in place of the standard PC parallel port.

I suspect the URI radio adapter or equivalent is all I would need in the way of hardware for Allstar. I have experience with Asterisk as I have been using it in a residential PBX for almost 10 years.

73 Doug WA3DSP

http://www.crompton.com/hamradio

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URL: <http://lists.keekles.org/pipermail/app_rpt-users/attachments/20130420/8269264a
/attachment.html>

[App_rpt-users] Beagleboard Black and Allstar

Doug Crompton doug at crompton.com

Thu Apr 25 23:21:34 UTC 2013

- Previous message (by thread): [App_rpt-users] A completely off-topic request
- Next message (by thread): [App_rpt-users] Beagleboard Black and Allstar
- Messages sorted by: [date] [thread] [subject] [author]

What are thoughts on using the Beaglebone Black in place of the Beagleboard for Allstar? It is about 1/3 of the price and it looks like it has as much or more capability. http://beagleboard.org/Products

73 Doug
WA3DSP
http://www.crompton.com/hamradio

icept//www.crompeonream/nameadro

----- next part -----

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URL: <http://lists.keekles.org/pipermail/app_rpt-users/attachments/20130425/86f8176e/attachment.html>

David and Doug release a package for the BBB June 2014

[App_rpt-users] BeagleBone Black Allstar Official Release

Doug Crompton doug at crompton.com

Thu Jun 26 05:02:00 UTC 2014

- Previous message (by thread): [App_rpt-users] Question
- Next message (by thread): [App_rpt-users] BeagleBone Black Allstar Official Release
- Messages sorted by: [date] [thread] [subject] [author]

Hello and welcome to the first public release of Allstar on the BeagleBone Black. This represents months of trial and error testing which first started with the Raspberry Pi back at the beginning of the year and was finally successful on the BBB platform. We have had several beta versions out now for over two months with many testers and we had very good reports. There is more to be done but this is a working system that will allow many to get rid of their energy consuming PC's with constantly running hard drives and have an Allstar system that fits in the palm of your hand.

The web page I have created gives very detailed instructions on how to download, install, and configure your BBB for use with Allstar. The web page is:

hamvoip.org

There you will find a link to download the image and also to join the arm-allstar email list. Please try to address questions specific to the BBB and Allstar to the arm-allstar list and not the apt-rpt list.

David and Doug release a package for the BBB June 2014 – Jims follow up

[App_rpt-users] BeagleBone Black Allstar Official Release

Jim Duuuude telesistant at hotmail.com

Thu Jun 26 18:04:03 UTC 2014

- Previous message (by thread): [App_rpt-users] BeagleBone Black Allstar Official Release
- Next message (by thread): [App_rpt-users] BeagleBone Black Allstar Official Release
- Messages sorted by: [date] [thread] [subject] [author]

It has quite sadly become painfully necessary to "Officially" and publicly state that this release is *NOT* an "Official" Allstar release. It has not been sanctioned by me or the Allstar network. It is something that this person is doing completely independently.

I am not in any way trying to infer that there is anything "right" or "wrong", or anything else about it, merely that is in an independent operation and I/we are not in any way responsible for its contents and/or use.

Jim WB6NIL

David and Doug release a package for the BBB June 2014 – David ack's it is not AllStar

David McGough kb4fxc at inttek.net

Thu Jun 26 22:03:33 UTC 2014

- Previous message (by thread): [App_rpt-users] BeagleBone Black Allstar Official Release
- Next message (by thread): [App_rpt-users] BeagleBone Black Allstar Official Release
- Messages sorted by: [date] [thread] [subject] [author]

Hi Jim and everyone,

I think Doug probably meant "officially released" since we've had several beta-test releases over the last 6 weeks or so.

I'm unfortunately not surprised by your comments, but, am disappointed. As the father of the AllStar project, you should be grinning from ear to ear that your baby has grown to the point that other developers are helping to build *your* network without requiring any of your time.

I, for one, hope this isn't another fork in the road. I hope it's just another step in the evolution of this already fantastic radio/repeater control and VoIP solution you envisioned.

As a software developer and project manager with decades of experience, one thing I've had to painfully learn is that everybody has there own vision for how a piece of software or a system should function. You can't stifle this creativity. Rather, you have to embrace it and direct it. If the system works reliably, inter-operates (with AllStar) compatibly and is being (pro)actively supported, then you smile and say: Good job guys! We need this feature next! Or, how about finding a solution for that issue???

This shouldn't be a competition. Lets work for a common goal.

Present Day September 2019 Skyler asks about hr_timer

```
On Wed, 24 Sep 2019, Skyler F wrote:

> 
> Dahdi only runs well on a raspberry pi with Dahdi_Hrtimer.
> What does this do? How was this written? What is the reason why without
> this the raspberry pi has unreliable audio timing versus a normal
> computer?
> 
> Other audio applications like audio / video streaming seem to run fine so
> why did you guys have to write something special for asterisk on the pi?
> 
> Thanks
> Skyler
```

From: David McGough <kb4fxc@inttek.net>
Date: Wed, Sep 25, 2019 at 8:22 PM
Subject: Re: What does dahdi_hrtimer do?
To: Skyler F <electricity440@gmail.com>

Skyler,

I've spent literally hundreds of hours fixing timing issues in AllStar; Too much to even begin to try and describe in an e-mail. The dahdi_hrtimer code is one part of all the fixes. The HRtimer driver was cobbled together by me.

This driver simulates (with lower resolution) the HPET timer functionality which X86 PC's have included in their design since about 2005. The ARM processor does not have any equivalent timer. So, I'm taking advantage of the ARM SDC counter found in the Broadcom SoC chips. The counter is free-running at 1MHz, with a 64-bit incrementing accumulator (so overflows are not a concern). I've adapted this timing source to the needs of AllStar / Asterisk---and, it works wonderfully.

As for why other audio/video applications work on the RPi?? Well, the demands of simply playing (or recording) an audio or video stream are simpler than what AllStar / Asterisk is doing. Asterisk is real-time mixing and perhaps transcoding audio from multiple sources. Each of these sources will have slightly different timing (meaning the stream bit rate will be a little fast or slow). An highly accurate clock is required to merge these streams, regenerating the frame timing in the process. Without this, you end up with pops, clicks, etc. What's happening underneath the covers is non-trivial.

What could this be? It's part of DAHDI, no?

• First we boot it up and look around at the kernel modules

What could this be? It's part of DAHDI, no?

First we boot it up and look around at the kernel modules

```
[root@booty ~]# lsmod |grep dahdi
dahdi_hrtimer 16384 0
dahdi 229376 69 dahdi_hrtimer
crc_ccitt _ 16384 1 dahdi
```

WOW, with it loaded!

```
[root@booty ~]# dahdi_test -c 20
Opened pseudo dahdi interface, measuring accuracy...
99.984% 99.989% 99.987% 99.988% 99.992% 99.991% 99.992% 99.994%
99.987% 99.989% 99.993% 99.988% 99.993% 99.988% 99.992% 99.990%
99.992% 99.993% 99.986% 99.991%
--- Results after 20 passes ---
Best: 99.994% -- Worst: 99.984% -- Average: 99.990017%
Cummulative Accuracy (not per pass): 99.990
```

What could this be? It's part of DAHDI, no?

Lets rmmod it and see

```
[root@booty ~]# rmmod dahdi_hrtimer
[root@booty ~]# dahdi_test -c 20
Opened pseudo dahdi interface, measuring accuracy...
99.458% 98.676% 98.519% 99.618% 99.621% 99.622% 98.519% 99.620%
99.618% 99.653% 99.600% 98.536% 99.620% 99.613% 99.449% 99.619%
99.620% 99.445% 99.619% 99.453%
--- Results after 20 passes ---
Best: 99.653% -- Worst: 98.519% -- Average: 99.374804%
Cummulative Accuracy (not per pass): 99.963
```

Yea, that's kind of normal for the RPi.

```
[root@booty ~]# cat /proc/sys/kernel/tainted
5120
```

Bits 10 and 12 are set, but the kernel is still all GPL: https://www.kernel.org/doc/html/latest/admin-guide/tainted-kernels.html

Remember John David is much smarter than us Where's the SOURCE!

- So lets decompress it and look at it
- gunzip /usr/lib/modules/4.19.65-1-ARCH/dahdi/dahdi_hrtimer.ko.gz
- Readelf
- Oh, it's got debugging symbols!
- It's GNU GPL code or so it says to the kernel!
- text (the code) is only 360h or 864 bytes.
- Lets strip it and see

```
Magic:
           7f 45 4c 46 01 01 01 00 00 00 00 00 00 00 00 00
 Class:
                                      ELF32
                                      2's complement, little endian
 Data:
 Version:
                                      1 (current)
 OS/ABI:
                                      UNIX - System V
 ABI Version:
                                      REL (Relocatable file)
  Type:
  Machine:
                                      ARM
  Version:
                                      0x1
 Entry point address:
                                      0x0
 Start of program headers:
                                      0 (bytes into file)
 Start of section headers:
                                      5524 (bytes into file)
                                      0x5000000, Version5 EABI
 Flags:
 Size of this header:
                                      52 (bytes)
                                      0 (bytes)
  Size of program headers:
  Number of program headers:
                                      40 (bytes)
 Size of section headers:
 Number of section headers:
                                      24
  Section header string table index: 21
Section Headers:
  [Nr] Name
                          Type
   0]
                          NÜLL
      .note.gnu.build-i NOTE
   2]
      .text
                          PROGBITS
      .rel.text
                         REL
      .ARM.extab
                          PROGBITS
      .ARM.exidx
                          ARM EXIDX
      .rel.ARM.exidx
                         REL
      .rodata
                          PROGBITS
      .rel.rodata
                         REL
                          PROGBITS
       .modinfo
      .rodata.str1.4
                          PROGBITS
       __param
                          PROGBITS
      .rel__param
  [12]
                          REL
       .note.Linux
                          PROGBITS
  [14]
       .data
                          PROGBITS
      .gnu.linkonce.thi PROGBITS
      .rel.gnu.linkonce REL
  [17]
      .bss
                          NOBITS
       .comment
                          PROGBITS
       .note.GNU-stack
                         PROGBITS
      .ARM.attributes
                         ARM ATTRIBUTES
      .shstrtab
                          STRTAB
  [22] .symtab
                          SYMTAB
                                                                          23
                                                                              63 4
                                          00000000 0009d4 000560 10
  [23] .strtab
                          STRTAB
                                          00000000 000f34 0002e7 00
                                                                              0
```

Remember John David is much smarter the description of the SOURCE!

- So lets decompress it and look at it
- gunzip /usr/lib/modules/4.19.65-1-ARCH/dahdi/dahdi_hrtimer.ko.gz
- Readelf
- Oh, it's got debugging symbols!
- It's GNU GPL code or so it says to the kernel!
- text (the code) is only 360h or 864 bytes
- Lets strip it and see:

```
dahdi hrtimer int
debug
description=Timing-Only Driver
parmtype=debug:int
depends=dahdi
name=dahdi hrtimer
vermagic=4.19.65-1-ARCH SMP preempt mod_unload ARMv7 p2v8
5dahdi hrtimer: HRTimer missed %lu ticks
7dahdi hrtimer: 5000 ticks from hrtimer
3dahdi_hrtimer: Unable to allocate memory
dahdi hrtimer
DAHDI_HRTIMER/1
%s (source: HRtimer) %d
DAHDI HRTIMER/%d/%d
DAHDI Hi-Res Kernel Timing
3dahdi_hrtimer: Unable to intialize DAHDI driver (%d)
7dahdi_hrtimer: Trying to load High Resolution Timer
7dahdi hrtimer: Initialized High Resolution Timer
7dahdi_hrtimer: Starting High Resolution Timer
6dahdi_hrtimer: High Resolution Timer started, good to go
7dahdi hrtimer: init() finished
7dahdi_hrtimer: cleanup() finished
Linux
dahdi hrtimer
GCC: (GNU) 5.3.0
GCC: (GNU) 5.3.0
aeabi
shstrtab
.note.gnu.build-id
.text
.ARM.extab
.ARM.exidx
.rodata
.modinfo
rodata.str1.4
 param
.note.Linux
.data
.gnu.linkonce.this_module
bss
comment
.note.GNU-stack
.ARM.attributes
```

[root@booty ~]# strings dahdi_hrtimer.ko-stripped

Remember John David is much smarter the License GPL v2 Where's the SOURCE!

- So lets decompress it and look at it
- gunzip /usr/lib/modules/4.19.65-1-ARCH/dahdi/dahdi hrtimer.ko.gz
- Readelf
- Oh, it's got debugging symbols!
- It's GNU GPL code or so it says to the kernel!
- .text (the code) is only 360h or 864 bytes
- Lets strip it and see:
- OhWhoa! Misspellings, I < 3 misspellings
- It's initialize not intialize
- **GOOGLE SEARCH!**
 - "Unable to intialize DAHDI driver"

```
depends=dahdi
name=dahdi hrtimer
vermagic=4.19.65-1-ARCH SMP preempt mod_unload ARMv7 p2v8
5dahdi_hrtimer: HRTimer missed %lu ticks
7dahdi hrtimer: 5000 ticks from hrtimer
3dahdi_hrtimer: Unable to allocate memory
dahdi hrtimer
DAHDI_HRTIMER/1
%s (source: HRtimer) %d
DAHDI HRTIMER/%d/%d
DAHDI Hi-Res Kernel Timing
3dahdi_hrtimer: Unable to <u>intialize</u> DAHDI driver (%d)
7dahdi_hrtimer: Trying to load High Resolution Timer
7dahdi hrtimer: Initialized High Resolution Timer
7dahdi_hrtimer: Starting High Resolution Timer
6dahdi_hrtimer: High Resolution Timer started, good to go
7dahdi hrtimer: init() finished
7dahdi_hrtimer: cleanup() finished
Linux
dahdi hrtimer
GCC: (GNU) 5.3.0
GCC: (GNU) 5.3.0
aeabi
.shstrtab
.note.gnu.build-id
.text
.ARM.extab
.ARM.exidx
rodata
modinfo
rodata.str1.4
 param
.note.Linux
.data
.gnu.linkonce.this_module
bss
comment
.note.GNU-stack
.ARM.attributes
```

[root@booty ~]# strings dahdi_hrtimer.ko-stripped

dahdi hrtimer int

parmtype=debug:int

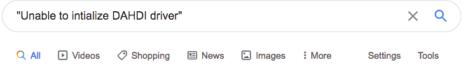
description=Timing-Only Driver

debug



Remember John David is much Where's the SOURCE!

- So lets decompress it and look at it
- gunzip /usr/lib/modules/4.19.65-1-ARCH/dahdi/dahdi_hrtimer.ko.gz
- Readelf
- Oh, it's got debugging symbols!
- It's GNU GPL code or so it says to the kernel!
- .text (the code) is only 360h or 864 bytes
- Lets strip it and see:
- OhWhoa! Misspellings, I <3 misspellings
- It's initialize not intialize
- GOOGLE SEARCH!
 - "Unable to intialize DAHDI driver"



6 results (0.45 seconds)

dahdi-linux/dahdi dummy.c at master · asterisk/dahdi-linux ...

https://github.com > asterisk > dahdi-linux > blob > master > drivers > dah... ▼ return -ENOMEM;. } res = dahdi_dummy_initialize(ztd);. if (res) {. printk(KERN_ERR. "dahdi_dummy: Unable to intialize DAHDI driver (%d)\n",. res);. kfree(ztd);.

Asterisk Guru

https://www.asteriskguru.com > archives > view-previous-topic-vt130850 ▼

Aug 27, 2008 - if (ztdummy_initialize(ztd)) { - printk(KERN_ERR "ztdummy: Unable to Intialize

DAHDI driver\n"); + memset(ztd, 0x0, sizeof(struct dahdi_dummy));

drivers/dahdi/dahdi_dummy.c - Debian Salsa

https://salsa.debian.org > pkg-voip-team > dahdi-firmware > blob > dahdi_... \checkmark ... allocate memory\n"); return -ENOMEM; } if (dahdi_dummy_initialize(ztd)) { printk(KERN_ERR "dahdi_dummy: Unable to intialize DAHDI driver\n"); kfree(ztd); ...

File rtc.patch of Package dahdi-linux - openSUSE Build Service

https://build.opensuse.org > package > view_file > home:vitsoft:asterisk-11 ▼ ... 0x0, sizeof(struct dahdi_dummy)); - if (dahdi_dummy_initialize(ztd)) { printk(KERN_ERR "dahdi_dummy: Unable to intialize DAHDI driver\n"); kfree(ztd); return ...

Loopback DAHDI Driver for DAHDI Telephony interface ...

https://moythreads.com > dahdi_loop •

... if (dahdi_loop_initialize(dahdi_loop)) { printk("dahdi_loop: Unable to Intialize dahdi driver\n"); kfree(dahdi_loop); return -ENODEV; } printk(KERN_DEBUG ...

WTF, it's not just Dummy, is it? dahdi_dummy is for timing only

- This hasn't been needed as dahdi has it built in, and Jim built the dummy driver in 2001
- Wait, only on intel. This be ARM.
- John David didn't just copy it and rename it
- Well lets look at the timer interface

```
[root@booty ~]# cat /proc/timer_list
Timer List Version: v0.8
HRTIMER_MAX_CLOCK_BASES: 8
now at 2224174991104 nsecs
cpu: 0
clock 0:
              85ed87e7
 .base:
 .index:
 .resolution: 1 nsecs
 .aet time:
              ktime_get
 .offset:
              0 nsecs
ctive timers:
#0: HRtimer, dahdi_hrtimer_int, S:01
# expires at 2224175546145-2224175546145 nsecs [in 555041 to 555041 nsecs]
#1: <24a6cb73>, tick_sched_timer, S:01
# expires at 2224180000000-2224180000000 nsecs [in 5008896 to 5008896 nsecs]
#2: <4cade7ed>, hrtimer_wakeup, S:01
# expires at 2227634625901-2227639625899 nsecs [in 3459634797 to 3464634795 nsecs]
#3: <776f36fe>, timerfd_tmrproc, S:01
# expires at 2298933067000-2298933067000 nsecs [in 74758075896 to 74758075896 nsecs]
#4: sched_clock_timer, sched_clock_poll, S:01
# expires at 4398046511078-4398046511078 nsecs [in 2173871519974 to 2173871519974 nsecs]
clock 1:
              b1d3c4df
  .base:
```

Can we compile dummy on ASL1.01? Lets see if it works.

- Grabbed the code and compiled on ASL
 - Have to enable it in /usr/src/asl-dahdi-linux-2.11.1/linux/drivers/dahdi/Kbuild
- It built, so lets try it!
- Hell look at that:

```
root@StPeteRpt:/usr/src/asl-dahdi-linux-2.11.1/linux# dahdi_test -c 20 Opened pseudo dahdi interface, measuring accuracy... 99.985% 99.984% 99.991% 99.989% 99.989% 99.989% 99.989% 99.989% 99.989% 99.991% 99.991% 99.991% 99.991% 99.991% 99.991% 99.991% 99.991% 99.991% 99.991% --- Results after 20 passes --- Best: 99.991% -- Worst: 99.984% -- Average: 99.989389% Cumulative Accuracy (not per pass): 99.989 root@StPeteRpt:/usr/src/asl-dahdi-linux-2.11.1/linux#
```

root@StPetekpt:/usr/src/asi-dandi-linux-2.11.1/linux/drivers -rw-r--r-- 1 root root 8716 Oct 1 14:26 dahdi_dummy.ko

Can we compile dummy on ASL1.01? Lets see if it works.

- Grabbed the code and compiled on AS
 - Have to enable it in /usr/src/asl-dahdi-lir
- It built, so lets try it!
- Hell look at that:
- This is not exact, the code is larger here in ASL1.01 (3e8 vs. 360 for hr)
- It is a different Kernel.
- Can we compile on HamVoIP and compare?

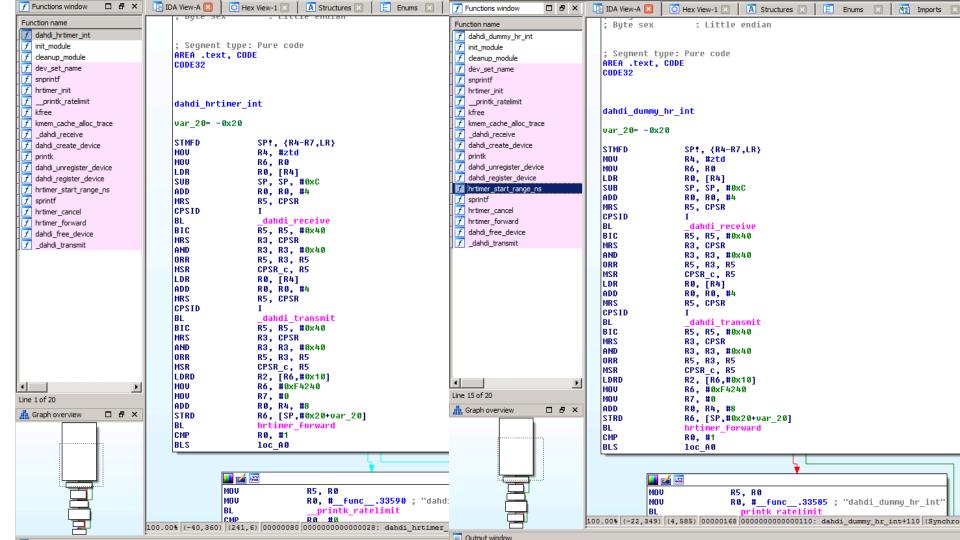
```
Start of section headers:
                                     7676 (bytes into file)
Flags:
                                     0x5000000, Version5 EABI
 Size of this header:
                                     52 (bytes)
 Size of program headers:
                                     0 (bytes)
 Number of program headers:
 Size of section headers:
                                     40 (bytes)
 Number of section headers:
                                     26
 Section header string table index: 25
Section Headers:
 [Nr] Name
                                         Addr
                                                  0ff
                                                          Size
                                                                 ES Flg Lk Inf Al
                         Type
 [ 0]
                         NULL
                                         0000000 000000 000000
      .note.gnu.build-i NOTE
                                         00000000 000034 000024 00
     .text
                         PROGBITS
                                         00000000 000058 0003e8 00
      .rel.text
                         REL
                                         00000000 001a04 000280 08
                         PROGBITS
      .ARM.extab
      .ARM.exidx
                         ARM EXIDX
      .rel.ARM.exidx
                         REL
                                                  001c84 000038 08
                         PROGBITS
      . rodata
                                         00000000 00047c 000076 00
      .rel.rodata
                         REL
                                         00000000 001cbc 000008 08
                                                                      I 23
      .modinfo
                         PROGBITS
                                         00000000 0004f4 0000d9 00
      .rodata.str1.4
                         PROGBITS
                                         00000000 0005d0 00022f 01 AMS
                         PROGBITS
                                         00000000 000800 000014 00
      __param
                                         00000000 001cc4 000020 08
      .rel__param
                         REL
      __mcount_loc
                         PROGBITS
                                         00000000 000814 00000c 00
 [14] .rel__mcount_loc
                        REL
                                                  001ce4 000018 08
        versions
                         PROGBITS
                                         00000000 000820 000600 00
 [16] .data
                         PROGBITS
                                         00000000 000e20 000000 00
      .gnu.linkonce.thi PROGBITS
                                         00000000 000e40 000200 00
                                                                              0 64
 [18] .rel.gnu.linkonce REL
                                         00000000 001cfc 000010 08
      .bss
                         NOBITS
      .comment
                         PROGBITS
                                         00000000 001040 00006a 01
```

Dahdi from ASL on HamVoIP Nate's port is working

- Nate and Stacy got dahdi compiling on the newer kernel for the Pi 4
- I grabbed it and compiled it on HamVoIP
- Stripped of symbols, the files are the same size!
- Well that doesn't mean they are the same, lets look in IDA

```
[root@booty ~]# ls -al *stripped
-rw-r--r- 1 root root 3384 Dec 29 22:08 dahdi_dummy.ko-stripped
-rw-r--r- 1 root root 3384 Dec 29 22:06 dahdi_hrtimer.ko-stripped
```

[root@booty ~]# readelf -e dahdi_h	rtimer.ko-stripped	[[root@booty ~]# readelf -e dahdi_dumm	my.ko-stripped			
ELF Header: Magic: 7f 45 4c 46 01 01 01 00	00 00 00 00 00 00 00	ELF Header:	2 22 22 22 22 22 22 22			
Magic: 7f 45 4c 46 01 01 01 00 Class:	ELF32	Magic: 7f 45 4c 46 01 01 01 00 0	6 66 66 66 66 66 66 66 ELF32			
Data:	2's complement, little endian	Class: Data:	2's complement, little endian			
Version:	1 (current)	Version:	1 (current)			
OS/ABI:		OS/ABI:				
ABI Version:	UNIX — System V 0	ABI Version:	UNIX – System V 0			
	REL (Relocatable file)		REL (Relocatable file)			
Type: Machine:	ARM	Type: Machine:	ARM			
Version:	0x1	Version:	0x1			
			0×0			
Entry point address:	0x0	Entry point address:	0 (bytes into file)			
Start of program headers: Start of section headers:	0 (bytes into file) 2704 (bytes into file)	Start of program headers: Start of section headers:	2704 (bytes into file)			
Flags: Size of this header:	0x5000000, Version5 EABI 52 (bytes)	Flags: Size of this header:	0x5000000, Version5 EABI 52 (bytes)			
		Size of program headers:	0 (bytes)			
Size of program headers:	0 (bytes) 0	Number of program headers:	0 (bytes)			
Number of program headers: Size of section headers:		Size of section headers:	•			
Number of section headers:	40 (bytes) 17	Number of section headers:	40 (bytes) 17			
		Section header string table index:				
Section header string table inde	X: 10	Section header string table index:	10			
Section Headers:		Section Headers:				
[Nr] Name Type	Addr Off Size ES Flg I		Addr Off Size ES Flg Lk Inf Al			
[0] NULL	0000000 000000 000000 00	[0] NÚLL	00000000 000000 000000 00 0 0 0			
[1] .note.gnu.build-i NOTE	00000000 000034 000024 00 A	[1] .note.gnu.build-i NOTE	00000000 000034 000024 00 A 0 0 4			
[2] .text PROGBITS	00000000 000058 000364 00 AX	[2] .text PROGBITS	00000000 000058 000364 00 AX 0 0 4			
[3] .ARM.extab PROGBITS	00000000 0003bc 000000 00 A	[3] .ARM.extab PROGBITS	00000000 0003bc 000000 00 A 0 0 1			
[4] .ARM.exidx ARM_EXIDX		[4] .ARM.exidx ARM_EXIDX	00000000 0003bc 000018 00 AL 2 0 4			
[5] .rodata PROGBITS	00000000 0003d4 000076 00 A	[5] .rodata PROGBITS	00000000 0003d4 000076 00 A 0 0 4			
[6] .modinfo PROGBITS	00000000 00044c 0000a4 00 A	[6] .modinfo PROGBITS	00000000 00044c 0000c6 00 A 0 0 4			
[7] .rodata.str1.4 PROGBITS	00000000 0004f0 000249 01 AMS	[7] .rodata.str1.4 PROGBITS	00000000 000514 00022f 01 AMS 0 0 4			
[8]param PROGBITS	00000000 00073c 000014 00 A	[8] <u>param</u> PROGBITS	00000000 000744 000014 00 A 0 0 4			
[9] .note.Linux PROGBITS	00000000 000750 000018 00 A	[9] .note.Linux PROGBITS	00000000 000758 000018 00 A 0 0 4			
[10] .data PROGBITS	00000000 000768 000000 00 WA	[10] .data PROGBITS	00000000 000770 000000 00 WA 0 0 1			
[11] .gnu.linkonce.thi PROGBITS	00000000 000780 000200 00 WA	<pre>[11] .gnu.linkonce.thi PROGBITS</pre>	00000000 000780 000200 00 WA 0 0 64			
[12] .bss NOBITS	00000000 000980 000040 00 WA	[12] .bss NOBITS	00000000 000980 000040 00 WA 0 0 8			
[13] .comment PROGBITS	00000000 000980 000024 01 MS	[13] .comment PROGBITS	00000000 000980 000024 01 MS 0 0 1			
[14] .note.GNU-stack PROGBITS	00000000 0009a4 000000 00	<pre>[14] .note.GNU-stack PROGBITS</pre>	00000000 0009a4 000000 00 0 0 1			
<pre>[15] .ARM.attributes ARM_ATTRI</pre>	BUTES 00000000 0009a4 00002f 00	<pre>[15] .ARM.attributes ARM_ATTRIBU</pre>				
[16] .shstrtab STRTAB	00000000 0009d3 0000bc 00	[16] .shstrtab STRTAB	00000000 0009d3 0000bc 00			
Key to Flags:		Key to Flags:				
W (write), A (alloc), X (execute), M (merge), S (strings) W (write), A (alloc), X (execute), M (merge), S (strings)						
I (info), L (link order), G (gro	up), T (TLS), E (exclude), x (unknown)), T (TLS), E (exclude), x (unknown)			
O (extra OS processing required) o (OS specific), p (processor specifi O (extra OS processing required) o (OS specific), p (processor specific)						



Dahdi from ASL on HamVoIP Nate's port is working

- Holy shit, it's the same damn code.
- The only differences are it was renamed and authors removed
- John David's M.O.
- Lol, I spent more time on this video
- Lets look at the assembled code
 Just to be sure there's nothing hidden in
 his kernel module
- objdump -d dahdi_hrtimer.ko-stripped

I've spent literally hundreds of hours fixing timing issues in AllStar; Too much to even begin to try and describe in an e-mail. The dahdi_hrtimer code is one part of all the fixes. The HRtimer driver was cobbled together by me.

```
dahdi_hrtimer.ko-stripped:
                               file format elf32-littlearm
                                                                                     dahdi_dummy.ko-stripped:
                                                                                                                   file format elf32-littlearm
                                                                                     Disassembly of section .text:
Disassembly of section .text:
000000000 <.text>:
                                                                                     000000000 <.text>:
        e92d40f0
                                                                                             e92d40f0
  0:
                        push
                                 {r4, r5, r6, r7, lr}
                                                                                                                      {r4, r5, r6, r7, lr}
                                                                                                             push
  4:
        e3004000
                                 r4, #0
                                                                                             e3004000
                                                                                                                      r4, #0
                        movw
                                                                                                             movw
        e3404000
                                 r4, #0
                                                                                             e3404000
                                                                                                                      r4, #0
   8:
                        movt
                                                                                                             movt
        e1a06000
  c:
                        mov
                                 r6, r0
                                                                                        c:
                                                                                             e1a06000
                                                                                                             mov
                                                                                                                      r6, r0
        e5940000
                                 r0, [r4]
                                                                                             e5940000
  10:
                        ldr
                                                                                       10:
                                                                                                             ldr
                                                                                                                      r0, [r4]
  14:
        e24dd00c
                                 sp, sp, #12
                                                                                       14:
                                                                                             e24dd00c
                                                                                                                      sp, sp, #12
                        sub
                                                                                                             sub
  18:
        e2800004
                                 r0, r0, #4
                                                                                      18:
                                                                                             e2800004
                                                                                                                      r0, r0, #4
                        add
                                                                                                             add
        e10f5000
 1c:
                        mrs
                                 r5, CPSR
                                                                                       1c:
                                                                                             e10f5000
                                                                                                             mrs
                                                                                                                      r5, CPSR
                                                                                                                                                   30
  20:
        f10c0080
                        cpsid
                                                                                       20:
                                                                                             f10c0080
                                                                                                             cpsid
  24:
        ebfffffe
                        bl
                                 0x24
                                                                                       24:
                                                                                             ebfffffe
                                                                                                             bl
                                                                                                                      0x24
  28:
        e3c55040
                        bic
                                 r5, r5, #64
                                                                                       28:
                                                                                             e3c55040
                                                                                                             bic
                                                                                                                      r5, r5, #64
                                                 ; 0x40
                                                                                                                                       ; 0x40
  2c:
        e10f3000
                                 r3, CPSR
                                                                                       2c:
                                                                                             e10f3000
                                                                                                                      r3, CPSR
                        mrs
                                                                                                             mrs
  30:
        e2033040
                                 r3, r3, #64
                                                                                       30:
                                                                                             e2033040
                                                                                                                      r3, r3, #64
                        and
                                                 : 0x40
                                                                                                             and
                                                                                                                                      ; 0x40
  34:
        e1835005
                        orr
                                 r5, r3, r5
                                                                                       34:
                                                                                             e1835005
                                                                                                             orr
                                                                                                                      r5, r3, r5
        e121f005
  38:
                                CPSR_c, r5
                                                                                       38:
                                                                                             e121f005
                                                                                                                      CPSR_c, r5
                        msr
                                                                                                             msr
        e5940000
                                 r0, [r4]
                                                                                             e5940000
                                                                                                                      r0, [r4]
  3c:
                        ldr
                                                                                       3c:
                                                                                                             ldr
        e2800004
                        add
                                                                                             e2800004
                                                                                                                      r0, r0, #4
 40:
                                 r0, r0, #4
                                                                                       40:
                                                                                                             add
 44:
        e10f5000
                                 r5, CPSR
                                                                                       44:
                                                                                             e10f5000
                                                                                                                      r5, CPSR
                        mrs
                                                                                                             mrs
 48:
        f10c0080
                                                                                             f10c0080
                        cpsid
                                                                                       48:
                                                                                                             cpsid
                                                                                                                      i
        ebfffffe
                        bl
                                                                                             ebfffffe
                                                                                                             bl
 4c:
                                 0x4c
                                                                                       4c:
                                                                                                                      0x4c
  50:
        e3c55040
                        bic
                                 r5, r5, #64
                                                                                       50:
                                                                                             e3c55040
                                                                                                             bic
                                                 ; 0x40
                                                                                                                      r5, r5, #64
                                                                                                                                      ; 0x40
 54:
        e10f3000
                                 r3, CPSR
                                                                                       54:
                                                                                             e10f3000
                                                                                                                      r3, CPSR
                        mrs
                                                                                                             mrs
 58:
        e2033040
                                                                                       58:
                                                                                             e2033040
                        and
                                 r3, r3, #64
                                                 ; 0x40
                                                                                                             and
                                                                                                                      r3, r3, #64
                                                                                                                                       ; 0x40
 5c:
        e1835005
                        orr
                                 r5, r3, r5
                                                                                       5c:
                                                                                             e1835005
                                                                                                             orr
                                                                                                                      r5, r3, r5
 60:
        e121f005
                        msr
                                 CPSR_c, r5
                                                                                       60:
                                                                                             e121f005
                                                                                                             msr
                                                                                                                      CPSR_c, r5
 64:
        e1c621d0
                        ldrd
                                 r2, [r6, #16]
                                                                                       64:
                                                                                             e1c621d0
                                                                                                             ldrd
                                                                                                                      r2, [r6, #16]
  68:
        e3046240
                                 r6, #16960
                                                 ; 0x4240
                                                                                       68:
                                                                                             e3046240
                                                                                                                      r6, #16960
                                                                                                                                       ; 0x4240
                        movw
                                                                                                             movw
        e340600f
                                 r6, #15
                                                                                             e340600f
                                                                                                                      r6, #15
  6c:
                        movt
                                                                                       6c:
                                                                                                             movt
```

68:	e3046240	movw	r6, #16960 ; 0x4240	68:	e3046240	movw	r6, #16960 ; 0x4240
6c:	e340600f	movt	r6, #15	6c:	e340600f	movt	r6, #15
70:	e3a07000	mov	r7, #0	70:	e3a07000	mov	r7, #0
74:	e2840008	add	r0, r4, #8	74:	e2840008	add	r0, r4, #8
78:	e1cd60f0	strd	r6, [sp]	78:	e1cd60f0	strd	r6, [sp]
7c:	ebfffffe	bl	0x7c	7c:	ebfffffe	bl	0x7c
80:	e3500001	cmp	r0, #1	80:	e3500001	cmp	r0, #1
84:	9a000005	bls	0xa0	84:	9a000005	bls	0xa0
88:	e1a05000	mov	r5, r0	88:	e1a05000	mov	r5, r0
8c:	e3000000	movw	r0, #0	8c:	e3000000	movw	r0, #0
90:	e3400000	movt	r0, #0	90:	e3400000	movt	r0, #0
94:	ebfffffe	bl	0x94	94:	ebfffffe	bl	0x94
98:	e3500000	cmp	r0, #0	98:	e3500000	cmp	r0, #0
9c:	1a000017	bne	0x100	9c:	1a000013	bne	0xf0
a0:	e5943038	ldr	r3, [r4, #56] ; 0x38	a0:	e5942038	ldr	r2, [r4, #56] ; 0x38
a4:	e3002000	movw	r2, #0	a4:	e3003000	movw	r3, #0
a8:	e3402000	movt	r2, #0	a8:	e3403000	movt	r3, #0
ac:	e3530001	cmp	r3, #1	ac:	e3520000	cmp	r2, #0
b0:	da00000b	ble	0xe4	b0:	0a00000b	beq	0xe4
b4:	e592303c	ldr	r3, [r2, #60] ; 0x3c	b4:	e593103c	ldr	r1, [r3, #60] ; 0x3c
b8:	e3080bad	movw	r0, #35757 ; 0x8bad	b8:	e3082bad	movw	r2, #35757 ; 0x8bad
bc:	e34608db	movt	r0, #26843 ; 0x68db	bc:	e34628db	movt	r2, #26843 ; 0x68db
c0:	e301c388	movw	ip, #5000 ; 0x1388	c0:	e3010388	movw	r0, #5000 ; 0x1388
c4:	e2831001	add	r1, r3, #1	c4:	e281c001	add	ip, r1, #1
c8:	e582103c	str	r1, [r2, #60] ; 0x3c	c8:	e583c03c	str	ip, [r3, #60] ; 0x3c
cc:	e0c10093	smull	r0, r1, r3, r0	cc:	e0c32291	smull	r2, r3, r1, r2
d0:	e1a02fc3	asr	r2, r3, #31	d0:	e1a02fc1	asr	r2, r1, #31
d4:	e06225c1	rsb	r2, r2, r1, asr #11	d4:	e06225c3	rsb	r2, r2, r3, asr #11
d8:	e063329c	mls	r3, ip, r2, r3	d8:	e0631290	mls	r3, r0, r2, r1
dc:	e3530000	cmp	r3, #0	dc:	e3530000	cmp	r3, #0
e0:	0a000002	beq	0xf0	e0:	0a000007	beq	0x104
e4:	e3a00001	mov	r0, #1	e4:	e3a00001	mov	r0, #1
e8:	e28dd00c	add	sp, sp, #12	e8:	e28dd00c	add	sp, sp, #12
ec:	e8bd80f0	рор	{r4, r5, r6, r7, pc}	ec:	e8bd80f0	pop	{r4, r5, r6, r7, pc}
f0:	e3000000	movw	r0, #0	f0:	e3000000	movw	r0, #0

2e0:	ebfffffe	bl	0x2e0	2e0:	ebfffffe	bl	0x2e0
2e4:	e3000000	movw	r0, #0	2e4:	e3000000	movw	r0, #0
2e8:	e3400000	movt	r0, #0	Ze8:	e3400000	movt	r0, #0
2ec:	ebfffffe	bl	0x2ec	Zec:	ebfffffe	bl	0x2ec
2f0:	e5954038	ldr	r4, [r5, #56] ; 0x38	2f0:	e5954038	ldr	r4, [r5, #56] ; 0x38
2f4:	e3540000	cmp	r4, #0	2f4:	e3540000	cmp	r4, #0
2f8:	0affffd6	beq	0x258	2f8:	0affffd6	beq	0x258
2fc:	e3000000	movw	r0, #0	2fc:	e3000000	movw	r0, #0
300:	e1a04007	mov	r4, r7	300:	e1a04007	mov	r4, r7
304:	e3400000	movt	r0, #0	304:	e3400000	movt	r0, #0
308:	ebfffffe	bl	0x308	308:	ebfffffe	bl	0x308
30c:	eaffffd1	b	0x258	30c:	eaffffd1	b	0x258
310:	00000014	andeq	r0, r0, r4, lsl r0	310:	00000014	andeq	r0, r0, r4, lsl r0
314:	e92d4010	push	{r4, lr}	314:	e92d4010	push	{r4, lr}
318:	e3004000	movw	r4, #0	318:	e3004000	movw	r4, #0
31c:	e3404000	movt	r4, #0	31c:	e3404000	movt	r4, #0
320:	e2840008	add	r0, r4, #8	320:	e2840008	add	r0, r4, #8
324:	ebfffffe	bl	0x324	324:	ebfffffe	bl	0x324
328:	e5943000	ldr	r3, [r4]	328:	e5943000	ldr	r3, [r4]
32c:	e5930000	ldr	r0, [r3]	32c:	e5930000	ldr	r0, [r3]
330:	ebfffffe	bl	0x330	330:	ebfffffe	bl	0x330
334:	e5943000	ldr	r3, [r4]	334:	e5943000	ldr	r3, [r4]
338:	e5930000	ldr	r0, [r3]	338:	e5930000	ldr	r0, [r3]
33c:	ebfffffe	bl	0x33c	33c:	ebfffffe	bl	0x33c
340:	e5940000	ldr	r0, [r4]	340:	e5940000	ldr	r0, [r4]
344:	ebfffffe	bl	0x344	344:	ebfffffe	bl	0x344
348:	e5943038	ldr	r3, [r4, #56] ; 0x38	348:	e5943038	ldr	r3, [r4, #56] ; 0x38
34c:	e3530000	cmp	r3, #0	34c:	e3530000	cmp	r3, #0
350:	08bd8010	popeq	{r4, pc}	350:	08bd8010	popeq	{r4, pc}
354:	e3000000	movw	r0, #0	354:	e3000000	movw	r0, #0
358:	e3400000	movt	r0, #0	358:	e3400000	movt	r0, #0
35c:	e8bd4010	pop	{r4, lr}	35c:	e8bd4010	pop	{r4, lr}
360:	eafffffe	b	0x360	360:	eafffffe	b	0x360

- It's 100% the same code at the ASM level
- But does it do the same thing?

```
[[root@booty ~]# lsmod

Module Size Used by
dahdi_hrtimer 16384 0
brcmfmac 225280 0
brcmutil 16384 1 brcmfmac
```

- It's 100% the same code at the ASM level
- But does it do the same thing?
- Yes

```
Asterisk 1.4.23-pre.hamvoip-V1.5.4-03-app_rpt-0.327-10/22/2019, Copyright (C) 1999 - 2019 HamVoIP.org and others.
Created by Mark Spencer <markster@digium.com>
Asterisk comes with ABSOLUTELY NO WARRANTY; type 'core show warranty' for details.
This is free software, with components licensed under the GNU General Public
License version 2 and other licenses; you are welcome to redistribute it under
certain conditions. Type 'core show license' for details.
Connected to Asterisk 1.4.23-pre.hamvoip-V1.5.4-03-app_rpt-0.327-10/22/2019 currently running on booty (pid = 323)
Verbosity is at least 3
booty*CLI> dahdi show
cadences channel channels status
booty*CLI> dahdi show status
Description
                                         Alarms
                                                    IR0
                                                               bpviol
                                                                          CRC4
DAHDI HRTIMER/1 (source: HRtimer) 1
                                         UNCONFIGUR 0
```

- It's 100% the same code at the ASM level
- But does it do the same thing?
- Yes

```
[root@booty ~]# rmmod dahdi_hrtimer
[root@booty ~]# asterisk -r
Asterisk 1.4.23-pre.hamvoip-V1.5.4-03-app_rpt-0.327-10/22/2019, Copyright (C) 1999 - 2019 HamVoIP.org and others.
Created by Mark Spencer <markster@digium.com>
Asterisk comes with ABSOLUTELY NO WARRANTY; type 'core show warranty' for details.
                                                                                             Removed
This is free software, with components licensed under the GNU General Public
License version 2 and other licenses; you are welcome to redistribute it under
                                                                                       dahdi_hrtimer.ko
certain conditions. Type 'core show license' for details.
Connected to Asterisk 1.4.23-pre.hamvoip-V1.5.4-03-app_rpt-0.327-10/22/2019 currently running on booty (pid = 323)
Verbosity is at least 3
booty*CLI> dahdi show status
Description
                                        Alarms
                                                              bpviol
                                                   IR0
                                                                         CRC4
booty*CLI> quit
```

- It's 100% the same code at the ASM level
- But does it do the same thing?
- Yes

booty*CLI> quit

[root@bootv ~]# asterisk -r

[root@booty ~]# dahdi_test -c 20

99.993% 99.991% 99.992% 99.990%

```
Asterisk 1.4.23-pre.hamvoip-V1.5.4-03-app_rpt-0.327-10/22/2019, Copyright (C) 1999 - 2019 HamVoIP.org and others.
Created by Mark Spencer <markster@digium.com>
Asterisk comes with ABSOLUTELY NO WARRANTY; type 'core show warranty' for details.
This is free software, with components licensed under the GNU General Public
License version 2 and other licenses; you are welcome to redistribute it under
certain conditions. Type 'core show license' for details.
Connected to Asterisk 1.4.23-pre.hamvoip-V1.5.4-03-app_rpt-0.327-10/22/2019 currently running on booty (pid = 323)
Verbosity is at least 3
booty*CLI> dahdi show status
Description
                                         Alarms
                                                    IR0
                                                               bpviol
                                                                          CRC4
                                                                                            Installed
DAHDI_DUMMY/1 (source: HRtimer) 1
                                         UNCONFIGUR 0
```

dahdi_dummy.ko

--- Results after 20 passes ---Best: 99.993% -- Worst: 99.981% -- Average: 99.990274% Cummulative Accuracy (not per pass): 99.990

99.983% 99.981% 99.992% 99.992% 99.990% 99.991% 99.993% 99.992% 99.993% 99.986% 99.990% 99.993% 99.992% 99.993% 99.989% 99.990%

Opened pseudo dahdi interface, measuring accuracy...

Summary

- John David McGough, KB4FXC is a thief.
- There is no way to spin this other than he is passing off othe
- This isn't an isolated incident.
 - Internet Technologies, Inc. (
- I reached out to the listed author, and he had no idea about



Summary

- John David McGough, KB4FXC is a thief.
- There is no way to spin this other than he is passing off others work as his own
- This isn't an isolated incident.
 - Internet Technologies, Inc. customers beware!
- I reached out to the listed author, and he had no idea about this, no permission was granted for it.
- Asterisk on Arch was created by others
- Wait! How do I enable this on my ASL1.01 RPi node?
 - https://wiki.w9cr.net

Questions?