

> ASTRO ${ }^{\circledR}$ Digital Spectra ${ }^{\circledR}$ and Digital Spectra Plus
> UHF/VHF/800 MHz Mobile Radios Detailed Service Manual

## Foreword

This manual provides sufficient information to enable qualified service technicians to troubleshoot and repair ASTRO ${ }^{\circledR}$ Digital Spectra ${ }^{\circledR}$ and ASTRO Digital Spectra Plus mobile radios (models W3, W4, W5, W7, and W9) to the component level. For the most part, the information in this manual pertains to both ASTRO Digital Spectra and ASTRO Digital Spectra Plus radios. Exceptions are clearly noted where they occur.

For details on radio operation or basic troubleshooting, refer to the applicable manuals available separately. A list of related publications is provided in the section, "Related Publications," on page xiv.

## Product Safety and RF Exposure Compliance



Before using this product, read the operating instructions for safe usage contained in the Product Safety and RF Caution Exposure booklet enclosed with your radio.

## ATTENTION!

This radio is restricted to occupational use only to satisfy FCC RF energy exposure requirements.
Before using this product, read the RF energy awareness information and operating instructions in the Product Safety and RF Exposure booklet enclosed with your radio (Motorola Publication part number 68P81095C99) to ensure compliance with RF energy exposure limits.

## Manual Revisions

Changes which occur after this manual is printed are described in FMRs (Florida Manual Revisions). These FMRs provide complete replacement pages for all added, changed, and deleted items, including pertinent parts list data, schematics, and component layout diagrams.

## Computer Software Copyrights

The Motorola products described in this manual may include copyrighted Motorola computer programs stored in semiconductor memories or other media. Laws in the United States and other countries preserve for Motorola certain exclusive rights for copyrighted computer programs, including, but not limited to, the exclusive right to copy or reproduce in any form the copyrighted computer program. Accordingly, any copyrighted Motorola computer programs contained in the Motorola products described in this manual may not be copied, reproduced, modified, reverse-engineered, or distributed in any manner without the express written permission of Motorola. Furthermore, the purchase of Motorola products shall not be deemed to grant either directly or by implication, estoppel, or otherwise, any license under the copyrights, patents or patent applications of Motorola, except for the normal non-exclusive license to use that arises by operation of law in the sale of a product.

## Document Copyrights

No duplication or distribution of this document or any portion thereof shall take place without the express written permission of Motorola. No part of this manual may be reproduced, distributed, or transmitted in any form or by any means, electronic or mechanical, for any purpose without the express written permission of Motorola.

## Disclaimer

The information in this document is carefully examined, and is believed to be entirely reliable. However, no responsibility is assumed for inaccuracies. Furthermore, Motorola reserves the right to make changes to any products herein to improve readability, function, or design. Motorola does not assume any liability arising out of the applications or use of any product or circuit described herein; nor does it cover any license under its patent rights nor the rights of others.

## Trademarks

MOTOROLA, the Stylized M logo, ASTRO, and Spectra are registered in the US Patent \& Trademark Office. All other products or service names are the property of their respective owners.
© Motorola, Inc. 2002.

## Table of Contents

Foreword ..... ii
Product Safety and RF Exposure Compliance .....  ii
Manual Revisions .....  ii
Computer Software Copyrights ..... ii
Document Copyrights .....  ii
Disclaimer .....  ii
Trademarks ..... ii
Commercial Warranty ..... xv
Limited Warranty ..... XV
MOTOROLA COMMUNICATION PRODUCTS ..... XV
I. What This Warranty Covers And For How Long ..... xv
II. General Provisions ..... xv
III. State Law Rights ..... $x v i$
IV. How To Get Warranty Service ..... xvi
V. What This Warranty Does Not Cover. ..... xvi
VI. Patent And Software Provisions ..... xvii
VII. Governing Law ..... xvii
Model Numbering, Charts, and Specifications ..... xix
Mobile Radio Model Numbering Scheme ..... xix
ASTRO Digital Spectra Motorcycle 15 Watt (Ranges 1 and 2) Model Chart ..... XX
ASTRO Digital Spectra Motorcycle 15 Watt (Ranges 3 and 3.5) Model Chart ..... xxi
ASTRO Digital Spectra VHF 10-25 Watt Model Chart ..... xxii
ASTRO Digital Spectra VHF 10-25 and 50-110 Watt Model Chart ..... xxiii
ASTRO Digital Spectra UHF 10-25 Watt Model Chart ..... xxv
ASTRO Digital Spectra UHF 20-40 Watt Model Chart ..... xxvi
ASTRO Digital Spectra UHF 50-110 Watt Model Chart ..... xxviii
ASTRO Digital Spectra 800 MHz Model Chart ..... xxx
ASTRO Digital Spectra Plus VHF 25-50 and 50-110 Watt Model Chart. ..... xxxi
ASTRO Digital Spectra Plus 800 MHz Model Chart ..... xxxiii
VHF Radio Specifications. ..... xxxv
UHF Radio Specifications ..... xxxvi
800 MHz Radio Specifications ..... xxxvii
Chapter 1 Introduction ..... 1-1
1.1 General ..... 1-1
1.2 Notations Used in This Manual ..... 1-2
Chapter 2 General Overview ..... 2-1
2.1 Introduction ..... 2-1
2.2 Analog Mode of Operation ..... 2-2
2.3 ASTRO Mode of Operation ..... 2-2
2.4 Control Head Assembly ..... 2-2
2.4.1 Display (W3 Model) ..... 2-2
2.4.2 Display (W4, W5, and W7 Models) ..... 2-2
2.4.3 Display (W9 Model). ..... 2-3
2.4.4 Vacuum Fluorescent Display Driver ..... 2-3
2.4.5 Vacuum Fluorescent Voltage Source (W9 Model) ..... 2-3
2.4.6 Controls and Indicators ..... 2-3
2.4.7 Status LEDs ..... 2-3
2.4.8 Backlight LEDs ..... 2-3
2.4.9 Vehicle Interface Ports ..... 2-4
2.4.10 Power Supplies ..... 2-4
2.4.11 Ignition Sense Circuits ..... 2-4
2.5 Power Amplifier ..... 2-5
2.5.1 Gain Stages ..... 2-5
2.5.2 Power Control ..... 2-5
2.5.3 Circuit Protection ..... 2-5
2.5.4 DC Interconnect ..... 2-5
2.6 Front-End Receiver Assembly ..... 2-6
2.7 RF Board Basic ..... 2-6
2.8 Voltage-Controlled Oscillator ..... 2-6
2.8.1 VHF Radios ..... 2-6
2.8.2 UHF and 800 MHz Radios ..... 2-7
2.9 Command Board ..... 2-7
2.10 ASTRO Spectra Vocoder/Controller Board ..... 2-7
2.11 Radio Power ..... 2-8
2.11.1 General ..... 2-8
2.11.2 B+ Routing for ASTRO Spectra VOCON Board ..... 2-9
Chapter 3 Theory of Operation ..... 3-1
3.1 RF Board ..... 3-1
3.1.1 General ..... 3-1
3.1.2 Synthesizer ..... 3-3
3.1.2.1 Reference Frequency Generation. ..... 3-3
3.1.2.2 First VCO Frequency Generation ..... 3-3
3.1.2.3 Programmable Reference Divider. ..... 3-4
3.1.2.4 Phase Modulator ..... 3-5
3.1.2.5 Loop Filter ..... 3-5
3.1.2.6 Auxiliary Control Bits ..... 3-5
3.1.2.7 Second VCO ..... 3-6
3.1.2.8 Power Distribution ..... 3-6
3.1.3 Receiver Back-End ..... 3-6
3.1.3.1 First IF ..... 3-6
3.1.3.2 ABACUS II IC ..... 3-7
3.2 Command Board ..... 3-8
3.2.1 Microcontroller and Support ICs ..... 3-8
3.2.2 Serial Input/Output IC ..... 3-8
3.2.3 Power-Up/-Down Sequence ..... 3-9
3.2.4 Regulators ..... 3-10
3.2.5 Reset Circuits ..... 3-10
3.2.6 Serial Communications on the External Bus ..... 3-11
3.2.7 Synchronous Serial Bus (MOSI) ..... 3-12
3.2.8 Received Audio ..... 3-12
3.2.9 Microphone Audio ..... 3-12
3.2.10 Transmit Deviation ..... 3-13
3.2.11 RS-232 Line Driver ..... 3-13
3.2.12 Flash Programming ..... 3-13
3.2.13 Encryption Voltages ..... 3-13
3.2.14 Regulator and Power-Control IC ..... 3-14
3.3 ASTRO Spectra VOCON Board ..... 3-15
3.3.1 General ..... 3-15
3.3.2 Controller Section ..... 3-15
3.3.3 Vocoder Section ..... 3-17
3.3.4 RX Signal Path ..... 3-18
3.3.5 TX Signal Path ..... 3-21
3.3.6 Controller Bootstrap and Asynchronous Buses ..... 3-22
3.3.7 Vocoder Bootstrap ..... 3-24
3.3.8 Serial Peripheral Interface (SPI) Bus ..... 3-24
3.3.9 Controller Memory Map ..... 3-24
3.3.10 Vocoder Memory Map ..... 3-26
3.3.11 MCU System Clock ..... 3-28
3.3.12 DSP System Clock ..... 3-28
3.3.13 Radio Power-Up/Power-Down Sequence ..... 3-28
3.3.14 VOCON BOARD Signals ..... 3-29
3.4 ASTRO Spectra Plus VOCON Board ..... 3-38
3.4.1 General ..... 3-38
3.4.2 ASTRO Spectra Plus Controller Section ..... 3-38
3.4.3 ASTRO Spectra Plus Vocoder Section. ..... 3-39
3.4.4 ASTRO Spectra Plus RX Signal Path. ..... 3-41
3.4.5 ASTRO Spectra Plus TX Signal Path ..... 3-42
3.4.6 ASTRO Spectra Plus Controller Bootstrap and Asynchronous Busses ..... 3-43
3.4.7 ASTRO Spectra Plus Serial Peripheral Interface Bus ..... 3-44
3.4.8 ASTRO Spectra Plus MCU and DSP System Clocks ..... 3-44
3.4.9 ASTRO Spectra Plus Voltage Regulators ..... 3-45
3.4.10 ASTRO Spectra Plus Radio Power-Up/Power-Down Sequence ..... 3-46
3.5 Voltage Control Oscillator ..... 3-47
3.5.1 VHF Band ..... 3-47
3.5.1.1 General ..... 3-47
3.5.1.2 DC Voltage Supplies ..... 3-47
3.5.1.3 VCO ..... 3-47
3.5.1.4 Synthesizer Feedback ..... 3-48
3.5.1.5 RX Buffer Circuitry ..... 3-48
3.5.1.6 Frequency Divider and TX Buffer Circuitry ..... 3-48
3.5.2 UHF Band ..... 3-48
3.5.2.1 General ..... 3-48
3.5.2.2 Super Filter 8.6 V ..... 3-49
3.5.2.3 VCO ..... 3-49
3.5.2.4 Receive Mode (AUX2* Low) ..... 3-49
3.5.2.5 Transmit Mode (AUX2* High) ..... 3-49
3.5.2.6 Bandshift Circuit. ..... 3-49
3.5.2.7 Output Buffer ..... 3-49
3.5.2.8 First Buffer ..... 3-49
3.5.2.9 Doubler ..... 3-50
3.5.2.10 Synthesizer Feedback ..... 3-50
3.5.2.11 Second Buffer ..... 3-50
3.5.2.12 Receive/Transmit Switch ..... 3-50
3.5.3 800 MHz Band ..... 3-50
3.5.3.1 General ..... 3-50
3.5.3.2 Super Filter 8.6 V ..... 3-50
3.5.3.3 VCO ..... 3-50
3.5.3.4 Receive Mode-AUX 1* and AUX 2* High ..... 3-51
3.5.3.5 Transmit Mode-AUX 1* High; AUX 2* Low ..... 3-51
3.5.3.6 TalkAround Mode-AUX 1* Low; AUX 2* Low ..... 3-51
3.5.3.7 VCO Buffer ..... 3-51
3.5.3.8 First Buffer Circuit ..... 3-51
3.5.3.9 Doubler ..... 3-51
3.5.3.10 Second Buffer ..... 3-52
3.5.3.11 K9.4 V Switch ..... 3-52
3.6 Receiver Front-End ..... 3-53
3.6.1 VHF Band ..... 3-53
3.6.1.1 General ..... 3-53
3.6.1.2 Theory of Operation ..... 3-53
3.6.2 UHF Band ..... 3-53
3.6.2.1 General ..... 3-53
3.6.2.2 Theory of Operation ..... 3-54
3.6.3 800 MHz Band ..... 3-54
3.6.3.1 General ..... 3-54
3.6.3.2 Theory of Operation ..... 3-54
3.7 Power Amplifiers ..... 3-55
3.7.1 VHF Band Power Amplifiers ..... 3-55
3.7.1.1 High-Power Amplifier ..... 3-55
3.7.1.1.1 Transmitter ..... 3-55
3.7.1.1.2 Antenna Switch and Harmonic Filter ..... 3-56
3.7.1.1.3 Power Control Circuitry ..... 3-57
3.7.1.2 25/10-Watt Power Amplifier ..... 3-59
3.7.1.2.1 Antenna Switch and Harmonic Filter ..... 3-60
3.7.1.2.2 Power Control Circuitry ..... 3-60
3.7.1.3 50-Watt Power Amplifiers ..... 3-63
3.7.1.3.1 Transmitter ..... 3-63
3.7.1.3.2 Antenna Switch and Harmonic Filter ..... 3-64
3.7.1.3.3 Power Control Circuitry ..... 3-65
3.7.2 UHF Band Power Amplifiers ..... 3-68
3.7.2.1 High-Power Amplifier ..... 3-68
3.7.2.1.1 Transmitter ..... 3-68
3.7.2.1.2 Antenna Switch and Harmonic Filter ..... 3-69
3.7.2.1.3 Power Control Circuitry ..... 3-69
3.7.2.2 40-Watt Power Amplifier ..... 3-72
3.7.2.2.1 Transmitter ..... 3-72
3.7.2.2.2 Antenna Switch and Harmonic Filter ..... 3-73
3.7.2.2.3 Power Control Circuitry ..... 3-74
3.7.3 $\quad 800 \mathrm{MHz}$ Band Power Amplifiers ..... 3-77
3.7.3.1 15- and $35-$ Watt Amplifiers ..... 3-77
3.7.3.1.1 Transmitter ..... 3-77
3.7.3.1.2 Antenna Switch and Harmonic Filter ..... 3-78
3.7.3.1.3 Power Control Circuitry ..... 3-79
3.7.3.1.4 Temperature Sensing ..... 3-81
Table of Contents ..... vii
Chapter 4 Troubleshooting Procedures ..... 4-1
4.1 ASTRO Spectra Procedures ..... 4-1
4.1.1 Handling Precautions ..... 4-1
4.1.2 Voltage Measurement and Signal Tracing ..... 4-2
4.1.3 Power-Up Self-Check Errors ..... 4-2
4.1.3.1 Power-Up Sequence ..... 4-3
4.1.4 RF Board Troubleshooting ..... 4-5
4.1.4.1 Display Flashes "FAIL 001" ..... 4-5
4.1.4.1.1 Incorrect Values at U602, Pin 19 ..... 4-6
4.1.4.1.2 Incorrect Values at U602 Pin 25 (MODULUS CONTROL) ..... 4-7
4.1.4.1.3 Incorrect Voltage at Positive Steering Line ..... 4-7
4.1.4.1.4 Incorrect Values at U602, pin 27 ..... 4-7
4.1.4.2 Review of Synthesizer Fundamentals ..... 4-7
4.1.4.3 Second VCO Checks ..... 4-8
4.1.4.4 Troubleshooting the Back-End ..... 4-8
4.1.5 Standard Bias Table ..... 4-9
4.2 ASTRO Spectra Plus Procedures ..... 4-10
4.2.1 ASTRO Spectra Plus Power-Up Self-Check Errors ..... 4-10
4.2.2 ASTRO Spectra Plus Power-Up Self-Check Diagnostics and Repair ..... 4-11
4.2.3 ASTRO Spectra Plus Standard Bias Table ..... 4-12
4.3 VCO Procedures ..... 4-13
4.3.1 VHF Band ..... 4-13
4.3.1.1 VCO Hybrid Assembly ..... 4-13
4.3.1.2 Out-of-Lock Condition ..... 4-13
4.3.1.3 No or Low Output Power (TX or RX Injection) ..... 4-15
4.3.1.4 No or Low Modulation ..... 4-15
4.3.2 UHF Band ..... 4-15
4.3.2.1 VCO Hybrid Assembly ..... 4-15
4.3.2.2 Out-of-Lock Condition ..... 4-16
4.3.2.3 No or Low Output Power (TX or RX Injection) ..... 4-16
4.3.2.4 No or Low Modulation ..... 4-17
4.3.3 $\quad 800 \mathrm{MHz}$ Band ..... 4-18
4.3.3.1 VCO Hybrid Assembly ..... 4-18
4.3.3.2 Out-of-Lock Condition ..... 4-18
4.3.3.3 No or Low Output Power (TX or RX Injection) ..... 4-19
4.3.3.4 No or Low Modulation ..... 4-19
4.4 Receiver Front-End (RXFE) ..... 4-20
4.4.1 VHF Band ..... 4-20
4.4.2 UHF Band ..... 4-20
4.4.3 800 MHz Band ..... 4-20
4.5 Power Amplifier Procedures ..... 4-21
4.5.1 VHF Band ..... 4-21
4.5.1.1 High-Power Amplifier ..... 4-21
4.5.1.1.1 General Troubleshooting and Repair Notes ..... 4-21
4.5.1.1.2 PA Functional Testing ..... 4-25
4.5.1.1.3 Power Control and Protection Circuitry ..... 4-28
4.5.1.2 25/10 Watt Power Amplifier ..... 4-29
4.5.1.2.1 General Troubleshooting and Repair Notes ..... 4-29
4.5.1.2.2 PA Functional Testing ..... 4-30
4.5.1.2.3 Localizing Problems ..... 4-34
4.5.1.2.4 Isolating Failures ..... 4-35
4.5.1.2.5 Power Control and Protection Circuitry ..... 4-37
4.5.1.3 50 Watt Power Amplifiers ..... 4-38
4.5.1.3.1 General Troubleshooting and Repair Notes ..... 4-38
4.5.1.3.2 PA Functional Testing ..... 4-39
4.5.1.3.3 Localizing Problems ..... 4-42
4.5.1.3.4 Isolating Failures ..... 4-43
4.5.1.3.5 Power Control and Protection Circuitry ..... 4-45
4.5.2 UHF Band ..... 4-47
4.5.2.1 High-Power Amplifier ..... 4-47
4.5.2.1.1 General Troubleshooting and Repair Notes ..... 4-47
4.5.2.1.2 PA Functional Testing ..... 4-51
4.5.2.1.3 Power Control and Protection Circuitry ..... 4-54
4.5.2.2 40 Watt Power Amplifiers ..... 4-56
4.5.2.2.1 General Troubleshooting and Repair Notes ..... 4-56
4.5.2.2.2 PA Functional Testing ..... 4-57
4.5.2.2.3 Localizing Problems ..... 4-61
4.5.2.2.4 Isolating Failures ..... 4-62
4.5.2.2.5 Power Control and Protection Circuitry ..... 4-64
4.5.3 800 MHz Band ..... 4-66
4.5.3.1 15 Watt and 35 Watt Power Amplifiers ..... 4-66
4.5.3.1.1 General Troubleshooting and Repair Notes ..... 4-66
4.5.3.1.2 PA Functional Testing. ..... 4-67
4.5.3.1.3 Localizing Problems ..... 4-71
4.5.3.1.4 Isolating Failures ..... 4-72
4.5.3.1.5 Power Control and Protection Circuitry ..... 4-74
Chapter 5 Troubleshooting Charts ..... 5-1
5.1 Introduction ..... 5-1
5.2 List of Troubleshooting Charts ..... 5-1
RF Board Back-End ..... 5-3
Command Board ..... 5-4
Radio Power-Up Fail ..... 5-5
Bootstrap Fail ..... 5-6
01/90, General Hardware Failure ..... 5-7
01/81, Host ROM Checksum Failure ..... 5-7
01/82 or 002, External EEPROM Checksum Failure ..... 5-8
01/84, SLIC Initialization Failure ..... 5-8
01/88, MCU (Host mC) External SRAM Failure ..... 5-9
01/92, Internal EEPROM Checksum Failure ..... 5-9
02/A0, ADSIC Checksum Failure ..... 5-10
02/81, DSP ROM Checksum Failure ..... 5-10
02/88, DSP External SRAM Failure U414 ..... 5-11
02/84, DSP External SRAM Failure U403 ..... 5-11
02/82, DSP External SRAM Failure U402 ..... 5-12
02/90, General DSP Hardware Failure ..... 5-12
09/10, Secure Hardware Failure. ..... 5-13
09/90, Secure Hardware Failure. ..... 5-13
No RX Audio ..... 5-14
No TX Modulation ..... 5-15
Key Load Fail. ..... 5-16
800 MHz Receiver Front-End Hybrid ..... 5-17
UHF Receiver Front-End Hybrid. ..... 5-17
VHF Receiver Front-End Hybrid ..... 5-18
ASTRO Spectra Plus VOCON Power-Up Failure ..... 5-19
ASTRO Spectra Plus VOCON DC Supply Failure ..... 5-20
ASTRO Spectra Plus VOCON TX Modulation Failure Sheet 1 of 4 ..... 5-21
ASTRO Spectra Plus VOCON TX Modulation Failure Sheet 2 of 4 ..... 5-22
ASTRO Spectra Plus VOCON TX Modulation Failure Sheet 3 of 4 ..... 5-23
ASTRO Spectra Plus VOCON TX Modulation Failure Sheet 4 of 4 ..... 5-24
ASTRO Spectra Plus VOCON RX Audio Failure ..... 5-24
ASTRO Spectra Plus VOCON Secure Hardware Failure ..... 5-25
ASTRO Spectra Plus VOCON Key Load Fail ..... 5-26
Chapter 6 Troubleshooting Waveforms ..... 6-1
6.1 Introduction ..... 6-1
6.2 ASTRO Spectra Waveforms ..... 6-1
Waveform W1: Power-On Reset Timing ..... 6-1
Waveform W2: DSP SSI Port RX Mode ..... 6-2
Waveform W3: DSP SSI Port TX Mode CSQ ..... 6-2
Waveform W4: ABACUS Programming at Mode Change ..... 6-3
Waveform W5: ABACUS/ADSIC Interface ..... 6-3
Waveform W6: SPI Bus Programming ADSIC ..... 6-4
Waveform W7: Receive Audio ..... 6-4
Waveform W8: Transmit Audio ..... 6-5
Waveform W9: Power-Down Reset ..... 6-5
Waveform W10: ADSIC 2.4 MHz Reference ..... 6-6
6.3 ASTRO Spectra Digital Plus VOCON Board Waveforms ..... 6-7
32 kHz Clock Waveform ..... 6-7
16.8 MHz Clock Waveform ..... 6-8
TX Modulation Out Waveform ..... 6-8
Differential ADDAG Output Waveform. ..... 6-9
TX SSI Waveform ..... 6-9
SPI Bus Waveform ..... 6-10
TX 1 kHz Tone Waveform ..... 6-10
Serial Audio Port Waveform ..... 6-11
RX Audio Waveform ..... 6-11
RX BBP Waveform ..... 6-12
Secure Interface Waveform ..... 6-12
8 kHz Frame Sync for Security Circuitry Waveform ..... 6-13
Chapter 7 Schematics, Component Location Diagrams, and
Parts Lists ..... 7-1
7.1 RF Section ..... 7-2
ASTRO Spectra Radio Interconnection ..... 7-4
HRN4009B/HRN6014A VHF RF Board; HRN4010B/HRN6020A UHF RF Board; and HRN6019A 800 MHz RF Board Schematic ..... 7-5
HRN4009B/HRN6014A VHF RF Board, HRN4010B/HRN6020A UHF RF Board, and HRN6019A 800 MHz RF Board Component Location Diagrams ..... 7-6
HRN4009C/HRN6014C VHF RF Board Schematic Diagram ..... 7-9
HRN4009C/HRN6014C VHF RF Board Component Location Diagrams ..... 7-10
HRN4009E and HRN6014D VHF RF Board; HRN4010D and HRN6020C UHF RF Board; and HRN6019C 800 MHz RF Board Schematic Diagram (Sheet 1 of 2) ..... 7-12
HRN4009E and HRN6014D VHF RF Board; HRN4010D and HRN6020C UHF RF Board; and HRN6019C 800 MHz RF Board Schematic Diagram (Sheet 2 of 2) ..... 7-13
HRN4009E and HRN6014D VHF RF Board; HRN4010D and HRN6020C UHF RF Board; and HRN6019C 800 MHz RF Component Location Diagram ..... 7-14
7.2 Command Board Section ..... 7-17
HLN5558E/F/G, HLN6529C/D/E/F/G, HLN6560C/D/E/F/G/H and HLN6562C/D/E/F/G/H Command Board Schematic Diagram ..... 7-17
HLN5558E/F/G, HLN6529C/D/E, HLN6560C/D/E/F/G/H, and HLN6562C/D/E/F/G/H CommandBoard Component Location Diagrams7-18
HLN5558H/J, HLN6529H, HLN6560J and HLN6562J Command Board Schematic Diagram ..... 7-21
HLN5558H/J, HLN6529H, HLN6560J and HLN6562J Component Location Diagram ..... 7-22
7.3 VOCON Section. ..... 7-26
HLN6458D VOCON Board Schematic (Sheet 1 of 2) ..... 7-26
HLN6458D VOCON Board Schematic (Sheet 2 of 2) ..... 7-27
HLN6458D VOCON Board Component Location Diagrams (Sheet 1 of 2). ..... 7-28
HLN6458D VOCON Board Component Location Diagrams (Sheet 2 of 2). ..... 7-29
HLN6458E VOCON Board Schematic (Sheet 1 of 2). ..... 7-32
HLN6458E VOCON Board Schematic (Sheet 2 of 2) ..... 7-33
HLN6458E VOCON Board Component Location Diagrams (Sheet 1 of 2) ..... 7-34
HLN6458E VOCON Board Component Location Diagrams (Sheet 2 of 2) ..... 7-35
HLN6458F/G VOCON Board Schematic (Sheet 1 of 2) ..... 7-38
HLN6458F/G VOCON Board Schematic (Sheet 2 of 2) ..... 7-39
HLN6458F/G VOCON Board Component Location Diagrams (Sheet 1 of 2) ..... 7-40
HLN6458F/G VOCON Board Component Location Diagrams (Sheet 2 of 2) ..... 7-41
HLN6458H VOCON Board Schematic (Sheet 1 of 2) ..... 7-44
HLN6458H VOCON Board Schematic (Sheet 2 of 2) ..... 7-45
HLN6458H VOCON Board Component Location Diagrams ..... 7-46
7.4 ASTRO Spectra Plus VOCON Section ..... 7-49
ASTRO Spectra Plus Top Level Schematic (Sheet 1 of 2) ..... 7-49
ASTRO Spectra Plus Top Level Schematic (Sheet 2 of 2) ..... 7-50
ASTRO Spectra Plus RF Interface Schematic (Sheet 1 of 2) ..... 7-51
ASTRO Spectra Plus RF Interface Schematic (Sheet 2 of 2). ..... 7-52
ASTRO Spectra Plus Digital/USB Schematic (Sheet 1 of 2) ..... 7-53
ASTRO Spectra Plus Digital/USB Schematic (Sheet 2 of 2) ..... 7-54
ASTRO Spectra Plus Audio/DC Schematic. ..... 7-55
ASTRO Spectra Plus Voltage Conversion Schematic ..... 7-56
ASTRO Spectra Plus Secure Interface Schematic. ..... 7-57
ASTRO Spectra Plus VOCON Component Location Diagram, Top View ..... 7-58
ASTRO Spectra Plus VOCON Component Location Diagram, Bottom View ..... 7-59
7.5 VCO Section ..... 7-62
HLD6061D and HLD6062D VHF VCO Hybrid Schematic ..... 7-62
HLD6061D and HLD6062D VHF VCO Hybrid Component Location Diagram ..... 7-63
HLD4342B and HLD4343B VHF VCO Carrier Schematic Diagram ..... 7-64
HLD4342D and HLD4343D VHF VCO Carrier Schematic Diagram. ..... 7-65
HLD4342B/HLD4343B VHF VCO Carrier Component Location Diagram. ..... 7-66
HLD4342D/HLD4343D VHF VCO Carrier Component Location Diagram ..... 7-67
UHF VCO Ranges 1, 2, 3, and 4 Hybrid Schematic. ..... 7-70
HLE6101A UHF VCO Range 1 Hybrid and HLE6102A Range 2 Hybrid Component Location Diagram ..... 7-71
HLE6103B UHF VCO Range 3 Hybrid and HLE6104B Range 4 Hybrid Component Location Diagram ..... 7-73
UHF VCO Ranges 1, 2, 3, and 4 Schematic Diagram ..... 7-75
HLE6045B Range 1 and HLE6046B Range 2 UHF VCO Component Location Diagram ..... 7-76
HLE6000D Range 3 and HLE6041D Range 4 UHF VCO Component Location Diagrams ..... 7-77
HLF6080B 800 MHz VCO Schematic Diagram ..... 7-79
HLF6080B 800 MHz VCO Component Location Diagram ..... 7-80
7.6 RX Front-End Section ..... 7-82
HRD6001E/6002E/6011E/6012E VHF Receiver Front-End Schematic ..... 7-82
HRD6001E/6002E/6011E/6012E VHF Component Location Diagram ..... 7-83
HRD6001G/6002G/6011G/6012G VHF Receiver Front-End Schematic ..... 7-87
HRD6001G/6002G/6011G/6012G VHF Receiver Front-End Component Location Diagram 7-88 HRE6001B/6002C/6003B/6004B/6011B/6012B/6014B UHF Receiver Front-End Preamp and Standard Schematics ..... 7-90
HRE6001B/6002C/6003B/6004B/6011B/6012B/6014B UHF Receiver Front-End Hybrid Component Location Diagram ..... 7-91
HRF6004B/C 800 MHz Receiver Front-End Schematic Diagram ..... 7-94
HRF6004B/C 800 MHz Receiver Front-End Component Location Diagram ..... 7-95
7.7 Power Amplifier Section ..... 7-97
HLD6022C VHF 50 Watt PA Schematic ..... 7-97
HLD6022C VHF 50-Watt PA Component Location Diagram, Side 1 ..... 7-98
HLD6022C VHF 50-Watt PA Component Location Diagram, Side 2 ..... 7-99
HLD6064C VHF 100-Watt PA Schematic ..... 7-101
HLD6064C VHF 100-Watt PA Component Location Diagram, Side 1 ..... 7-102
HLD6064C VHF 100-Watt PA Component Location Diagram, Side 2 ..... 7-103
HLD6032B/HLD6066B VHF 25-Watt PA Schematic ..... 7-105
HLD6032B/HLD6066B VHF 25-Watt PA Component Location Diagram, Side 1 ..... 7-106
HLD6032B/HLD6066B VHF 25-Watt PA Component Location Diagram, Side 2 ..... 7-107
HLE6062B and HLE6071B UHF 25-Watt PA Schematic ..... 7-110
HLE6062B UHF 25-Watt PA Component Location Diagram, Side 1 ..... 7-111
HLE6062B UHF 25-Watt PA Component Location Diagram, Side 2 ..... 7-112
HLE6043C, HLE6044C, and HLE6049B UHF 40-Watt PA Schematic ..... 7-114
HLE6043C, HLE6044C, and HLE6049B UHF 40-Watt PA Component Location Diagram, Side 1 ..... 7-115
HLE6043C, HLE6044C, and HLE6049B UHF 40-Watt PA Component Location Diagram,
Side 2 ..... 7-116
HLE6039C, HLE6040C, and HLE6051C UHF 100-Watt PA Schematic ..... 7-120
HLE6039C, HLE6040C, and HLE6051C UHF 100-Watt PA Component Location Diagram,
Side 1 ..... 7-121
HLE6039C, HLE6040C, and HLE6051C UHF 100-Watt PA Component Location Diagram, Side 2 ..... 7-122
HLF6078B 800 MHz 15-Watt PA Schematic. ..... 7-127
HLF6078B 800 MHz 15-Watt PA Component Location Diagram, Side 1 ..... 7-128
HLF6078B 800 MHz 15-Watt PA Component Location Diagram, Side 2 ..... 7-129
HLF6077D 800 MHz 35-Watt PA Schematic ..... 7-131
HLF6077D 800 MHz 35-Watt PA Component Location Diagram, Side 1 ..... 7-132
HLF6077D 800 MHz 35-Watt PA Component Location Diagram, Side 2 ..... 7-133
Appendix A Secure Modules ..... A-1
A. 1 Introduction ..... A-1
A. 2 Circuit Description ..... A-2
A. 3 Troubleshooting Secure Operations ..... A-2
A.3.1 Error 09/10, Error 09/90 ..... A-2
A.3.2 Keyload ..... A-2
Appendix B Replacement Parts Ordering ..... B-1
B. 1 Basic Ordering Information ..... B-1
B. 2 Transceiver Board and VOCON Board Ordering Information ..... B-1
B. 3 Motorola Online ..... B-1
B. 4 Mail Orders ..... B-1
B. 5 Telephone Orders ..... B-2
B. 6 Fax Orders ..... B-2
B. 7 Parts Identification ..... B-2
B. 8 Product Customer Service ..... B-2
GlossaryGlossary-1

## List of Figures

Figure 2-1. DC Voltage Routing Block Diagram ..... 2-9
Figure 2-2. ASTRO Spectra B+ Routing for Vocoder/Controller (VOCON) Board ..... 2-10
Figure 3-1. Prescaler IC Block Diagram ..... 3-2
Figure 3-2. Synthesizer IC Block Diagram ..... 3-2
Figure 3-3. Loop Divider Waveforms ..... 3-4
Figure 3-4. Loop Filter Schematic ..... 3-5
Figure 3-5. Power-on Reset ..... 3-11
Figure 3-6. Transmitter Attack Time ..... 3-14
Figure 3-7. VOCON Board - Controller Section ..... 3-16
Figure 3-8. VOCON Board - Vocoder Section ..... 3-18
Figure 3-9. DSP RSSI Port - RX Mode ..... 3-19
Figure 3-10. DSP RSSI Port - TX Mode ..... 3-21
Figure 3-11. Host SB9600 and RS232 Ports ..... 3-23
Figure 3-12. Controller Memory Mapping ..... 3-25
Figure 3-13. Vocoder Memory Mapping ..... 3-27
Figure 3-14. ASTRO Spectra Plus VOCON Board - Controller Section ..... 3-39
Figure 3-15. ASTRO Spectra Plus VOCON Board - Vocoder Section ..... 3-40
Figure 3-16. ASTRO Spectra Plus RX Mode ..... 3-41
Figure 3-17. ASTRO Spectra Plus TX Mode ..... 3-42
Figure 3-18. ASTRO Spectra Plus Host SB9600 and RS232 Ports ..... 3-44
Figure 3-19. ASTRO Spectra Plus VOCON DC Distribution ..... 3-45
Figure 3-20. RPCIC Block Diagram ..... 3-57
Figure 3-21. Regulator/Power Control IC Block Diagram ..... 3-61
Figure 3-22. 50-Watt Power Amplifier Block Diagram ..... 3-63
Figure 3-23. Regulator/Power Control IC Block Diagram ..... 3-65
Figure 3-24. UHF High-Power, Power Amplifier Block Diagram ..... 3-68
Figure 3-25. RPCIC Block Diagram ..... 3-70
Figure 3-26. RPCIC Block Diagram ..... 3-74
Figure 3-27. RPCIC Block Diagram ..... 3-79
Figure 4-1. VCO Block Diagram - VHF Band ..... 4-14
Figure 4-2. VCO Block Diagram - UHF Band ..... 4-17
Figure 4-3. VCO Block Diagram - 800 MHz Band ..... 4-18
Figure 4-4. Connector Pin-Out - High-Power Amplifier ..... 4-22
Figure 4-5. PA Test Adapter, 25/10 Watt Power Amplifier ..... 4-31
Figure 4-6. PA Test Adapter, 50 Watt Power Amplifier ..... 4-40
Figure 4-7. Connector Pin-Out - High-Power Amplifier ..... 4-48
Figure 4-8. Block Diagram for Spectra High-Power Power Amplifier ..... 4-56
Figure 4-9. PA Test Adapter, 40 Watt Power Amplifier ..... 4-58
Figure 4-10. PA Test Adapter, 15 and 35 Watt Power Amplifier ..... 4-67

Go to Chapter 7 on page 7-1 for a listing of schematics and component location diagrams.

## List of Tables

Table 3-1. Integrated Circuits Voltages ..... 3-10
Table 3-2. VOCON Board Address Bus (A) Pinouts ..... 3-29
Table 3-3. VOCON Board Address Bus (HA) Pinouts ..... 3-30
Table 3-4. VOCON Board Data Bus (D) Pinouts ..... 3-30
Table 3-5. VOCON Board Data Bus (HD) Pinouts ..... 3-31
Table 3-6. U204 (MCU) ..... 3-32
Table 3-7. U206 (SLIC) ..... 3-33
Table 3-8. VOCON U405 (DSP) ..... 3-35
Table 3-9. VOCON U406 (ADSIC) ..... 3-36
Table 4-1. Power-Up Self-Check Error Codes ..... 4-2
Table 4-2. Voltage by Location ..... 4-5
Table 4-3. Feedback Frequency Ranges ..... 4-7
Table 4-4. Standard Operating Bias ..... 4-9
Table 4-5. ASTRO Spectra Plus Power-Up Self-Check Error Codes ..... 4-10
Table 4-6. ASTRO Spectra Plus Standard Operating Bias ..... 4-12
Table 4-7. VCO Frequency ..... 4-15
Table 4-8. Power Control DC Voltage Chart ..... 4-23
Table 4-9. LLA and 2nd Stage Typical Voltages ..... 4-26
Table 4-10. DC Voltages and Input Power Chart ..... 4-30
Table 4-11. Power Control DC Voltage Chart ..... 4-31
Table 4-12. Antenna Switch DC Voltage Chart ..... 4-34
Table 4-13. LLA and Driver Typical Voltages ..... 4-35
Table 4-14. DC Voltages and Input Power Chart ..... 4-39
Table 4-15. Power Control DC Voltage Chart ..... 4-40
Table 4-16. LLA and Pre-Driver Typical Voltages ..... 4-43
Table 4-17. Power Control DC Voltage Chart ..... 4-49
Table 4-18. LLA and 2nd Stage Typical Voltages ..... 4-52
Table 4-19. DC Voltages and Input Power Chart ..... 4-58
Table 4-20. Power Control DC Voltage Chart ..... 4-59
Table 4-21. Antenna Switch DC Voltage Chart ..... 4-61
Table 4-22. LLA and Pre-Driver Typical Voltages ..... 4-62
Table 4-23. DC Voltages and Input Power Chart ..... 4-68
Table 4-24. Power Control DC Voltage Chart ..... 4-68
Table 4-25. Antenna Switch DC Voltage Chart ..... 4-71
Table 5-1. List of Troubleshooting Charts ..... 5-1
Table A-1. ASTRO Digital Spectra Secure Modules ..... A-1
Table A-2. ASTRO Digital Spectra Plus Secure Modules. ..... A-1

## Related Publications

ASTRO Digital Spectra and Digital Spectra Plus Model W3 User's Guide ..... 68P81090C61
ASTRO Digital Spectra and Digital Spectra Plus Models W4, W5, W7, and W9 User's Guide ..... 68P81090C62
ASTRO Digital Spectra Hand-Held Control Head User's Guide (Model W3) ..... 68P81073C25
ASTRO Digital Spectra (Model W4, W5, W7, and W9) User's Guide ..... 68P81074C80
ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual ..... 68P81076C20
ASTRO Digital Spectra Mobile Radios Dual Control Head Radio System Service Manual ..... 68P81091C78
ASTRO Spectra and Digital Spectra FM Two-Way Mobile Radios Installation Manual ..... 68P81070C85
ASTRO Spectra Motorcycle Radios Supplemental Installation Manual ..... 68P80103W01
KVL 3000 User's Manual ..... 68P81131E16
Spectra VHF VCO Section Detailed Service Manual Supplement ..... 68P81074C48
Spectra High-Power Power Amplifier Detailed Service Manual Supplement ..... 68P81077C25
Spectra Systems 9000 Control Unit Detailed Service Manual Supplement ..... 68P81077C30
Spectra A5 and A7 Control Head Instruction Manual. ..... 68P81109C33
Spectra A4 Control Head Instruction Manual ..... 68P81109C34

## Commercial Warranty

## Limited Warranty <br> MOTOROLA COMMUNICATION PRODUCTS

## I. What This Warranty Covers And For How Long

MOTOROLA INC. ("MOTOROLA") warrants the MOTOROLA manufactured Communication Products listed below ("Product") against defects in material and workmanship under normal use and service for a period of time from the date of purchase as scheduled below:

| ASTRO Digital Spectra and Digital Spectra <br> Plus Units | One (1) Year |
| :--- | :--- |
| Product Accessories | One (1) Year |

Motorola, at its option, will at no charge either repair the Product (with new or reconditioned parts), replace it (with a new or reconditioned Product), or refund the purchase price of the Product during the warranty period provided it is returned in accordance with the terms of this warranty. Replaced parts or boards are warranted for the balance of the original applicable warranty period. All replaced parts of Product shall become the property of MOTOROLA.

This express limited warranty is extended by MOTOROLA to the original end user purchaser only and is not assignable or transferable to any other party. This is the complete warranty for the Product manufactured by MOTOROLA. MOTOROLA assumes no obligations or liability for additions or modifications to this warranty unless made in writing and signed by an officer of MOTOROLA. Unless made in a separate agreement between MOTOROLA and the original end user purchaser, MOTOROLA does not warrant the installation, maintenance or service of the Product.

MOTOROLA cannot be responsible in any way for any ancillary equipment not furnished by MOTOROLA which is attached to or used in connection with the Product, or for operation of the Product with any ancillary equipment, and all such equipment is expressly excluded from this warranty. Because each system which may use the Product is unique, MOTOROLA disclaims liability for range, coverage, or operation of the system as a whole under this warranty.

## II. General Provisions

This warranty sets forth the full extent of MOTOROLA'S responsibilities regarding the Product. Repair, replacement or refund of the purchase price, at MOTOROLA's option, is the exclusive remedy. THIS WARRANTY IS GIVEN IN LIEU OF ALL OTHER EXPRESS WARRANTIES. IMPLIED WARRANTIES, INCLUDING WITHOUT LIMITATION, IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE, ARE LIMITED TO THE DURATION OF THIS LIMITED WARRANTY. IN NO EVENT SHALL MOTOROLA BE LIABLE FOR DAMAGES IN EXCESS OF THE PURCHASE PRICE OF THE PRODUCT, FOR ANY LOSS OF USE, LOSS OF TIME, INCONVENIENCE, COMMERCIAL LOSS, LOST PROFITS OR SAVINGS OR OTHER INCIDENTAL, SPECIAL OR CONSEQUENTIAL DAMAGES ARISING OUT OF THE USE OR INABILITY TO USE SUCH PRODUCT, TO THE FULL EXTENT SUCH MAY BE DISCLAIMED BY LAW.

## III. State Law Rights

## SOME STATES DO NOT ALLOW THE EXCLUSION OR LIMITATION OF INCIDENTAL OR CONSEQUENTIAL DAMAGES OR LIMITATION ON HOW LONG AN IMPLIED WARRANTY LASTS, SO THE ABOVE LIMITATION OR EXCLUSIONS MAY NOT APPLY.

This warranty gives specific legal rights, and there may be other rights which may vary from state to state.

## IV. How To Get Warranty Service

You must provide proof of purchase (bearing the date of purchase and Product item serial number) in order to receive warranty service and, also, deliver or send the Product item, transportation and insurance prepaid, to an authorized warranty service location. Warranty service will be provided by Motorola through one of its authorized warranty service locations. If you first contact the company which sold you the Product, it can facilitate your obtaining warranty service. You can also call Motorola at 1-888-567-7347 US/Canada.

## V. What This Warranty Does Not Cover

A. Defects or damage resulting from use of the Product in other than its normal and customary manner.
B. Defects or damage from misuse, accident, water, or neglect.
C. Defects or damage from improper testing, operation, maintenance, installation, alteration, modification, or adjustment.
D. Breakage or damage to antennas unless caused directly by defects in material workmanship.
E. A Product subjected to unauthorized Product modifications, disassemblies or repairs (including, without limitation, the addition to the Product of non-Motorola supplied equipment) which adversely affect performance of the Product or interfere with Motorola's normal warranty inspection and testing of the Product to verify any warranty claim.
F. Product which has had the serial number removed or made illegible.
G. Rechargeable batteries if:
H. any of the seals on the battery enclosure of cells are broken or show evidence of tampering.
I. the damage or defect is caused by charging or using the battery in equipment or service other than the Product for which it is specified.
J. Freight costs to the repair depot.
K. A Product which, due to illegal or unauthorized alteration of the software/firmware in the Product, does not function in accordance with MOTOROLA's published specifications or the FCC type acceptance labeling in effect for the Product at the time the Product was initially distributed from MOTOROLA.
L. Scratches or other cosmetic damage to Product surfaces that does not affect the operation of the Product.
M. Normal and customary wear and tear.

## VI. Patent And Software Provisions

MOTOROLA will defend, at its own expense, any suit brought against the end user purchaser to the extent that it is based on a claim that the Product or parts infringe a United States patent, and MOTOROLA will pay those costs and damages finally awarded against the end user purchaser in any such suit which are attributable to any such claim, but such defense and payments are conditioned on the following:
A. that MOTOROLA will be notified promptly in writing by such purchaser of any notice of such claim;
B. that MOTOROLA will have sole control of the defense of such suit and all negotiations for its settlement or compromise; and
C. should the Product or parts become, or in MOTOROLA's opinion be likely to become, the subject of a claim of infringement of a United States patent, that such purchaser will permit MOTOROLA, at its option and expense, either to procure for such purchaser the right to continue using the Product or parts or to replace or modify the same so that it becomes noninfringing or to grant such purchaser a credit for the Product or parts as depreciated and accept its return. The depreciation will be an equal amount per year over the lifetime of the Product or parts as established by MOTOROLA.

MOTOROLA will have no liability with respect to any claim of patent infringement which is based upon the combination of the Product or parts furnished hereunder with software, apparatus or devices not furnished by MOTOROLA, nor will MOTOROLA have any liability for the use of ancillary equipment or software not furnished by MOTOROLA which is attached to or used in connection with the Product. The foregoing states the entire liability of MOTOROLA with respect to infringement of patents by the Product or any parts thereof.

Laws in the United States and other countries preserve for MOTOROLA certain exclusive rights for copyrighted MOTOROLA software such as the exclusive rights to reproduce in copies and distribute copies of such Motorola software. MOTOROLA software may be used in only the Product in which the software was originally embodied and such software in such Product may not be replaced, copied, distributed, modified in any way, or used to produce any derivative thereof. No other use including, without limitation, alteration, modification, reproduction, distribution, or reverse engineering of such MOTOROLA software or exercise of rights in such MOTOROLA software is permitted. No license is granted by implication, estoppel or otherwise under MOTOROLA patent rights or copyrights.

## VII. Governing Law

This Warranty is governed by the laws of the State of Illinois, USA.

# This Page Intentionally Left Blank 

# Model Numbering, Charts, and Specifications 

## Mobile Radio Model Numbering Scheme



## ASTRO Digital Spectra Motorcycle 15 Watt (Ranges 1 and 2) Model Chart

| Model Number |  |  |  |  |  |  |  |  |  |  |  |  | Description |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| M04JGF9PW4AN |  |  |  |  |  |  |  |  |  |  |  |  | Model W4 (136-162 MHz), Range 1, 15 Watt, 128 Channels |
| M04JGF9PW5AN |  |  |  |  |  |  |  |  |  |  |  |  | Model W5 (136-162 MHz), Range 1, 15 Watt, 128 Channels |
| M04JGH9PW7AN |  |  |  |  |  |  |  |  |  |  |  |  | Model W7 (136-162 MHz), Range 1, 15 Watt, 128 Channels |
|  |  |  | M04KGF9PW4AN |  |  |  |  |  |  |  |  |  | Model W4 (146-174 MHz), Range 2, 15 Watt, 128 Channels |
|  |  |  |  | M04KGF9PW5AN |  |  |  |  |  |  |  |  | Model W5 (146-174 MHz), Range 2, 15 Watt, 128 Channels |
|  |  |  |  | M04KGH9PW7AN |  |  |  |  |  |  |  |  | Model W7 (146-174 MHz), Range 2, 15 Watt, 128 Channels |
|  |  |  |  |  |  | M04RGF9PW4AN |  |  |  |  |  |  | Model W4 (438-470 MHz), Range 2, 15 Watt, 128 Channels |
|  |  |  |  |  |  |  | M04RGF9PW5AN |  |  |  |  |  | Model W5 (438-470 MHz), Range 2, 15 Watt, 128 Channels |
|  |  |  |  |  |  |  |  | M04RGH9PW7AN |  |  |  |  | Model W7 (438-470 MHz), Range 2, 15 Watt, 128 Channels |
|  |  |  |  |  |  |  |  |  | M04UGF9PW4AN |  |  |  | Model W4 (800 MHz), 15 Watt, 128 Channels |
|  |  |  |  |  |  |  |  |  |  | M04UGF9PW5AN |  |  | Model W5 ( 800 MHz ), 15 Watt, 128 Channels |
|  |  |  |  |  |  |  |  |  |  |  | M04UGH9PW7AN |  | Model W7 ( 800 MHz ), 15 Watt, 128 Channels |
|  |  |  |  |  |  |  |  |  |  |  |  | Item ${ }^{\text {No. }}$ | Description |
| X | X | X |  |  |  |  |  |  |  |  |  | HLD6066_ | VHF Power Amplifier Board, 25-Watt |
| X | X | X | X | X | X | X | X | X | X | X | X | HKN6062_ | Cable, Control Head to Radio |
| X | X | X |  |  |  |  |  |  |  |  |  | HLD4342_ | VHF VCO Carrier |
|  |  |  | X | X | X |  |  |  |  |  |  | HLD4343 | VHF VCO Carrier, CEPT |
|  |  |  | X | X | X |  |  |  |  |  |  | HLD6032_ | VHF Power Amplifier Board, Range 2, 25-Watt |
| X | X | X |  |  |  |  |  |  |  |  |  | HLD6061_ | VHF VCO, Range 1, 136-162 MHz |
|  |  |  | X | X | X |  |  |  |  |  |  | HLD6062_ | VHF VCO Board, Range 2, 146-174 MHz |
|  |  |  |  |  |  | X | X | X |  |  |  | HLE6046_ | UHF VCO Carrier, Range 2 |
|  |  |  |  |  |  | X | X | X |  |  |  | HLE6062 | UHF RF Power Amplifier Board, Range 2, 25-Watt |
|  |  |  |  |  |  | X | X | X |  |  |  | HLE6102 | UHF VCO Board, Range 2 |
|  |  |  |  |  |  |  |  |  | X | X | X | HLF6078 | 800 MHz RF Power Amplifier Board, 15-Watt |
|  |  |  |  |  |  |  |  |  | X | X | X | HLF6079 | 800 MHz VCO Board |
|  |  |  |  |  |  |  |  |  | X | X | X | HLF6080_ | 800 MHz VCO Carrier Board |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN1368 | White Motorcycle Enclosure and Hardware |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN6127_* | Low-Power Dash Hardware |
| X | X | X | X | X | X |  | X | X |  | X | X | HLN6193 | MPL Button Kit |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN6342_* | Motorcycle Hardware |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN6365 | Interface Board Kit |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN6418_* | Transceiver Hardware |
|  | X | X |  | X |  |  | X |  |  | X |  | HLN6444_* | W5 Motorcycle Control Head Hardware |
|  |  |  |  |  | X |  |  | X |  |  | X | HLN6445_* | W7 Motorcycle Control Head Hardware |
|  | X |  |  |  |  |  |  |  |  |  |  | HLN6454_ | Motorcycle Control Head Board Kit |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN6458_ | Vocoder Controller |
| X |  |  | X |  |  |  |  |  |  |  |  | HLN6459 | Interface Board |
|  |  | X |  |  | X |  |  | X |  |  | X | HLN6523_* | W7 Button Kit |
|  | X |  |  | X |  |  | X |  |  | X |  | HLN6548_* | W5 Button Kit |
|  |  |  |  |  |  | X |  |  | X |  |  | HLN6549_* | W4 Button Kit |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN6562_ | Motorcycle Command Board Kit |
|  | X | X |  | X | X |  | X | X |  | X | X | HLN6563 | Motorcycle Control Head |
| X |  |  | X |  |  | X |  |  | X |  |  | HLN6571_ | Spare Button Kit |
| X | X | X | X | X | X | X | X | X | X | X | X | HMN1079_ | Weatherproof Microphone |
| X | X | X |  |  |  |  |  |  |  |  |  | HRD6001_ | VHF Receiver Board, Range 1, Standard |
|  |  |  | X | X | X |  |  |  |  |  |  | HRD6002_ | VHF Receiver Board, Range 2, Standard |
|  |  |  |  |  |  | X | X | X |  |  |  | HRE6002_ | UHF Receiver Board, Range 2, Standard |
|  |  |  |  |  |  |  |  |  | X | X | X | HRF6004_ | $800 \mathrm{MHz} \mathrm{FX} \mathrm{Front-End}$ |
|  |  |  | X | X | X |  |  |  |  |  |  | HRN4009_ | VHF RF Board |
|  |  |  |  |  |  | X | X | X |  |  |  | HRN4010_ | UHF RF Board |
| X | X | X |  |  |  |  |  |  |  |  |  | HRN6014_ | VHF RF Board, ASTRO |
|  |  |  |  |  |  |  |  |  | X | X | X | HRN6019_ | $800 \mathrm{MHz} \mathrm{RF} \mathrm{Board}$, |
| X | X | X | X | X | X | X | X | X | X | X | X | HSN6003 | Weatherproof Speaker |
| X |  |  | X |  |  | X |  |  | X |  |  | PMLN4019 | W4 Motorcycle Control Head |
|  |  |  |  |  |  |  |  | X |  |  |  | RAE4024_ | UHF Antenna, Quarterwave |
|  |  |  |  |  |  |  |  |  | X | X | X | RAF4011_ | 800 MHz Antenna, 3 dB Gain |
|  | Item | Incl | vers | ion <br> e. O | kit. rder | hen piece | $\begin{aligned} & \text { orde } \\ & \text { par } \end{aligned}$ | ring ts fro | $\begin{aligned} & \text { a kit, } \\ & \text { om th } \end{aligned}$ | refe $e A c$ |  | your specific kit for sories and Afterma | suffix number. Division. |

## ASTRO Digital Spectra Motorcycle 15 Watt (Ranges 3 and 3.5) Model Chart

| Model Number |  |  |  |  |  | Description |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| M04RGF9PW4ANSP02 |  |  |  |  |  | Model W4 (450-482 MHz), Range 3, 15 Watt, 128 Channels |
| M04RGF9PW5ANSP02 |  |  |  |  |  | Model W5 (450-482 MHz), Range 3, 15 Watt, 128 Channels |
|  |  | M04RGF9PW4ANSP01 |  |  |  | Model W4 (453-488 MHz), Range 3.5, 15 Watt, 128 Channels |
|  |  |  | M04RGF9PW5ANSP01 |  |  | Model W5 (453-488 MHz), Range 3.5, 15 Watt, 128 Channels |
|  |  |  |  | M04RGH9PW7ANSP01 |  | Model W7 ( $453-488 \mathrm{MHz}$ ), Range 3.5, 15 Watt, 128 Channels |
|  |  |  |  |  | Item No. | Description |
| X | X | X | X | X | HKN6062_ | Cable, Control Head to Radio |
| X | X |  |  |  | HLE6000 | UHF VCO Carrier, Range 3 |
|  |  | X | X | X | HLE6000DSP01 | UHF VCO Carrier, Range 3.5 |
| X | X |  |  |  | HLE6043 | UHF RF Power Amplifier Board, Range 3, 40-Watt |
|  |  | X | X | X | HLE6043CSP01 | UHF RF Power Amplifier Board, Range 3.5, 40-Watt |
| X | X |  |  |  | HLE6103 | UHF VCO Hybrid, Range 3 |
|  |  | X | X | X | HLE6103BSP01 | UHF VCO Hybrid, Range 3.5 |
| X | X | X | X | X | HLN1368 | White Motorcycle Enclosure and Hardware |
| X | X | X | X | X | HLN6127_* | Low-Power Dash Hardware |
|  | X |  | X | X | HLN6193 | MPL Button Kit |
| X | X | X | X | X | HLN6342_* | Motorcycle Hardware |
| X | X | X | X | X | HLN6365 | Interface Board Kit |
| X | X | X | X | X | HLN6418_* | Transceiver Hardware |
|  | X |  | X |  | HLN6444-* | W5 Motorcycle Control Head Hardware |
|  |  |  |  | X | HLN6445_* | W7 Motorcycle Control Head Hardware |
| X | X | X | X | X | HLN6458- | Vocoder Controller |
|  |  |  |  | X | HLN6523_* | W7 Button Kit |
|  | X |  | X |  | HLN6548_* | W5 Button Kit |
| X |  | X |  |  | HLN6549_* | W4 Button Kit |
| X | X | X | X | X | HLN6562_ | Motorcycle Command Board Kit |
|  | X |  | X | X | HLN6563_ | Motorcycle Control Head |
| X | X | X | X | X | HLN6571_ | Spare Button Kit |
| X | X | X | X | X | HMN1079_ | Weatherproof Microphone |
| X | X |  |  |  | HRE6003 | UHF Receiver Board, Range 3, Standard |
|  |  | X | X | X | HRE6003BSP01 | UHF Receiver Board, Range 3.5, Standard |
| X | X | X | X | X | HRN6020_ | UHF RF Board, ASTRO |
| X | X | X | X | X | HSN6003 | Weatherproof Speaker |
| X |  | X |  |  | PMLN4019 | W4 Motorcycle Control Head |
| X | X | X | X | X | RAE4024_ | UHF Antenna, Quarterwave |

[^0]
## ASTRO Digital Spectra VHF 10-25 Watt Model Chart



[^1]
## ASTRO Digital Spectra VHF 10-25 and 50-110 Watt Model Chart



[^2]
## ASTRO Digital Spectra VHF 10-25 and 50-110 Watt Model Chart (cont.)



[^3]
## ASTRO Digital Spectra UHF 10-25 Watt Model Chart



[^4]
## ASTRO Digital Spectra UHF 20-40 Watt Model Chart



[^5]
## ASTRO Digital Spectra UHF 20-40 Watt Model Chart (cont.)


$X=$ Item Included
= the latest version kit. When ordering a kit, refer to your specific kit for the suffix number.
${ }^{*}=$ kit not available. Order piece parts from the Accessories and Aftermarket Division.

## ASTRO Digital Spectra UHF 50-110 Watt Model Chart

| Model Number |  |  |  |  |  |  |  |  |  |  |  |  | Description <br> Model W4 (403-433 MHz), 50-110 Watt, 128 Channels |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| T04QLF9PW4AN |  |  |  |  |  |  |  |  |  |  |  |  |  |
| T04QLF9PW5AN |  |  |  |  |  |  |  |  |  |  |  |  | Model W5 (403-433 MHz), 50-110 Watt, 255 Channels |
| T04QLH9PW7AN |  |  |  |  |  |  |  |  |  |  |  |  | Model W7 (403-433 MHz), 50-110 Watt, 255 Channels |
| T04QLH9PW9AN |  |  |  |  |  |  |  |  |  |  |  |  | Model W9 (403-433 MHz), 50-110 Watt, 255 Channels |
|  |  |  | T04RLF9PW4AN |  |  |  |  |  |  |  |  |  | Model W4 (450-482 MHz), 50-110 Watt, 128 Channels |
|  |  |  |  | T04RLF9PW5AN |  |  |  |  |  |  |  |  | Model W5 (450-482 MHz), 50-110 Watt, 128 Channels |
|  |  |  |  |  | T04RLH9PW7AN |  |  |  |  |  |  |  | Model W7 (450-482 MHz), 50-110 Watt, 255 Channels |
|  |  |  |  |  |  |  | T04RLH9PW9AN |  |  |  |  |  | Model W9 (450-482 MHz), 50-110 Watt, 255 Channels |
|  |  |  |  |  |  |  | T04SLF9PW4AN |  |  |  |  |  | Model W4 (482-512 MHz), 50-110 Watt, 128 Channels |
|  |  |  |  |  |  |  |  |  | T04SLF9PW5AN |  |  |  | Model W5 (482-512 MHz), 50-110 Watt, 128 Channels |
|  |  |  |  |  |  |  |  |  | T04SLHPW7AN |  |  |  | Model W7 (482-512 MHz), 50-110 Watt, 128 Channels |
|  |  |  |  |  |  |  |  |  |  |  | T04SLHPW9AN |  | Model W9 (482-512 MHz), 50-110 Watt, 128 Channels |
|  |  |  |  |  |  |  |  |  |  |  |  | Item No. | Description |
| X |  |  |  | X |  |  |  | X |  |  |  | AAHN4045 | Front Housing |
| X | X | X | X |  |  |  |  |  |  |  |  | HAE4002_ | Antenna, Roof Top |
|  |  |  |  | X | X | X | X |  |  |  |  | HAE4003 | Antenna, Quarterwave |
|  |  |  |  |  |  |  |  | X | X | X | X | HAE4004_ | Antenna, Roof Top |
| X | X | X | X | X | X | X | X | X | X | X | X | HKN4051_ | Cable and Fuse |
| X | X | X | X | X | X | X | X | X | X | X | X | HKN4356_ | Radio Cable (Length-17 Feet) |
| X | X | X | X | X | X | X | X | X | X | X | X | HKN6039_ | Cable (Length-17 Feet) |
|  |  |  |  | X | X | X | X |  |  |  |  | HLE6039_ | VCO Carrier, Range 3 |
|  |  |  |  |  |  |  |  | X | X | X | X | HLE6040_ | Power Amplifier Board, Range 4 |
|  |  |  |  |  |  |  |  | X | X | X | X | HLE6041_ | VCO Carrier, Range 4 |
| X | X | X | X |  |  |  |  |  |  |  |  | HLE6045 | VCO Carrier, Range 1 |
| X | X | X | X |  |  |  |  |  |  |  |  | HLE6051_ | Power Amplifier Board, 100W, Range 1 |
| X | X | X | X |  |  |  |  |  |  |  |  | HLE6101_ | VCO Hybrid Kit, Range 1 |
|  |  |  |  | X | X | X | X |  |  |  |  | HLE6103 | VCO Hybrid Kit, Range 3 |
|  |  |  |  |  |  |  |  | X | X | X | X | HLE6104_ | VCO Hybrid Kit, Range 4 |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN4952_ | Fuse Kit |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN5558 | Command Board Kit |
| X |  |  |  | X |  |  |  | X |  |  |  | HLN6105 | Emergency/Secure/MPL Button Kit |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN6121_* | High-Power Radio Hardware |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN6132_* | Installation Hardware, High-Power |
| X | X | X |  | X | X | X |  | X | X | X |  | HLN6231_ | Remote W4, W5, W7 Control-Head Trunnion |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN6233_* | Option Connector Hardware |
| X |  |  |  | X |  |  |  | X |  |  |  | HLN6549_* | C4 Button Kit |
| X | X | X |  | X | X | X |  | X | X | X |  | HLN6432_ | Back Housing, Control Head |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN6458_ | VOCON Board Kit |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN6486_ | Interconnect Board |
|  |  |  |  | X | X |  |  | X | X | X |  | HLN6493_* | Plug Kit |
| X | X | X | X | X | X | X | X | X | X | X | X | HLN6525_* | High-Power Transceiver Hardware |
| X | X | X | X | X | X | X |  | X | X | X |  | HMN1080_ | Microphone |
| X | X | X | X |  |  |  | X |  |  |  | X | HMN1061_ | Microphone |
|  |  |  | X |  |  |  |  |  |  |  |  | HRE6001_ | Receiver Board Kit, Range 1 |
|  |  |  |  | X | X | X | X |  |  |  |  | HRE6003_ | Receiver Board Kit, Range 3 |
|  |  |  |  |  |  |  |  | X | X | X | X | HRE6004_ | Receiver Board Kit, Range 4 |
| X | X | X | X | X | X | X | X | X | X | X | X | HRN6020_ | RF Board |
| X | X | X | X | X | X | X | X | X |  | X | X | HSN6001_ | Speaker |
|  | X |  |  |  | X |  |  |  | X |  |  | HLN6548_* | SMARTNET Button Kit |
|  | X | X |  |  | X | X |  |  | X | X |  | HLN6193_ | Emergency/MPL Field Option Button Kit |
|  | X | X |  |  | X | X |  |  | X | X |  | HLN6396 | W5, W7 Control Head Board |

## X = Item Included

= the latest version kit. When ordering a kit, refer to your specific kit for the suffix number.
${ }^{*}=$ kit not available. Order piece parts from the Accessories and Aftermarket Division.

## ASTRO Digital Spectra UHF 50-110 Watt Model Chart (cont.)



[^6]
## ASTRO Digital Spectra 800 MHz Model Chart



## X = Item Included

= the latest version kit. When ordering a kit, refer to your specific kit for the suffix number.

* $=$ kit not available. Order piece parts from the Accessories and Aftermarket Division.

ASTRO Digital Spectra Plus VHF 25-50 and 50-110 Watt Model Chart


[^7]
## ASTRO Digital Spectra Plus VHF 25-50 and 50-110 Watt Model Chart (cont.)



[^8]
## ASTRO Digital Spectra Plus 800 MHz Model Chart



[^9]
## ASTRO Digital Spectra Plus 800 MHz Model Chart (cont.)



[^10]
## VHF Radio Specifications



Specifications subject to change without notice.
All measurements are taken in the test mode at 25 kHz channel spacing except where indicated.

## UHF Radio Specifications



Specifications subject to change without notice.
All measurements are taken in the test mode at 25 kHz channel spacing except where indicated.

* Maximum power 78 Watts above 470 MHz .


## 800 MHz Radio Specifications



Specifications subject to change without notice.
All measurements are taken in the test mode at 25 kHz channel spacing except where indicated.

# This Page Intentionally Left Blank 

## Chapter 1 Introduction

### 1.1 General

This manual includes all the information necessary to maintain peak product performance and maximum working time. This detailed level of service (component-level) is typical of some service centers, self-maintained customers, and distributors.

Use this manual in conjunction with the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (Motorola part number 68P81076C20), which helps in troubleshooting a problem to a particular board.
Conduct the basic performance checks first to verify the need to analyze the radio and help pinpoint the functional problem area. In addition, you will become familiar with the radio test mode of operation which is a helpful tool. If any basic receiver or transmitter parameters fail to be met, the radio should be aligned using the radio alignment procedure described in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual.

Included in other areas of this manual are functional block diagrams, detailed Theory of Operation, troubleshooting charts and waveforms, schematics, and parts list. You should be familiar with these sections to aid in deducing the problem circuit. Also included are component location diagrams to aid in locating individual circuit components, as well as IC diagrams, which identify some convenient probe points.

The Theory of Operation section of this manual contains detailed descriptions of operations of many circuits. Once you locate the problem area, review the Troubleshooting Chart for that circuit to fix the problem.

### 1.2 Notations Used in This Manual

Throughout the text in this publication, you will notice the use of warnings, cautions, and notes. These notations are used to emphasize that safety hazards exist, and care must be taken and observed.

NOTE: An operational procedure, practice, or condition that is essential to emphasize.


Caution


WARNING

CAUTION indicates a potentially hazardous situation which, if not avoided, may result in equipment damage.

WARNING indicates a potentially hazardous situation which, if not avoided, could result in death or injury.

DANGER indicates an imminently hazardous situation which, if not avoided, will result in death or injury.

You will also find in this publication the use of the asterisk symbol (*) to indicate a negative or NOT logic true signal.

## Chapter 2 General Overview

### 2.1 Introduction

The ASTRO Digital Spectra radio is a dual-mode (trunked/conventional), microcontroller-based transceiver incorporating a Digital Signal Processor (DSP). The microcontroller handles the general radio control, monitors status, and processes commands input from the keypad or other user controls. The DSP processes the typical analog signals and generates the standard signaling digitally to provide compatibility with existing analog systems. In addition it provides for digital modulation techniques utilizing voice encoding techniques with error correction schemes to provide the user with enhanced range and audio quality all in a reduced bandwidth channel requirement. It allows embedded signaling which can mix system information and data with digital voice to add the capability of supporting a multitude of system features.
The ASTRO Digital Spectra radio comes in five models and are available in the following bands; VHF ( $136-174 \mathrm{MHz}$ ), UHF ( $403-470 \mathrm{MHz}$ or $450-512 \mathrm{MHz}$ ), and $800 \mathrm{MHz}(806-870 \mathrm{MHz}$ ).

The ASTRO Digital Spectra radio comprises seven major assemblies, six of which are in the main radio housing. They are:

- Control-Head Assembly (Dash- or Remote-Mount) - is connected, directly or remotely, to the front of the transceiver by the interconnect board or remote interconnect board and control cable. This assembly contains a vacuum fluorescent (VF) display, VF driver, microprocessor and serial bus interface.
- Power Amplifier (PA) — contains antenna switch, directional coupler/detector, and amplifier(s).
- Front-End Receiver Assembly - contains pre-amplifier, preselector, mixer, and injection filter.
- RF Board - contains receiver I-F amplifier, demodulator, synthesizer logic and filtering circuitry, and digital receiver back-end integrated circuit (IC).
- VCO/Buffer/Divider Board - contains voltage controlled oscillator (VCO), divider, receive and transmit buffers.
- Command Board - contains power control/regulator, digital-to-analog (D/A) IC, serial bus interface, and audio power amplifier (PA).
- VOCON (Vocoder/Controller) Board - contains the microcomputer unit (MCU), its associated memory and memory management integrated circuit, and the digital signal processor (DSP) and its associated memories and support IC.
- VOCON Plus (Vocoder/Controller) Board - the architecture is based on a Dual-Core processor, which contains a DSP Core, an MCORE 210 Microcontroller Core, and custom peripherals. The board also contains memory ICs and DSP support ICs.


### 2.2 Analog Mode of Operation

When the radio is receiving, the signal comes from the antenna/antenna-switch on the power amplifier board to the front-end receiver assembly. The signal is then filtered, amplified, and mixed with the first local-oscillator signal generated by the voltage-controlled oscillator (VCO). The resulting intermediate frequency (IF) signal is fed to the IF circuitry on the RF board, where it is again filtered and amplified. This amplified signal is passed to the digital back-end IC, where it is mixed with the second local oscillator to create the second IF at 450 kHz . The analog IF is processed by an analog-to-digital (A/D) converter, where it is converted to a digital bit stream and divided down to a baseband signal, producing digital samples. These samples are converted to current signals and sent to the DSP support IC. The digital-signal-processor-support IC digitally filters and discriminates the signal, and passes it to the digital-signal processor (DSP). The DSP decodes the information in the signal and identifies the appropriate destination for it. For a voice signal, the DSP will route the digital voice data to the DSP-support IC for conversion to an analog signal. The DSP-support IC will then present the signal to the audio power amplifier on the command board, which drives the speaker. For signalling information, the DSP will decode the message and pass it to the microcomputer.
When the radio is transmitting, microphone audio is passed to the command board limiter then to the DSP-support IC, where the signal is digitized. The DSP-support IC passes digital data to the DSP, where pre-emphasis and low-pass (splatter) filtering are done. The DSP returns this signal to the DSP-support IC, where it is reconverted into an analog signal and scaled for application to the voltage-controlled oscillator as a modulation signal. Transmitted signalling information is accepted by the DSP from the microcomputer, coded appropriately, and passed to the DSP-support IC, which handles it the same as a voice signal. Modulation information is passed to the synthesizer along the modulation line. A modulated carrier is provided to the power amplifier (PA) board, which transmits the signal under dynamic power control.

### 2.3 ASTRO Mode of Operation

In the ASTRO mode (digital mode) of operation, the transmitted or received signal is limited to a discrete set of deviation levels, instead of continuously varying. The receiver handles an ASTROmode signal identically to an analog-mode signal up to the point where the DSP decodes the received data. In the ASTRO receive mode, the DSP uses a specifically defined algorithm to recover information. In the ASTRO transmit mode, microphone audio is processed identically to an analog mode with the exception of the algorithm the DSP uses to encode the information. This algorithm will result in deviation levels that are limited to discrete levels.

### 2.4 Control Head Assembly

This section discusses the basic operation and components of each control head assembly.

### 2.4.1 Display (W3 Model)

The control head assembly for a W3 model has a two-line, 14-character liquid-crystal display (LCD) with eight Status annunciators.

### 2.4.2 Display (W4, W5, and W7 Models)

The control head assembly for W4, W5, and W7 models has an 8-character, alphanumeric, vacuum fluorescent display. The anodes and the grids operate at approximately 34 Vdc when on and 0 Vdc when off. The filament operates at approximately 2.4 Vac . The voltage for the display is generated by a fixed-frequency, variable duty-cycle controlled "flyback" voltage converter. The switching frequency is approximately 210 kHz . The internal microprocessor controls the voltage converter, which provides approximately 37 Vdc to the vacuum fluorescent (VF) driver and approximately 2.4 Vrms to the VF display.

### 2.4.3 Display (W9 Model)

The control-head assembly for a W9 model has an 11-character, alphanumeric, vacuum fluorescent display. It needs three separate voltages to operate; the cathode needs 35 V to accelerate electrons to the anode; the grid needs 40 V to totally shut off current flow; the filament needs 3.8 Vac at 80 mA . These voltages are obtained from the transformer on the display controller board.

### 2.4.4 Vacuum Fluorescent Display Driver

This Vacuum Fluorescent (VF) display driver receives ASCII data from the VOCON board, decodes it into display data, and then scans the display with the data. Once properly loaded into the display, data is refreshed without any further processor action. The display driver is periodically reset by the actions of transistors that watch the clock line from the microprocessor to the display driver. When the clock line is held low for more than 600 ms , the display driver resets and new display data follows.

### 2.4.5 Vacuum Fluorescent Voltage Source (W9 Model)

Voltage for the VF display is generated by a fixed frequency, variable-duty cycle driven, flyback voltage converter. An emitter-coupled stable multi vibrator runs at approximately 150 kHz . The square wave output from this circuit is integrated to form a triangle that is applied to the non-inverting input of half an integrated circuit (IC).

During start up, the inverting input is biased at 3.7 V . A transistor is on while the non-inverting input voltage is below 3.7 V . This allows current to flow in a transformer, building a magnetic field. When the triangle wave exceeds 3.7 V , the transistor turns off and the magnetic field collapses, inducing negative current in the transformer.

This current flow charges two capacitors. As the voltage on one of the capacitor increases beyond 35 V , a diode begins to conduct, pulling the integrated circuit's inverting input below 3.7 V. This decreases the cycle time to produce the 35 V . The 41 V supply is not regulated, but it tracks the 35 V supply.

Similarly, the ac supply for VF filament is not regulated, but is controlled to within one volt by an inductor on the display board.

### 2.4.6 Controls and Indicators

The control-head assembly processes all the keypad (button) inputs and visual indicators through the microprocessor. Some of the buttons double as function keys for radio options. All buttons are backlit to allow operation in low-light conditions.

### 2.4.7 Status LEDs

These LEDs are driven by the display driver as though they were decimal points on the VF display. Level shifting transistors are required for this since the display driver uses 39 V for control signals.

### 2.4.8 Backlight LEDs

The microprocessor operates the backlight LEDs. A transistor supplies base current to the individual LED driver transistors. The driver transistors act as constant current sources to the LEDs. Some backlight LEDs are connected to a thermistor. This circuit allows more current to flow through these LEDs at room temperature and reduces current as the temperature rises.

### 2.4.9 Vehicle Interface Ports

The Vehicle Interface Ports (VIPs) allow the control head to activate external circuits and receive inputs from the outside world. In general, VIP outputs are used for relay control and VIP inputs accept inputs from external switches. See the cable kit section for typical connections of VIP input switches and VIP output relays.

The VIP outputs are driven by logic within the control head for both the Dash and Remote Mount configuration. Field programming of the radio can define the functions of these pins. The output transistors that drive the VIP outputs can sink 300 mA of current. Primarily, they are used to control external relays. These relays should be connected between the respective VIP output pin and switched B+. Typical applications for VIP outputs are controlling the external horn/lights alarm and activating the horn-ring transfer relay function.

## Remote Mount Configuration:

The VIP pins are located on the back of the control head below the area labeled "VIP". For Remote Mount radios, all three VIP inputs and outputs are available at the rear of the control head. The VIP inputs are connected to ground with either normally-open or normally-closed switches.

## Dash Mount Configuration:

For the Dash Mount configuration, only two VIP output pins are available and they are located at the 15 -pin accessory connector. VIP input lines are not available in this configuration.

### 2.4.10 Power Supplies

The $+5-\mathrm{V}$ supply is a three-terminal regulator IC to regulate the 12 V SWB+ down for the digital logic hardware.

### 2.4.11 Ignition Sense Circuits

A transistor senses the vehicle ignition's state, disabling the radio when the ignition is off. For negative-ground systems, the orange lead is typically connected to the fuse box ( +12 V ).

NOTE: Refer to the ASTRO Spectra and Digital Spectra FM Two-Way Mobile Radios Installation Manual (68P81070C85) for more information on operating the radio independent of the ignition switch.

### 2.5 Power Amplifier

The power amplifier (PA) is a multi-stage, discrete-transistor RF amplifier consisting of the following:

- Low-level power controlling stage
- Drivers
- Final amplifier
- Directional coupler
- Antenna switch
- Harmonic filter


### 2.5.1 Gain Stages

The first stage buffers the RF signal, filters harmonics, and acts as a variable amplifier. All of the amplifying stages are matched using transmission lines, capacitors, and inductors and are supplied with DC from either A+, keyed $9.4-\mathrm{V}$, or $9.6-\mathrm{V}$ sources. Following the last gain stage, PIN diodes switch the signal flow either from the antenna to the receiver, or from the last gain stage to the antenna.

### 2.5.2 Power Control

A directional coupler and detector network controls power. It senses the forward power from the last gain stage and feeds the detected voltage back to the command board control circuitry where it is compared to a reference voltage set during power-set procedures. The DC feed voltage is corrected and supplied to the "controlled" stage of the power amplifier. Circuitry on the power amplifier board controls the gain of the first stage and is proportional to the DC control voltage.

### 2.5.3 Circuit Protection

Current and temperature sensing circuitry on the power amplifier board feed sensed voltages to the command board for comparison. If the command board suspects a fault condition, it overrides the power control function and cuts the power back to a level that is safe for the conditions. In addition, some high-power amplifier boards include circuitry that monitors the power supply line. If the battery voltage exceeds or drops below a pre-determined level, the power output of the amplifier is adjusted to ensure proper operation of the transmitter.

### 2.5.4 DC Interconnect

The ribbon cable connector carries sensed voltages for power and protection to the command board. It also carries A+ feed to the command board for distribution throughout the internal transceiver housing and carries control voltage from the command board to the power amplifier board.

The rear battery connector carries A+ from the battery to the power amplifier board. The red lead goes directly to the A+ terminal on the PA board. The black lead from the battery connector ties to the chassis, and connection to the power amplifier board is made through the board mounting screws.

A+ ground connection for the internal transceiver housing is through the RF coax ground connectors and through the mechanical connection of the power amplifier heatsink to the rest of the radio. During test conditions in which the power amplifier assembly (board and heatsink) is physically disconnected from the rest of the radio, it is acceptable to rely on the coax cable connections to carry ground to the internal housing.

### 2.6 Front-End Receiver Assembly

The receiver front-end consists of a preselector, a mixer circuit, and an injection filter. The receiver injection (1st local oscillator) comes from the VCO assembly through a coax cable. The injection filter is either fixed-tuned or tuned at the factory depending upon the bandsplit. The output of the filter is connected to the mixer.

The preselector is a fixed-tuned filter. The receiver signal is fed to the preselector from the antenna switch in the PA for the 800 MHz and UHF radios, or the preamp output for VHF. The signal is then sent to the mixer integrated circuit where it is connected to the mixer transistor. The receiver injection is also fed to this point. The mixer output is at the 1st IF center frequency of 109.65 MHz . This signal is sent to the 1st IF amplifier stage on the RF board through a coaxial cable.

### 2.7 RF Board Basic

The RF board contains the common synthesizer circuits, dual IF receiver and demodulation circuits. A 4-pole crystal filter at 109.65 MHz provides first IF selectivity. (For HRN6014D, HRN6020C, HRN6019C, HRN4009D, HRN4010C and later RF board kits, two 2-pole crystal filters provide first IF selectivity at 109.65 MHz .) The output of the filter circuit is fed directly to the custom digital back-end circuit module. An amplification circuit at 109.65 MHz , the second mixer, the second IF amplifiers (at 450 kHz ), the IF digital-to-analog converter, and the baseband down-converter comprise the digital back-end circuit module.

Synthesizing for the first and second VCO is performed by the prescaler and synthesizer ICs. These ICs are programmed through a serial data bus from signals generated on the VOCON board. A DC voltage generated on the command board, sets the synthesizer's reference oscillator frequency of 16.8 MHz . This voltage is controlled by the digital-to-analog converter (D/A), and is the only element of the RF board requiring alignment.

The second local oscillator runs at 109.2 MHz (low-side injection), or 110.1 MHz (high-side injection) and consists of a VCO which is frequency-locked to the reference oscillator. Part of the local oscillator's circuitry is in the prescaler IC.

A clamp and rectifier circuit on the RF board generates a negative DC voltage of -4 V (nominal) for increasing the total voltage available to the first VCO and second local oscillator's VCO. The circuit receives a 300 kHz square wave output from the prescaler IC, then clamps, rectifies, and filters the signal for use as the negative steering line for the two VCOs.

### 2.8 Voltage-Controlled Oscillator

This section discusses the voltage-controlled oscillator components and basic operation for each band.

### 2.8.1 VHF Radios

The voltage-controlled oscillator (VCO) assembly utilizes a common-gate Field Effect Transistor (FET) in a Colpitts configuration as the gain device. The LC tank circuit's capacitive portion consists of a varactor bank and a laser-trimmed stub capacitor. The inductive portion consists of microstrip transmission line resonators. The stub capacitor serves to tune out build variations. Tuning is performed at the factory and is not field adjustable. The varactor network changes the oscillator frequency when the DC voltage of the steering line changes. The microstrip transmission lines are shifted in and out of the tank by PIN diodes for coarse frequency jumps. A third varactor is used in a modulation circuit to modulate the oscillator during transmit.

The VCO output is coupled to a transistor for amplification and for impedance buffering. The output of this stage passes through a low-pass filter where the signal is split into three paths. One path feeds back to the synthesizer prescaler; the other two provide injection for the RX and TX amplification strings. The receive injection signal is further amplified and passed to the RX front-end injection filter. The transmit signal goes to an analog divider, which divides the signal by two. The signal is amplified and buffered and then injected into the transmitter's low-level amplifier.
All transmit circuitry operates from keyed 9.4 V to conserve current drain while the radio is receiving. A transistor/resistor network drives the PIN diodes in the VCO tank. These driver networks provide forward bias current to turn diodes on and reverse the bias voltage to turn the diodes off. AUX 1 AND AUX 2 lines control the PIN diode driver networks.

### 2.8.2 UHF and 800 MHz Radios

The voltage-controlled oscillator (VCO) assembly generates variable frequency output signals controlled by the two steering lines. The negative steering line increases the tuning range of the VCO, while the positive steering line affects the synthesizer control loop to incrementally change the frequency.
The VCO generates a signal in the required frequency range. For UHF and 800 MHz radios, this signal is fed to the doubler/buffer circuit which, in turn, doubles the VCO output frequency and amplifies it to the power level required by the TX buffer and RX mixer. A PIN diode switch routes the signal to the TX port when the keyed 9.4 V is high. Otherwise, the signal is routed to the RX port. The synthesizer feedback is provided from the output of the doubler stage.

### 2.9 Command Board

The serial input/output IC provides command board functions including buffers for PTT, channel active, squelch mute, busy, and data transmission, and logic functions for switched $B+$, emergency, reset, and power control.
The regulator and power control circuits include an unswitched +5 V discrete circuit and the regulator/power control IC, which produces both switched +5 V and 9.6 V . The unswitched +5 V source is used as a reference for its switched +5 V source. Filtered unswitched +5 V is used for the microcontrol circuits. Switched +5 V and 9.6 V are controlled by a digital transistor from the serial input/output IC.The power control circuitry receives power set and limit inputs from the digital-toanalog IC, and feedback from the RF power amplifier. Based on those inputs, the power control circuitry produces a control voltage to maintain a constant RF power level to the antenna.

The reset circuits consist of the power-on reset, high/low battery voltage reset, and the external bus system reset. The reset circuits allow the microcomputer to recover from an unstable situation; for example, no battery on the radio, battery voltage too high or too low, and remote devices on the external bus not communicating. Communication in RS-232 protocol is provided by an IC which interfaces to the rear accessory connector (J2).

### 2.10 ASTRO Spectra Vocoder/Controller Board

The Vocoder/Controller (VOCON) board, located on the top side of the radio housing, contains a microcontrol unit (MCU) with its flash memory, DSP, and DSP support ICs. The VOCON board controls receive/transmit frequencies, the display, and various radio functions, using either direct logic control or serial communication to external devices. The connector J801 provides interface between the encryption module and the VOCON board for encrypting voice messages.

The VOCON board executes a stored program located in the FLASH ROM. Data is transferred to and from memory by the microcontrol unit data bus. The memory location from which data is read, or to which data is written, is selected by the address lines.

The support-logic IC acts as an extension of the microcontrol unit by providing logic functions such as lower address latch, reset, memory address decoding, and additional control lines for the radio. The VOCON board controls a crystal-pull circuit to adjust the crystal oscillator frequency on the microcontrol unit, so that the E-clock harmonics do not cause interference with the receive channel.

The vocoder circuitry on the VOCON board is powered by a switched $+5-\mathrm{V}$ regulator located on the command board. This voltage is removed from the board when the radio is turned off by the control head switch.

The DSP (digital-signal processing) IC performs signaling, voice encoding/decoding, audio filtering, and volume control functions. This IC performs Private-Line/Digital Private Line (PL/DPL) encode and alert-tone generation. The DSP IC transmits pre-emphasized analog signals and applies a lowpass (splatter) filter to all transmitted signals. It requires a 33 MHz crystal to function. An 8 kHz interrupt signal generated by the DSP-support IC is also required for functionality. This device is programmed using parallel programming from the microcontrol unit and the DSP-support IC.

The DSP-support IC performs analog-to-digital and digital-to-analog conversions on audio signals. It contains attenuators for volume, squelch, deviation, and compensation, and it executes receiver filtering and discrimination. The IC requires a 2.4 MHz clock to function (generated by the digital back-end IC) and is programmed by the microcontrol unit's Serial Peripheral Interface (SPI) bus.

### 2.11 Radio Power

This section provides information on DC power distribution in ASTRO radios.

### 2.11.1 General

In the ASTRO radio, power is distributed to seven boards: command, VOCON, control head, synthesizer, receiver front end, RF, and RF power amplifier.
Power for the radio is supplied by the vehicle's $12-\mathrm{V}$ battery. When using a desktop adapter unit, an external DC power supply can be connected to replace the vehicle's battery source.
A+ (referred to as incoming unswitched battery voltage) enters the radio through the rear RF power amplifier connector ( P 1 ) and is the main entry for DC power. The second path, through P2, pin 5 , provides ignition sense to inhibit the RF transmitter when the ignition switch is off.

When the command board regulators are "on," the 9.6-V output sources the command board and RF board circuits. The switched +5 V is routed to the VOCON board. See Figure 2-1.


Figure 2-1. DC Voltage Routing Block Diagram
The 9.6 V and the $\mathrm{A}+$ voltage are the main DC power for the RF board. Outputs from the RF board provide DC power to the synthesizer and the receiver front-end filter. The RF board has an internal $+5-\mathrm{Vdc}$ regulator that is sourced from the $\mathrm{A}+$ voltage.
The voltage to power the $9.4-\mathrm{V}$ regulator is produced by the command board's $9.6-\mathrm{V}$ regulator. The 9.4 V (referred to as "keyed 9.4 V ") is controlled by the VOCON board through P501, pin 45. This DC voltage enables the transmitter's RF power amplifier when the VOCON board senses a lock detect from the synthesizer.

### 2.11.2 B+ Routing for ASTRO Spectra VOCON Board

Refer to Section 3.4, "ASTRO Spectra Plus VOCON Board," on page 3-38 for information on the ASTRO Spectra Plus.

See Figure 2-2 and your specific schematic diagram.
The $\mathrm{A}+$ power for the radio is derived from the $12-\mathrm{V}$ battery, which is applied to the command board through connector P503, pins 5 and 9. This A+ voltage is routed through the command board to the control head connector, P502, pin 30 and to the VOCON board, J501, pin 38.

The interconnect board couples the A+ voltage from the command board to the control head, where a power FET (Q51) provides the means of controlling the main power source (SWB+) by the on/off switch. SWB+ is routed back to the SIO/IC (U522) on the command board through connector P502. The SIO/ICcontrols the RPCIC enable line.

When the RPCIC enable line toggles low, the $9.6-\mathrm{V}$ and the $\mathrm{SW}+5-\mathrm{V}$ regulators turn on. The $\mathrm{SW}+5-$ V regulator is the main power source for the VOCON board. Digital and analog +5 V are derived by filtering SW +5 V through $.005 \mu \mathrm{H}$ chokes L511 and L510 on the command board. These two 5-V regulated supplies are used to partition the digital logic circuitry from the analog circuitry.

Transistor Q206 controls solid-state power switch Q207, providing SWB+ to the encryption module (if equipped). The "SWB+" and "UNSWB+" encryption voltages both originate from pin 38 of J501 and are fed to the encryption module via J801.

Port PL3 (5-V EN) on the SLIC and Q207 are under the control of the microcontroller unit (MCU), U204. This allows the MCU to follow an orderly power-down sequence when it senses that the B+ sense is off. This sense is provided via resistor network R222 and R223, which provides an input to the A/D port to the MCU.
It should also be noted that a system reset is provided by the undervoltage detector, U407. This device brings the system out of reset on power-up, and provides a system reset to the microcomputer on power-down.


Figure 2-2. ASTRO Spectra B+ Routing for Vocoder/Controller (VOCON) Board

## Chapter 3 Theory of Operation

### 3.1 RF Board

This section provides a detailed circuit description of the ASTRO RF board for VHF, UHF and 800 MHz models. This board contains the common synthesizer circuits (synthesizer section) and dual IF receiver and demodulation circuits (receiver back-end). When reading the theory of operation, refer to your appropriate schematic and component location diagrams located in "Chapter 7. Schematics, Component Location Diagrams, and Parts Lists". This detailed Theory of Operation will help isolate the problem. However, first use the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20) to troubleshoot the problem to a particular board.

### 3.1.1 General

The synthesizer section includes the prescaler IC (U601), the synthesizer IC (U602), and the reference oscillator (U600). The prescaler and the synthesizer chips are completely controlled by the serial data bus.

The prescaler IC (see Figure 3-1) provides the following:

- Multi-dual modulus prescaler
- 5-V regulator
- Super filter 8.6-V regulator
- Fixed divide-by-8 circuit for the reference oscillator
- Programmable divide-by- N and charge pump phase detector to support the second injection VCO

The synthesizer IC (see Figure 3-2) provides:

- Reference divider
- Phase modulator
- Dual-bandwidth adaptive filter
- Ramp generator
- Sample-and-hold phase detector
- Programmable loop divider
- Auxiliary output bits for system control


MAEPF-25181-O
Figure 3-1. Prescaler IC Block Diagram


Figure 3-2. Synthesizer IC Block Diagram

The reference oscillator generates the 16.8 MHz signal that serves as the reference for all radio frequency accuracy. It uses a proprietary temperature compensation circuit to keep the radio within its specified frequency tolerance.

The receiver back-end uses the ABACUS II IC (U301) to demodulate all the way to baseband, starting from the first IF.

### 3.1.2 Synthesizer

This section discusses the synthesizer components and detailed theory of operation.

### 3.1.2.1 Reference Frequency Generation

The reference oscillator (U600) generates a 16.8 MHz reference signal that is tuned onto frequency via a DC-fed varactor input. The digital/analog IC (U502), which is on the command board and is under the control of the serial data bus, generates the DC voltage to the varactor. The reference signal from U600-3 is capacitively coupled into the prescaler (U601-21), where it is divided by 8 . The resulting 2.1 MHz signal is routed to the synthesizer IC (U602).

The 2.1 MHz signal is divided by 7, with the result, a 300 kHz signal, serving the following purposes:

- Input to the prescaler IC for second VCO reference
- A source for the negative voltage generator
- Input to the programmable reference divider


### 3.1.2.2 First VCO Frequency Generation

For reasons of clarity and simplicity, 800 MHz is used as the example product in all synthesizer text. In the 800 MHz models, the feedback is taken before the doubler circuit of the VCO. Band-to-band and kit-to-kit variations are noted in the text as required.
The first VCO in ASTRO radios is a thick-film hybrid transmission line resonator. Its frequency is controlled by a DC-fed varactor bank.

A transmission line feedback path from J601-1 to C604 couples the output frequency back to the prescaler. The signal from the prescaler output (U601, pin 40) is routed to the synthesizer input (U602, pin 27), where it is divided by the A\&B counters of the loop divider. The loop equations required for calculating the counter values are as follows:

NOTE: These are examples - the prescaler modulus and the reference frequency are programmable and vary from band-to-band. The examples that follow are for 800 MHz and assume:
$P / P+1=255 / 256$ and $F_{r}=6.25 \mathrm{kHz}$. For UHF and VHF, $P / P+1=127 / 128$ and $F_{r}=5 \mathrm{kHz}$.
EQUATION: $N=F_{v c o} / F_{r}$
EXAMPLE: $\mathrm{N}=\left(\mathrm{F}_{\mathrm{vco}} / \mathrm{F}_{\mathrm{r}}\right)=(403 \mathrm{MHz} / 6.25 \mathrm{kHz})$ or $\mathrm{N}=64,480$
EQUATION: $A=($ fractional remainder of $N / P)(P)$
EXAMPLE: $A=N / P=(72,000 / 255)=252.8627 ; .8627 \times 255$
or $\mathrm{A}=220$
EQUATION: $\mathrm{B}=[\mathrm{N}-\{\mathrm{A} x(\mathrm{P}+1)\}] / \mathrm{P}$
EXAMPLE: $B=[64,480-\{220 \times(255+1)\}] / 255$ or B = 32
Plug in the calculated numbers to test the value of $N$ with the following equation:

$$
\begin{aligned}
& \text { EQUATION: } N=B(P)+A(P+1) \\
& \text { EXAMPLE: } N=(32)(255)+(220)(256) \text { or } N=64,480
\end{aligned}
$$

The synthesizer generates a modulus control output which instructs the prescaler to divide by either P or $\mathrm{P}+1$ (that is, 255 or 256 ). When modulus control is low, the prescaler is dividing by $\mathrm{P}+\mathrm{I}(256)$ and the A counter is running; when modulus control is high, the prescaler is dividing by $P(255)$ and the B counter is running. One complete cycle of loop division is repeated for each reference period.

Assume that the VCO is operating correctly at 403 MHz , and the reference frequency is 6.25 kHz . The prescaler and loop divider work in tandem to divide the VCO frequency down to the reference frequency. The waveforms in Figure 3-3 depict what happens in a locked system. Notice in the waveforms that the leading edge of $F_{r}$ goes high to turn on the constant current source Q607. The ramp capacitor (C634) begins to charge through Q607 and R627, charging at a constant rate, while the prescaler and loop divider are dividing the VCO frequency by $N(64,480$ in the example). At this point, the loop divider generates a loop pulse $\left(F_{v}\right)$ which turns off the current source.


MAEPF-25183-O
Figure 3-3. Loop Divider Waveforms
The voltage that was on C 634 is sampled and held by the phase detector. This voltage is amplified approximately 1.8 times and applied to the VCO varactors via the adaptive loop filter and the steering line. This event is repeated at the reference rate so that frequency errors will always be corrected.

NOTE: In VHF receive mode, for frequencies divisible only by 2.5 kHz (for example, 146.0025 MHz ), capacitor C 670 will be switched in parallel with C 634 by Q670. The reference frequency will be 2.5 kHz instead of 5.0 kHz or 6.25 kHz . In transmit mode, the 2.5 kHz reference is not used.

Assume that the VCO frequency tends to drift low. If this happens, the loop pulse will occur at some later time. The current source still begins at the rising edge of $F_{r}$ but it stays on longer because the leading edge of $F_{V}$ has been time delayed. Thus, C634 charges to a higher value and the steering line drives the VCO to a higher frequency. The opposite case also applies.

### 3.1.2.3 Programmable Reference Divider

The reference frequency for 800 MHz is 6.25 kHz ; for VHF and UHF, the typical reference frequency is 5.0 kHz . In VHF radios, the reference frequency is 2.5 kHz for receive frequencies not evenly divisible by 5.0 kHz or 6.25 kHz .

### 3.1.2.4 Phase Modulator

ASTRO radios use a dual-port modulation scheme. The nature of the synthesizer loop is to track out low-frequency errors. In order to enable low-frequency modulation, such as DPL, the reference signal is modulated with the same signal as the VCO. Effectively, this prevents the low-frequency error in the loop (DPL) from tracking out because the same error is on the reference signal. The net effect is that the leading edge of the reference pulse is time-varying at the same rate as the loop pulse; therefore, there is no phase error between the two signals and low-frequency modulation is allowed to pass.

The phase modulation comparator has two inputs: U602, pins 28 and 29. R625 and C630 form an exponential ramp into the plus side of the comparator on U602, pin 29. This ramp is tickled at the reference rate. R626 and C631 form an integrator through which modulation is applied to the minus side of the comparator. The comparator trips when the ramp voltage reaches the voltage on U602, pin 28. The output of the comparator is the time-shifted leading edge of $F_{r}$.

### 3.1.2.5 Loop Filter

ASTRO radios use a switchable, dual-bandwidth loop filter. They also use adaptive filter switching to achieve fast lock. The output of the phase detector is routed to an external device (Q608), the output of which is routed back into the IC for proper filter path selection.

In normal operation, the high drive buffer output is routed through the appropriate transmission gates into the selected filter. A simplified schematic is shown in Figure 3-4.


MAEPF-25184-O
Figure 3-4. Loop Filter Schematic
The loop filters greatly minimize voltage transients that contribute to system hum and noise but, due to their lowpass nature, it takes considerable time to change the average charge in the filters. Therefore, the adapt scheme was implemented. When the radio is changing frequency, the loop goes into the adapt mode. Selected transmission gates in the IC effectively place a short across the resistors in the filter (eliminating associated RC time constants) and quickly charge the loop filter capacitors to the correct steering line voltage for the new frequency. At the end of the adapt sequence, the appropriate filter is reconnected via internal transmission gates.

### 3.1.2.6 Auxiliary Control Bits

The auxiliary control bits are system control outputs whose states are controlled by the microprocessor via the serial data bus. AUX 1 and AUX 2 are sent to the first VCO to control pin shift states. AUX 3 controls the state of the negative steering line.

### 3.1.2.7 Second VCO

The second VCO is a grounded-gate, FET Colpitts oscillator. The resonator consists of a fixed inductor and a varactor. A potentiometer, R634, adjusts the negative voltage to the varactor. This adjustment is performed at board test to bring the phase detector output to the center of its linear region; that is approximately 2.25 V. (For HRN6014D, HRN6020C, HRN6019C, HRN4009D, HRN4010C and later RF board kits, a voltage divider consisting of R633 and R635 brings the phase voltage detector output to the center of its linear region ( 2.25 V ), eliminating the adjustment at board test.) The negative voltage is filtered by R611 and C612. The oscillator output is coupled into the IF IC (U301) as a second injection source. It is also fed back to the prescaler (U601, pin 26) for phase locking.

The prescaler contains a programmable, single modulus, divide-by- N circuit, and a charge pump phase detector. The reference frequency ( $F_{r}$ ) is 300 kHz and comes in on U601, pin 31. The low-side injection oscillator runs at 109.2 MHz and is divided by 364 inside the IC. The phase detector in the chip compares the divided signal to $F_{r}$ and either sources or sinks current, as necessary, in order to maintain frequency control.
The phase detector output is routed to the varactor via decoupling choke L604. A divide-by-N test point is also provided from U601, pin 29.

### 3.1.2.8 Power Distribution

The command board provides all power to the synthesizer in the form of 9.6 Vdc . The prescaler has onboard voltage regulators for 5 V and super filter 8.6 V . The $5-\mathrm{V}$ regulator drives the external series pass device Q602; the super filter's pass device is Q603.

### 3.1.3 Receiver Back-End

This section discusses the receiver back-end components and detailed theory of operation.

### 3.1.3.1 First IF

The 109.65 MHz IF signal reaches the RF board via a connector J350. Transistor Q350 amplifies the signal approximately 9dB and supplies the proper impedance for crystal filter Y350. (For HRN6014D, HRN6020C, HRN6019C, HRN4009D, HRN4010C and later RF board kits, amplification circuitry consisting of transistors Q350 and Q354 amplifies the signal approximately 9dB and supplies the proper impedance for crystal filters FL350 and FL351.)

Transistor Q351 supplies filtered A+ for powering Q350 and the receiver front-end. Transistor Q352 switches the filtered A+ supply by reducing the base current from Q351.
NOTE: Since there is 12.5 Vdc on J350, it is important to use a DC block when connecting J350 to an external source.

Y350 is a 4-pole crystal filter, consisting of two independent 2-pole crystal filters contained in a single package. The filter package has a polarization mark located on the top to ensure proper installation.
Y350 supplies the 109.65 MHz IF selectivity and its output passes through a matching network and then goes to ABACUS II IC (U301) pin 30.
(For HRN6014D, HRN6020C, HRN6019C, HRN4009D, HRN4010C and later RF board kits, FL350 and FL351 are 2-pole crystal filters which supply the 109.65 IF selectivity. The output passes through a matching network and goes to the ABACUS II IC (U301), pin 30.)

### 3.1.3.2 ABACUS II IC

Once in the ABACUS II IC (U301), the first IF frequency is amplified and then down converted to 450 kHz , the second IF frequency. At this point, the analog signal is converted into two digital bit streams by a sigma-delta A/D converter. The bit streams are then digitally filtered and mixed down to baseband and filtered again. The differential output data stream is then sent to the VOCON board where it is decoded to produce the recovered audio.

The ABACUS II IC is electronically programmable, and the amount of filtering, which is dependent on the radio channel spacing and signal type, is controlled by the microcomputer. Additional filtering, which used to be provided externally by a conventional ceramic filter, is replaced by internal digital filters in the ABACUS II IC.

The ABACUS II IC contains a feedback AGC circuit to expand the dynamic range of the sigma-delta converter. The differential output data contains the quadrature (I and $Q$ ) information in 16-bit words, the AGC information in a 9-bit word, imbedded word sync information and fill bits dependent on sampling speed. A fractional-n synthesizer is also incorporated in the ABACUS II IC for the 2nd LO generation.
The second LO/VCO is a Colpitts oscillator (see Section 3.1.2.7, "Second VCO," on page 3-6). Its output feeds into the ABACUS II IC on pin 35, providing injection to the second mixer for converting the IF frequency to 450 kHz .

### 3.2 Command Board

This section of the theory of operation provides a detailed circuit description of the ASTRO Digital Spectra Command Board. When reading the Theory of Operation, refer to your appropriate schematic and component location diagrams located in "Chapter 7. Schematics, Component Location Diagrams, and Parts Lists". This detailed Theory of Operation will help isolate the problem to a particular component. However, first use the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual to troubleshoot the problem to a particular board.

The command board includes the following integrated circuits:

- U401, U402 - Differential Amplifiers
- U450 - Audio Amplifier
- U500 - Regulator/Power Control IC (RPCIC)
- U501 - 555 Timer
- U502 - D/A Converter
- U503 - Precision Voltage Regulator
- U522 - Serial Input/Output IC (SIOIC)
- U523, U524 - Analog Switch
- U526 - RS232 Level Shifter
- U530 - 8-Bit Shift Register


### 3.2.1 Microcontroller and Support ICs

The microcontroller and support ICs are located on the VOCON board, and are interconnected to the command board via connector P501. The control lines linking the boards are either drivers or receivers, depending upon their application. The VOCON board is responsible for decoding or encoding ASTRO and analog data, and producing receive audio and transmit deviation.

### 3.2.2 Serial Input/Output IC

The serial input/output IC (SIOIC), U522, is a special-function logic/linear integrated circuit. In the ASTRO mobile application, the device provides power-on reset, power control, and bipolar driver/ receivers for serial communication. The SIOIC supports the following functions:

1. A buffer for push-to-talk (PTT) to SLIC (U522, pins 37 and 38). Normally a contact closure for PTT is detected by the control head, which sends a command to the VOCON board via the external serial bus protocol. However, some applications require direct PTT control. To generate PTT via the buffer inverter (pin 37), a contact closure to ground at J502, pin 24, or from the accessory connector P503, pin 17, will generate a logic high to the SLIC device (U206, port PH6) on the VOCON board.
2. A buffer for the Busy signal from the VOCON board to the external bus (Busy Out) and the return path back to the VOCON board (Busy RTS). This function is described in Section 3.2.6, "Serial Communications on the External Bus," on page 3-11.
3. A buffer for Data Transmission from the VOCON board to the External Bus and a received data return to the VOCON board. This function is described in Section 3.2.6, "Serial Communications on the External Bus," on page 3-11.
4. Inputs to sense Switched B+ or Emergency enabling the Power Regulators and provide the switched $+5-\mathrm{V}$ regulated supply. This function is described in Section 3.2.3, "Power-Up/Down Sequence," on page 3-9.
5. Power-on reset (POR*) circuits provide reset to the Host processor (U204). This function is described in Section 3.2.5, "Reset Circuits," on page 3-10.

### 3.2.3 Power-Up/-Down Sequence

Normally, switched $B+(S W B+)$ enters the command board from P502, pin 31. This voltage is derived from the battery $A+$ voltage which enters the control head through P502, pin 30. A power FET transistor, located in the control head (W5 and W7 models), provides the means of controlling the main power source via the control head's on/off switch.

When SWB+ or EMERG become active, the RPCIC EN output (U522, pin 15) goes to a logic low, enabling the Switched $+5-\mathrm{V}$ and $+9.6-\mathrm{V}$ regulators of the RPCIC (U500). Approximately 220 ms after the B+ is active (see Waveform W1), the power-on-reset (POR*) from U522, pin 40 switches to a logic 1 state, enabling the microprocessor on the VOCON board. The microprocessor then completes an initialization sequence and sets Row 5/5-V enable input to a logic low at P501, pin 15. The input provides a low to the SIOIC to hold the $9.6-\mathrm{V}$ enable on. Therefore, if SWB+ or EMERG go inactive, the regulators will remain enabled until the microcontroller turns them off by returning the $9.6 / 5-\mathrm{V}$ EN state to a logic high. (This is especially true with emergency, since the foot switch is usually momentary.)
The emergency input is provided to enable the radio transceiver to be activated, regardless of the state of the control head's on/off power switch. The emergency input (EMERG) is activated by opening the normally grounded foot switch connected to either P502, pin3 or P503, pin 24 . This input is routed to the SIOIC (U522, pin 31) and is internally connected to a pull-up resistor within the IC to provide the logic 1 state change.

This change is inverted through an exclusive OR gate within the IC, outputting a logic 0 at pin 30 and the NOR gate input (internal to the IC) to enable the $9.6-\mathrm{V}$ regulator. The logic low at pin 30 is connected to a time-out timer, which latches the $9.6-\mathrm{V}$ enable output for 100 ms . This delay is required to allow the VOCON microprocessor to initiate its start-up vectors and poll the emergency interrupt input from P501, pin 16. The microprocessor takes control of the 9.6 V (P501, pin 15), holding it active low regardless of the states of other inputs.
The emergency active state depends on the emergency polarity (EMERG POL) input to the SIOIC (U522, pin 32). When the jumper JU502 is installed, emergency is active with the foot switch open. Removing JU502 causes the emergency to go active with the switch closed.

To turn off the radio, SWB+ is taken inactive ( -Vdc ) by pressing the on/off switch on the control head. The microcontroller periodically audits the SWB+ at its input port (pin 20) to determine if it has returned to a logic high. When it sees the logic high condition (caused by an inactive switch), the microcontroller initiates the power-down sequence, turning the voltage regulators and the radio off.

### 3.2.4 Regulators

The regulator circuits include an unswitched +5 V (UNSW5V) discrete circuit, and the regulator/ power-control IC (RPCIC) that produces switched +5 V (U500, pin 14) and 9.6 V (U500, pin 17). The UNSW $+5-\mathrm{V}$ source is used by the RPCIC as a reference (U500, pin 20) for its switched $+5-\mathrm{V}$ source. This regulated voltage is produced from the A+ voltage and is present when the battery is connected. The regulators within the RPCIC are controlled by the input to pin 24 via a digital transistor, Q538. This device is controlled from an output (9.6/5-V enable) of the SIOIC (U522, pin 15).

The various voltages used by the ICs on the command board are shown in Table 3-1.
Table 3-1. Integrated Circuits Voltages

| Integrated Circuit | UNSW5V | SW +5V | SW +9.6V |
| :--- | :---: | :---: | :---: |
| Serial Input/ Output (SIOIC) | U522-6, -24 | U522-3, -12 | U522-14 |
| Regulator/ Power Control <br> (RPCIC) | U500-20 | U500-14 | U500-17 |
| Digital/ Analog IC (DAIC) |  | U502-1,-28 |  |
| Analog Switch |  |  | U523-16, U524-14 |
| RS232 Driver (IC) |  | U526-19 |  |
| 555 Timer (IC) |  | U501-8 |  |
| 8-Bit Shift Register |  | U530-16 |  |
| Differential Amplifiers |  |  | U401-4, U402-4 |

### 3.2.5 Reset Circuits

The reset circuits consist of the power-on reset (POR), high-/low- battery voltage reset, and the external bus system reset. The reset circuits allow the microcontroller to recover from an unstable condition, such as no battery on the radio, battery voltage too high or too low, and remote devices on the external bus not communicating.

When the battery ( $\mathrm{A}+$ ) is first applied to the radio, the unregulated voltage source powers the unswitched $+5-\mathrm{V}$ regulator and the SIOIC internal regulator. The voltage is also sent to the control head, where it is switched on/off by a series FET transistor. The transistor returns the voltage to the command board, via connector P502-31, as switched B+. The switched B+ voltage is sensed by the SIOIC on pin 28, and changes the state of the 9.6-V enable output gate (RPCIC_EN*) to an active "low." The low state turns on the 9.6-V regulator (U500-24), and its regulated output is fed back to the input of the voltage comparator on the SIOIC (U522-14). The comparator output switches to a logic low upon exceeding the 5.6-V threshold (see Figure 3-5).

The three inputs to the NOR gate (SW9.6-V, RPCIC EN, and RPCIC_EN delayed) must be at a logic low to enable the power-on reset (POR*) to a high logic state. During this power-up sequence, this reset is delayed approximately 170 ms after the $\mathrm{B}+$ voltage is sensed. This delay is needed to allow the supply voltages and oscillators to stabilize before releasing the VOCON board's microprocessor. Figure 3-5 illustrates the internal function of the POR* within the SIOIC device.


MAEPF-25185-O
Figure 3-5. Power-on Reset

### 3.2.6 Serial Communications on the External Bus

Serial communications on the external bus use the BUS+ (J502-25), BUS- (J502-22), and BUSY (J502-9) lines.
These three lines are bidirectional; therefore, numerous devices can be in parallel on the bus. All devices monitor the bus while data is being transmitted at a 9600-baud rate. The transmitted data includes the address of the device for which the data is intended. Examples of the different types of data are: control head display data and button closure data.

Data bus drivers for the BUS+ and BUS- lines are differentially driven, having BUS- inverted from the state of BUS+. The idle states are: BUS+, a logic high; and BUS-, a logic low. The drivers are so designed that any of the devices on the bus can drive these lines to their non-idle state without loading problems.

In a typical transmission, the microcontroller examines the BUSY line. If the BUSY line is in the idle state, the microcontroller sets the BUSY line and then transmits. At the end of transmission, the microcontroller returns the BUSY line to idle. The microcontroller sets the BUSY line via microcontroller pin 30, SIOIC pins 10 and 13, and J502-9.
Data transmission is sent onto the bus asynchronously. When the microcontroller sends data onto the bus, the microcontroller also monitors the transmitted data as a collision detection measure. If a collision is detected as a result of receiving a different data pattern, the microcontroller will stop transmission and try again. The microcontroller monitors and receives data via the BUS+ line (J50225) to the SIOIC (U522, pin 17) and the BUS- line (J502-22) to the SIOIC (U522, pins 18 and 20), and pin 20 of the microcontroller. Data is transmitted from microcontroller pin 19 to the SIOIC to BUS+ (J501, pin 25), and the SIOIC to BUS- (J501, pin 22).

In the remote version of the radio, option cards can be installed. If data transmission is required, data is transmitted from J502-20 to SIOIC pin 19, then from the SIOIC to BUS+ (J501, pin 25), and the SIOIC to BUS- (J501, pin 22).

### 3.2.7 Synchronous Serial Bus (MOSI)

The synchronous serial bus is an internal bus used by the microcontroller for communicating with various ICs. The serial bus, called MOSI (master out/ slave in), is used to program the digital-toanalog (D/A) converter IC (U526), the serial-to-parallel shift register (U530) on the command board, and the ABACUS II IC (U301) on the RF board. The MOSI data is sent from the VOCON board's microprocessor (U204) through the ADSIC input/output IC (U406) and enters the command board through P501, pin 9. This serial bus has an associated clock and individual select lines for steering the data to one of the three possible devices.

The clock and data are routed in parallel to all serially programmed ICs. The ICs are programmed one-at-a-time by the microcontroller, with each IC ignoring activity on its clock and data lines unless it has been selected.

### 3.2.8 Received Audio

The received audio is sent from the ADSIC D/A converter as the SDO signal. The audio enters the command board at P501, pin 40, and is routed to the analog multiplex gate (U524, pin 1). The gate's output (U524, pin 2 ) is paralleled with the output of a second multiplex gate (U524, pin 9 ) and sent to voltage divider R455 and R456. The voltage divider provides the required attenuation for minimum/ maximum volume control settings. Capacitor C454 provides a DC block and couples the audio into U450, pin 2 for amplification.

The two multiplex gates provide control of either receive audio or vehicular repeater audio. These gates are controlled by the inputs to U524, pin 13 and U524, pin 6 from the serial shift register, U530. The independent inputs are software selected by the VOCON's microcontroller.

The audio power amplifier (PA), U450, is a DC-coupled-output bridge-type amplifier. The gain is internally fixed at 36 dB . Speaker audio leaves U 450 on pins 11 and 13. For dash-mount models, the audio is routed to the speaker via P503, pins14 and 16. The amplifier is biased to one half of the A+ voltage and connected directly to the speaker from the rear accessory connector (J2, pins 6 and 7). The speaker outputs must NOT be grounded in any way. An audio isolation transformer must be used if grounded test equipment (such as a service monitor) is to be connected to the speaker outputs.

When the radio is squelched, the audio PA is disabled by the VOCON board's controller, providing a low output state to P501, pin 44 (speaker-enable input). The low input turns off Q401 and Q400, removing SWB+ voltage to the audio PA, U450. When U450, pin 10 does not have SW+B applied, the speaker is totally muted and the audio PA current drain is greatly reduced. Diode CR402 (not normally installed) is used when a vehicular repeater is installed and audio muting is required.

A second output for filtered receive audio is provided to drive accessory hardware. The output of P501, pin 49 (MOD IN/DISC AUDIO) is primarily used for transmitter modulation. In the receive mode, the digital signal processor (DSP), via ADSIC, outputs audio at a fixed level (approximately 800 mV pp). This output can be connected to the accessory connector (P503, pin 21) by selecting the appropriate jumper settings.

### 3.2.9 Microphone Audio

The mobile microphone connects to the front of the control head through connector P104. Microphone high audio enters the command board via P502, pin 6 and is routed to differential amplifier buffer U402. Resistors R414 and R415 provide 9.6-V bias voltage for the microphone's internal circuitry. Amplifier U402 pre-emphasizes and limits the incoming microphone audio through components C462, R407, C463, and R408, which perform an active filter function. Components R441, R442, C467, C465, R443, C466, and C568 provide de-emphasis, developing the required clamped microphone audio, referred to as "mic audio in" (MAI).

### 3.2.10 Transmit Deviation

The analog transmit deviation (MAI) enters the VOCON board through P501, pin 39, and is converted to a digital format. The digital representation is processed and pre-emphasized by the DSP processor. The pre-emphasized digital bit stream is converted back to analog by the ADSIC device.

The modulation enters the command board through P501, pin 49 (MOD IN) and P501, pin 48 (REF MOD). The two audio signals are required to compensate for low-frequency non-linearities caused by the loop filter in the VCO. The two transmit modulation signals enter a buffer (U401, pin 5 and U401, pin 3). The outputs are sent to a multiplex gate (U523), used to disable the outputs during the receive mode. The multiplex gate is controlled by the serial shift register (U530), and the control lines (U530, pins 10 and 11) are pulled low in the transmit mode.

The modulation is sent out on U530, pins 14 (MOD IN) and 15 (REF MOD). Modulation from U530, pin 14, is coupled through R400 to a non-inverting amplifier, U401. Resistors R403 and R437 fix the closed-loop output gain to 4. Modulation from U530, pin 15 is coupled through R420 to the second non-inverting amplifier, U401. Resistors R422 and R438 fix the closed-loop output gain to 6 . The amplified modulation leaves the command board through J500, pins 11 and 17 , and is routed to the RF board to provide the transmit modulation.

### 3.2.11 RS-232 Line Driver

The U526 device is a driver/receiver IC, capable of interfacing with external hardware that utilizes the RS-232 protocol. The device includes an internal oscillator, a voltage doubler, a voltage inverter, and a level shifter. The IC is sourced by +5 V and outputs digital signals at voltage levels of $\pm 10 \mathrm{Vdc}$.

The device accepts incoming RS-232 data and converts it to a 5-V logic level. The command board jumper default settings are arranged to have the RS-232 driver normally connected to the accessory outputs, except when ordered as Motorcycle models.

### 3.2.12 Flash Programming

The command board provides multiplexing of the receive and transmit data inputs from the control head's microphone connector (P104). The microphone connector is used (during certain conditions) as a Flash programming input port. When the special programming cable is inserted into P104, the "microphone high" line (normally 9.6 V ) increases to 13 V , due to internal connections made within the radio interface box (RIB). Zener diode VR401 (and resistor R519), connected to the "Mic Hi" input (P502, pin 6), is forward-biased beyond its breakdown voltage of 11 Vdc . The voltage drop across R516 forward-biases Q401, turning on the transistor. The collector of Q401 pulls the voltage provided by R521 to ground. The change in state causes the multiplex control line (U525, pins 9, 10, 11) to change the gate inputs. The change allows the receive and transmit data paths to be multiplexed to P502, pin 23 (Key Fail), P502, pin 15 (P_RX data), and P502, pin 2 (PTT*/P reset).

### 3.2.13 Encryption Voltages

The command board produces two voltages that are used by the encryption module: $10-\mathrm{V}(9-\mathrm{V}$ on G and earlier boards) constant and $5-\mathrm{V}$ key storage. The constant 10 V is generated using components U604, R608, R609, and C605 (R420, VR403, C457, and Q508 on G and earlier boards) and is fed to pin 38 of P501. On the VOCON board, the 10 V provides continuous unswitched voltage when the vehicular battery is connected to the radio and is also switched via VOCON transistors Q206 and Q207 to provide SWB+ to the encryption module. A 5-V storage circuit comprised of components R532, R533, and C571 ( 0.47 farad capacitor) provides +5 Vdc to the encryption module via P501 pin 36 to hold encryption keys for a period of three days with no A+ voltage present. Provision is made for a battery holder to replace capacitor C571. The addition of the battery will increase encryption key hold time to approximately one year.

### 3.2.14 Regulator and Power-Control IC

The regulator and power-control IC (RPCIC), U500, contains internal circuitry for the $9.6-\mathrm{V}$ regulator and the switched $+5-\mathrm{V}$ regulator. Refer to Section 3.2.4, "Regulators," on page $3-10$ for detailed theory of operation.

The power-control section of the device is responsible for maintaining a constant RF output power. A directional coupler and detector network, located within the RF power amplifier circuit, rectifies the sensed forward power from the last RF gain stage. The detected voltage is routed back to the command board control circuitry (U500) via P503, pin 8. The voltage is then coupled through a buffer amplifier and summed, through a resistor network (R509, R508, and R507), with the transmit power set voltage (U500, pin 6) and the temperature sense voltage. The resulting voltage is applied to the control amplifier's inverting port (U502, pin 2) for automatic RF gain control.

The U500 current-sense inputs, pin 37 (sense +) and pin 38 (sense -), are sourced from the currentsensing resistor on the RF power amplifier. The two inputs are applied to a differential amplifier internal to the RPCIC. The current limit is set by a software-programmable D/A device (U502) that causes a cut back in RF output power when the set limit is exceeded.

The transmitter attack and off times are software programmable to meet domestic and international specifications. Transistors Q514 and Q515 are controlled by a serial shift register (U530). The transistors, when turned on (logic 1 input) cause the output of Q504 (the PA control line) to ramp up slowly to prevent an abrupt RF PA turn-on. The slower rate is required to meet international spurious requirements. When the transistors are turned off, the attack times return to a standard domestic response with fast rise times. Refer to Figure 3-6 for attack time diagrams.


MAEPF-25186-O
Figure 3-6. Transmitter Attack Time

### 3.3 ASTRO Spectra VOCON Board

This section of the theory of operation provides a detailed circuit description of an ASTRO Digital Spectra Vocoder/Controller (VOCON) Board. When reading the Theory of Operation, refer to your appropriate schematic and component location diagrams located in "Chapter 7. Schematics, Component Location Diagrams, and Parts Lists". This detailed Theory of Operation will help isolate the problem to a particular component. However, first use the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual to troubleshoot the problem to a particular board.

NOTE: The information in this subsection applies to the non Plus VOCON Board. Refer to Section 3.4, "ASTRO Spectra Plus VOCON Board," on page 3-38 for information on the ASTRO Spectra Plus VOCON board.

### 3.3.1 General

The VOCON board consists of two subsystems; the vocoder and the controller. Although these two subsystems share the same printed circuit board and work closely together, it helps to keep their individual functionality separate in describing the operation of the radio.

The controller section is the central interface between the various subsystems of the radio. It is very similar to the digital logic portion of the controllers on many existing Motorola radios. Its main task is to interpret user input, provide user feedback, and schedule events in the radio operation, which includes programming ICs, steering the activities of the DSP, and sending messages to the display through the control head.
The vocoder section performs all tone signaling, trunking signalling, conventional analog voice, etc. All analog signal processing is done digitally utilizing a DSP56001. In addition it provides a digital voice plus data capability utilizing VSELP or IMBE voice compression algorithms. Vocoder is a general term used to refer to these DSP based systems and is short for voice encoder.

In addition, the VOCON board provides the interconnection between the microcontroller unit (MCU), digital-signal processor (DSP), command board, and encryption board on secure-equipped radios.

### 3.3.2 Controller Section

Refer to Figure 3-7 and your specific schematic diagram.
The controller section of the VOCON board consists entirely of digital logic comprised of a microcontrol unit (MCU-U204), a custom support logic IC (SLIC-U206), and memory consisting of: SRAM (U202), EEPROM (U201), and FLASH ROM (U205).

The MCU (U204) memory system is comprised of a $32 \mathrm{k} \times 8$ SRAM (U202), 32k x 8 EEPROM (U201), and 512k x 8 FLASH ROMs (U205). The MCU also contains 1024 bytes of internal SRAM and 512 bytes of internal EEPROM. The EEPROM memory is used to store customer specific information and radio personality features. The FLASH ROM contains the programs which the HC11F1 executes. The FLASH ROM allows the controller firmware to be reprogrammed for future software upgrades or feature enhancements. The SRAM is used for scratchpad memory during program execution.

The SLIC (U206) performs many functions as a companion IC for the MCU. Among these are expanded input/output (I/O), memory decoding and management, and interrupt control. It also contains the universal asynchronous receiver transmitter (UART) used for the RS232 data communications. The SLIC control registers are mapped into the MCU (U204) memory space.


MAEPF-25105-O
Figure 3-7. VOCON Board - Controller Section
The controller performs the programming of all peripheral ICs. This is done via a serial peripheral interface (SPI) bus. ICs programmed through this bus include the synthesizer prescaler, DAIC, and ADSIC. On secure-equipped model, the encryption board is also controlled through the SPI bus.
In addition to the SPI bus, the controller also maintains two asynchronous serial buses; the SB9600 bus and an RS232 serial bus. The SB9600 bus is for interfacing the controller section to different hardware option boards, some of which may be external to the radio. The RS232 is used as common data interface for external devices.

User input from the control head is sent to the controller via the SB9600 bus. Feedback to the user is provided by the display on the control head. The display is 2 line 14 characters on the W3 model, 8 characters on W4, W5, and W7 models, and 11 characters on the W9 model.

The controller schedules the activities of the DSP through the host port interface. This includes setting the operational modes and parameters of the DSP. The controlling of the DSP is analogous to programming analog signaling ICs on standard analog radios.

### 3.3.3 Vocoder Section

Refer to Figure 3-8 and your specific schematic diagram.
The vocoder section of the VOCON board is made up of a digital signal processor (DSP) (U405), 24k x24 static-RAM (SRAM) (U414, U403, and U402), 256kB FLASH ROM (U404), and ABACUS II/DSP support IC (ADSIC) (U406).

The FLASH ROM (U404) contains the program code executed by the DSP. As with the FLASH ROM used in the controller section, the FLASH ROM is reprogrammable so new features and algorithms can be updated in the field as they become available. Depending on the mode and operation of the DSP, corresponding program code is moved from the FLASH ROM into the faster SRAM, where it is executed at full bus rate.
The ADSIC (U406) is basically a support IC for the DSP. It provides among other things, the interface from the digital world of the DSP to the analog world. The ADSIC also provides some memory management and provides interrupt control for the DSP processing algorithms. The configuration programming of the ADSIC is performed by the MCU. However some components of the ADSIC are controlled through a parallel memory mapped register bank by the DSP.
In the receive mode, The ADSIC (U406) acts as an interface to the ABACUS II IC, which can provide digital output of I (In phase) and Q (Quadrature) data words directly to the DSP for processing. Or the data can be filtered and discriminated by the ADSIC and data provided to the DSP as raw discriminator sample data. The latter mode, with the ADSIC performing the filtering and discrimination, is the typical mode of operation.

In the transmit mode, the ADSIC (U406) provides a serial digital-to-analog (D/A) converter. The data generated by the DSP is filtered and reconstructed as an analog signal to present to the VCO and Synthesizer as a modulation signal. Both the transmit and receive data paths between the DSP and ADSIC are through the DSP SSI port.

When transmitting, the microphone audio is passed from the command board to the ADSIC, which incorporates an analog-to-digital (A/D) converter to translate the analog waveform to a series of data. The data is available to the DSP through the ADSIC parallel registers. In the converse way, the DSP writes speaker data samples to a D/A in the ADSIC, which provides an analog speaker audio signal to the audio power amplifier on the command board.


Figure 3-8. VOCON Board - Vocoder Section

### 3.3.4 RX Signal Path

The vocoder processes all received signals digitally. This requires a unique back end from a standard analog radio. This unique functionality is provided by the ABACUS II IC with the ADSIC (U406) acting as the interface to the DSP. The ABACUS II IC located on the RF board provides a digital back-end for the receiver section. It provides a digital output of I (In phase) and Q (Quadrature) data words at 20 kHz sampling rate through the ADSIC interface to the DSP. Refer to the appropriate transceiver section for details on ABACUS II operation.

The ADSIC interface to the ABACUS II is comprised of the four signals SBI, DIN, DIN*, and ODC (refer to Figure 3-9).


MAEPF-25107-O
Figure 3-9. DSP RSSI Port - RX Mode
NOTE: An asterisk symbol (*) next to a signal name indicates a negative or NOT logic true signal.
ODC is a clock ABACUS II provides to the ADSIC. Most internal ADSIC functions are clocked by this ODC signal at a rate of 2.4 MHz and is available as soon as power is supplied to the circuitry. This signal may initially be 2.4 or 4.8 MHz after power-up. It is programmed by the ADSIC through the SBI signal to 2.4 MHz when the ADSIC is initialized by the MCU through the SPI bus. For any functionality of the ADSIC to exist, including initial programming, this reference clock must be present. SBI is a programming data line for the ABACUS II. This line is used to configure the operation of the ABACUS II and is driven by the ADSIC. The MCU programs many of the ADSIC operational features through the SPI interface. There are 36 configuration registers in the ADSIC of which four contain configuration data for the ABACUS II. When these particular registers are programmed by the MCU, the ADSIC in turn sends this data to the ABACUS II through the SBI.
DIN and DIN* are the data lines on which the I and Q data words are transferred from the ABACUS II. These signals make up a differentially encoded current loop. Instead of sending TTL type voltage signals, the data is transferred by flowing current one way or the other through the loop. This helps to reduce internally generated spurious emissions on the RF board. The ADSIC contains an internal current loop decoder which translates these signals back to TTL logic and stores the data in internal registers.

In the fundamental mode of operation, the ADSIC transfers raw baseband data to the DSP. The DSP can perform IF filtering and discriminator functions on this data to obtain a baseband demodulated signal. However, the ADSIC contains a digital filter and discriminator function and can provide this baseband demodulated signal directly to the DSP, this being the typical mode of operation. The internal digital IF filter is programmable up to 24 taps. These taps are programmed by the MCU through the SPI interface.

The DSP accesses this data through its SSI port. This is a 6 port synchronous serial bus. It is used by the DSP for both transmit and receive data transferal, but only the receive functions will be discussed here. The ADSIC transfers the data to the DSP on the SRD line at a rate of 2.4 MHz . This is clocked synchronously by the ADSIC which provides a 2.4 MHz clock on SCO. In addition, a 20 kHz interrupt is provided on SC1 signaling the arrival of a data packet. This means a new I and Q sample data packet is available to the DSP at a 20 kHz rate which represents the sampling rate of the received data. The DSP then processes this data to extract audio, signaling, etc. based on the 20 kHz interrupt.
In addition to the SPI programming bus, the ADSIC also contains a parallel configuration bus consisting of D8-D23, A0-A2, A13-A15, RD* and $\mathrm{WR}^{*}$, This bus is used to access registers mapped into the DSP memory starting at Y:FFFO. Some of these registers are used for additional ADSIC configuration controlled directly by the DSP. Some of the registers are data registers for the speaker D/A. Analog speaker audio is processed through this parallel bus where the DSP outputs the speaker audio digital data words to this speaker D/A and an analog waveform is generated which is output on SDO (Speaker Data Out). In conjunction with the speaker D/A, the ADSIC contains a programmable attenuator to set the rough signal attenuation. However, the fine levels and differences between signal types is adjusted through the DSP software algorithms. The speaker D/A attenuator setting is programmed by the MCU through the SPI bus.

The ADSIC provides an 8 kHz interrupt to the DSP on IRQB for processing the speaker data samples. IRQB is also one of the DSP mode configuration pins at start up. This 8 kHz signal must be enabled through the SPI programming bus by the MCU and is necessary for any audio processing to occur.

For secure messages, the digital signal data must be passed to the secure module for decryption prior to processing speaker data. The DSP transfers the data to and from the secure module through it's SCI port consisting of TXD and RXD. The SCI port is a two wire duplex asynchronous serial port. Configuration and mode control of the secure module is performed by the MCU through the SPI bus.

The ADSIC presents the analog speaker audio to the command board's audio power amplifier., which drives an external speaker. For more information on this subject, refer to Section 3.2, "Command Board," on page 3-8.

Since all of the audio and signaling is processed in DSP software algorithms, all types of audio and signalling follow this same path. There is, however, one exception. Low-speed trunking data is processed by the host $\mu \mathrm{C}$ through the SCLK port of the DSP. This port is connected to port PA0 on the host $\mu \mathrm{C}$. The DSP extracts the low-speed data from the received signal and relays it to the host $\mu \mathrm{C}$ for processing.

### 3.3.5 TX Signal Path

The transmit signal path follows some of the same design structure as the receive signal path described in Section 3.3.4, "RX Signal Path," on page 3-18 (refer to Figure 3-10). It is advisable to read through the section on RX Signal Path that precedes this section.


MAEPF-25108-O
Figure 3-10. DSP RSSI Port - TX Mode
The ADSIC contains a microphone A/D with a programmable attenuator for coarse level adjustment. As with the speaker D/A attenuator, the microphone attenuator value is programmed by the MCU through the SPI bus. The analog microphone signal from the command board is input to the A/D on MAI (Mic Audio In). The microphone A/D converts the analog signal to a digital data stream and stores it in internal registers. The DSP accesses this data through the parallel configuration bus consisting of D8-D23, A0-A2, A13-A15, RD*, and WR*. As with the speaker data samples, the DSP reads the microphone samples from registers mapped into it's memory space starting at Y:FFF0. The ADSIC provides an 8 kHz interrupt to the DSP on IRQB for processing these microphone data samples.

As with the received trunking low-speed data, low speed Tx data is processed by the MCU and returned to the DSP at the DSP SCLK port connected to the MCU port PAO.
For secure messages, the digital signal may be passed to the secure module for encryption prior to further processing. The DSP transfers the data to and from the secure module through its SCI port, consisting of TXD and RXD. Configuration and mode control of the secure module is performed by the MCU via the SPI bus.

The DSP processes these converted microphone samples, generates and mixes the appropriate signalling, and filters the resultant data. This data is then transferred to the ADSIC IC on the DSP SSI port. The transmit side of the SSI port consists of SC2, SCK, and STD. The DSP SSI port is a synchronous serial port. SCK is the 1.2 MHz clock input derived from the ADSIC, which makes it synchronous. The data is clocked over to the ADSIC on STD at a 1.2 MHz rate. The ADSIC generates a 48 kHz interrupt on SC2 so that a new sample data packet is transferred at a 48 kHz rate which sets the transmit data sampling rate at 48 Ksp .

These samples are then input to a transmit D/A, which converts the data to an analog waveform. This waveform is the modulation out signal from the ADSIC ports, VVO and VRO. These signals are both sent to the command board, where they go through a gain stage and then to the VCO and Synthesizer. VVO is used primarily for audio frequency modulation; VRO is used to compensate for low-frequency response to pass Digital Private Line (DPL) modulated signals. The transmit side of the transceiver is virtually identical to a standard analog FM radio.

Also required is the 2.4 MHz ODC signal from the ABACUS II IC. Although the ABACUS II IC provides receiver functions, it is important to note that this 2.4 MHz reference is required for all of the ADSIC operations.

### 3.3.6 Controller Bootstrap and Asynchronous Buses

The SB9600 bus (see Figure 3-11) is an asynchronous serial communication bus, utilizing a Motorola proprietary protocol. It provides a means for the MCU to communicate with other hardware devices. In the ASTRO Digital Spectra radio, it communicates with hardware accessories connected to the accessory connector and the remote interface board.

The SB9600 bus utilizes the UART internal to the MCU, operating at 9600 baud. The SB9600 bus consists of LH/TX_Data (J501-18), LH/RX_Data (J501-17), and Busy_RTS (J501-20) signals. LH/TX_Data and LH/RX_Data are the SCI TXD and RXD ports (U204-PD0 and PD1), respectively. Busy_RTS (U204-PA3) is an active-low signal, which is pulled low when a device wants control of the bus.

The same UART internal to the MCU is used in the controller bootstrap mode of operation. This mode is used primarily in downloading new program code to the FLASH ROMs on the VOCON board. In this mode, the MCU accepts special code downloaded at 7200 baud through the SCI bus instead of operating from program code resident in its ROMs.


Figure 3-11. Host SB9600 and RS232 Ports
A voltage greater than 10 Vdc applied to $\mathrm{J} 501-31(\mathrm{Vpp})$ will trip the circuit comprising Q203, Q204, and VR207. This circuit sets the MODA and MODB pins of the MCU to bootstrap mode (logic 0,0 ). If the Vpp voltage is raised to 12 Vdc required on the FLASH devices for programming, the circuit comprising VR208, Q211, and Q208 will trip, supplying Vpp to the FLASH devices, U205 and U404.

The ASTRO Digital Spectra radio has an additional asynchronous serial bus which utilizes RS232 bus protocol. This bus utilizes the UART in the SLIC IC (U206). It consists of TX/RS232 (J501-43), RX/RS232 (J501-50), CTS/RS232 (J501-5), and RTS/RS232 (J501-42). It is a four-wire duplex bus used to connect to external data devices.

### 3.3.7 Vocoder Bootstrap

The DSP has two modes of bootstrap: from program code stored in the FLASH ROM U404, or retrieving code from the host port.

During normal modes of operation, the DSP executes program code stored in the FLASH ROM, U404. Unlike the MCU, however, the DSP moves the code from the FLASH ROM into the three SRAMs, U402, U403, and U414, where it is executed from. Since, at initial start-up, the DSP must execute this process before it can begin to execute system code, it is considered a bootstrap process. In this process, the DSP fetches 512 words, 1536 bytes, of code from the FLASH ROM, starting at physical address \$C000, and moves it into internal P memory. This code contains the system vectors, including the reset vector. It then executes this piece of bootstrap code, which basically in turn moves additional code into the external SRAMs.

A second mode of bootstrap allows the DSP to load this initial 512 words of data from the host port, being supplied by the MCU. This mode is used for FLASH programming the DSP ROM when the ROM may initially be blank. In addition, this mode may be used for downloading some diagnostic software for evaluating that portion of the board.

The bootstrap mode for the DSP is controlled by three signals; MODA/IRQA*, MODB/IRQB*, and D23. All three of these signals are on the DSP (U405). MODA and MODB configure the memory map of the DSP when the DSP reset become active. These two signals are controlled by the ADSIC (U406) during power-up, which sets MODA low and MODB high for proper configuration. Later these lines become interrupts for analog signal processing. D23 controls whether the DSP will look for code from the MCU or will retrieve code from the FLASH ROM. D23 by default is pulled high through R404 which will cause the DSP to seek code from the FLASH ROM (U404) if this line is read high out of reset. This line is also connected to an I/O port on the MCU which can configure it for the second, host port, mode of bootstrap.

### 3.3.8 Serial Peripheral Interface (SPI) Bus

This bus is a synchronous serial bus made up of a data, a clock, and an individual IC unique select line. It's primary purpose is to configure the operating state of each IC. ICs programmed by this include; ADSIC, Synthesizer, Prescaler, DAIC, and, if equipped, the secure module.

The MCU (U204) is configured as the master of the bus. It provides the synchronous clock (SPI_SCK), a select line, and data (MOSI [Master Out Slave In]). In general the appropriate select line is pulled low to enable the target IC and the data is clocked in. The SPI bus is a duplex bus with the return data being clocked in on MISO (Master In Slave Out). The only place this is used is when communicating with the secure module. In this case, the return data is clocked back to the MCU on MISO (master in slave out).

### 3.3.9 Controller Memory Map

Figure 3-12 depicts the controller section memory map for the parallel data bus as used in normal modes of operation. There are three maps available for normal operation, but map 2 is the only one used. In bootstrap mode, the mapping is slightly different and will be addressed later.

The external bus for the host controller (U204)) consists of one 32Kx8 SRAM (U202), one 32Kx8 EEPROM (U201), one IMEG FLASH ROM U205, and SLIC (U206) configuration registers. In addition the DSP host port is mapped into this bus through the SLIC address space. The purpose of this bus is to interface the MCU (U204) to these devices

SLIC III REGISTER \$1400-\$14FF $\square$ COMMON ROM

RAM
F1 REGISTERS

EXTERNAL EEPROM CONTROLLED BY F1
INT RAM: \$1060-\$13FF
INT EE: \$0E00-\$0FFF REGISTERS: $\$ 1000-\$ 105 F$

Figure 3-12. Controller Memory Mapping

The MCU executes program code stored in the FLASH ROMs. On a power-up reset, it fetches a vector from \$FFFE, \$FFFF in the ROMs and begins to execute code stored at this location. The external SRAM along with the internal 1Kx8 SRAM is used for temporary variable storage and stack space. The internal 512 bytes of EEPROM along with the external EEPROM are used for non volatile storage of customer-specific information. More specifically the internal EEPROM space contains transceiver board tuning information and on power-down some radio state information is stored in the external EEPROM.

The SLIC is controlled through sixteen registers mapped into the MCU memory at \$1400-\$14FF. This mapping is achieved by the following signals from the MCU: R/W*, CSIO1*, HA0-HA4,HA8, HA9. Upon power-up, the MCU configures the SLIC including the memory map by writing to these registers.
The SLIC memory management functions in conjunction with the chip selects provided by the MCU provide the decoding logic for the memory map which is dependent upon the "map" selected in the SLIC. The MCU provides a chip select, CSGEN*, which decodes the valid range for the external SRAM. In addition CSIO1* and CSPROG* are provided to the SLIC decoding logic for the external EEPROM and FLASH ROM respectively. The SLIC provides a chip select and banking scheme for the EEPROM and FLASH ROM. The FLASH ROM is banked into the map in 16KB blocks with one 32 KB common ROM block. The external EEPROM may be swapped into one of the banked ROM areas. This is all controlled by EE1CS*, ROM1CS*, ROM2CS*, HA14_OUT, HA15_OUT, HA16, and HA17 from the SLIC (U206) and D0-D8 and A0-16 from the MCU (U204).
The SLIC provides three peripheral chip selects; XTSC1B, XTCS2B, and XTCS3B. These can be configured to drive an external chip select when its range of memory is addressed. XTSC1B is used to address the host port interface to the DSP. XTSC2B is used to address a small portion of external SRAM through the gate U211. XTSCB3 is used as general purpose I/O for interrupting the secure module.

In bootstrap mode the memory map is slightly different. Internal EEPROM is mapped at \$FE00\$FFFF and F1 internal SRAM starts at $\$ 0000-\$ 03 F F$. In addition, a special bootstrap ROM appears in the ROM space from $\$$ B600-\$BFFF. For additional information on bootstrap mode, refer to Section 3.3.6, "Controller Bootstrap and Asynchronous Buses," on page 3-22.

### 3.3.10 Vocoder Memory Map

The vocoder (DSP) external bus consists of three 32k x 8 SRAMs (U401, U402, and U403), one 256k x 8 FLASH ROM (U404), and ADSIC (U406) configuration registers. Refer to Figure 3-13.
The DSP56001A (U405) has a 24 bit wide data bus (D0-D23) and a 16 bit wide address bus (A0 - A15). The DSP can address three $64 \mathrm{k} \times 24$ memory spaces: P (Program), Dx (Data X), and Dy (Data Y). These additional RAM spaces are decoded using PS* (Program Strobe), DS* (Data Strobe), and $X / Y^{*}$. RD* and $W R^{*}$ are separate read and write strobes.

The ADSIC provides memory decoding for the FLASH ROM (U404). EPS* provides the logic:

## $\overline{\mathrm{A} 15 \times(\mathrm{A} 14 \oplus \mathrm{~A} 13)}$

and is used as a select for the ROM. The ADSIC provide three bank lines for selecting 16k byte banks from the ROM. This provides decoding for 128k bytes from the ROM in the P: memory space. PS* is used to select A17 to provide an additional 128k bytes of space in Dx: memory space for the ROM.


Figure 3-13. Vocoder Memory Mapping
The ADSIC internal registers are decoded internally and start at \$E000 in Dy:. These registers are decoded using A0-A2, A13-A15, and PS* from the DSP. The ADSIC internal registers are 16 bits wide, so only D8-D23 are used.

The DSP program code is stored in the FLASH ROM, U404. During normal modes of operation, the DSP moves the appropriate program code into the three SRAMs (U401, U402, and U403) and internal RAM for execution. The DSP never executes program code from the FLASH ROM itself. At power-up after reset, the DSP downloads 512 words (1536 bytes) from the ROM, starting at \$C000, and puts it into the internal RAM, starting at $\$ 0000$, where it is executed. This segment of program code contains the interrupt vectors and the reset vector, and is basically an expanded bootstrap code. When the MCU messages the DSP that the ADSIC has been configured, the DSP overlays more code from the ROM into external SRAM and begins to execute it. Overlays occur at different times when the DSP moves code from the ROM into external SRAM, depending on immediate mode of operation, such as changing from transmit to receive.

### 3.3.11 MCU System Clock

The MCU (U701) system clock is provided by circuitry internal to the MCU and is based on the crystal reference, Y 100 . The nominal operating frequency is 7.3728 MHz . This signal is available as a clock at 4XECLK on U701 and is provided to the SLIC (U702) for internal clock timing. The MCU actually operates at a clock rate of $1 / 4$ the crystal reference frequency or 1.8432 MHz . This clock is available at ECLK on U701.

The MCU clock contains a crystal warp circuit comprised of L120, Q102, and C162. This circuit is controlled by an I/O port (PA6) on the MCU. This circuit moves the operating frequency of the oscillator about 250ppM on certain receive channels to prevent interference from the MCU bus noise.

### 3.3.12 DSP System Clock

The DSP (U405) system clock, DCLK, is provided by the ADSIC (U406). It is based off the crystal reference, Y401, with a nominal operating frequency of 33.0000 MHz . The ADSIC contains an internal clock-divider circuit that can divide the system clock from 33 MHz to 16.5 MHz or 8.25 MHz operation. The DSP controls this divider by writing to the ADSIC parallel registers. The frequency is determined by the processes the DSP is running and, to reduce system power consumption, is generally configured to the slowest operating speed possible.
The additional circuitry of CR402, L401, C416, C417, C419, and C422 make up a crystal warp circuit. This circuit is controlled by the OSCw signal from ADSIC, which is configured by the host through the SPI bus. The crystal warp circuit moves the operating frequency of the oscillator about 400 ppM on certain receive channels to prevent interference from the DSP bus noise.

### 3.3.13 Radio Power-Up/Power-Down Sequence

Radio power-up begins when the user closes the radio on/off switch on the control top, placing 7.5 Vdc on the B+_SENSE line. This signal enables the pass element Q106 through Q105, enabling SW_B+ to the controller board and the transceiver board. B+_SENSE also enables the +5 Vdc regulator, U709. When +5 Vdc has been established, it is sensed by the supervisory IC, U726, which disables the system reset through the delay circuit R208 and C214.

When the MCU comes out of reset, it fetches the reset vector in ROM at \$FFFE, \$FFFF and begins to execute the code this vector points to. It configures the SLIC through the parallel bus registers. Among other things it enables the correct memory map for the MCU. It configures all the transceiver devices on the SPI bus. The MCU then pulls the ADSIC out of reset and, after a minimal delay, the DSP also. It then configures the ADSIC via the SPI bus, configuring, among other things, the DSP memory map. While this is happening, the DSP is fetching code from ROM U404 into internal RAM and beginning to execute it. It then waits for a message from the MCU that the ADSIC has been configured, before going on.

During this process, the MCU does power diagnostics. These diagnostics include verifying the MCU system RAM, and verifying the data stored in the internal EEPROM, external EEPROM, and FLASH ROMs. The MCU queries the DSP for proper status and the results of DSP self tests. The DSP self tests include testing the system RAM, verifying the program code in ROM U404, and returning the ADSIC configuration register checksum. Any failures cause the appropriate error codes to be sent to the display. If everything is OK, the appropriate radio state is configured and the unit waits for user input.
On power-down, the user opens the radio on/off switch, removing the B+_SENSE signal from the controller board. This does not immediately remove power, as the MCU holds this line active through B+_CNTL. The MCU then saves pertinent radio status data to the external EEPROM. Once this is done, $\mathrm{B}_{+}$CNTL is released, shutting off SW_B+ at Q106 and shutting down the $5-\mathrm{Vdc}$ regulator U709. When the regulator slumps to about 4.7 Vdc , supervisory IC U726 activates a system reset to the SLIC, which in turn resets the MCU.

### 3.3.14 VOCON BOARD Signals

Due to the nature of the schematic-generating program, signal names must be different when they are not directly connected to the same point. The following tables provide a cross-reference to the various pinouts for the same functional signal.

Table 3-2. VOCON Board Address Bus (A) Pinouts

| Bus | U402 | U403 | U404 | U405 | U406 | U414 | U415 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| A0 | A4 | A4 | 20 | C2 | E9 | A4 | -- |
| A1 | B4 | B4 | 19 | D3 | E10 | B4 | -- |
| A2 | A3 | A3 | 18 | D2 | E8 | A3 | -- |
| A3 | B3 | B3 | 17 | E2 | -- | B3 | -- |
| A4 | A2 | A2 | 16 | D4 | -- | A2 | -- |
| A5 | B2 | B2 | 15 | B1 | -- | B2 | -- |
| A6 | J6 | J6 | 14 | E3 | -- | J6 | -- |
| A7 | K7 | K7 | 13 | F1 | -- | K7 | -- |
| A8 | J7 | J7 | 3 | F2 | -- | J7 | -- |
| A9 | K8 | K8 | 2 | F3 | -- | K8 | -- |
| A10 | B8 | B8 | 31 | G1 | -- | B8 | -- |
| A11 | A8 | A8 | 1 | J2 | -- | A8 | -- |
| A12 | B7 | B7 | 12 | K1 | -- | B7 | -- |
| A13 | J3 | -- | 4 | H3 | D9 | -- | 2 |
| A14 | -- | -- | 5 | G2 | B9 | -- | 1 |
| A15 | K3 | K3 | 11 | H2 | D10 | J3 | -- |
|  |  |  |  |  |  |  |  |

Table 3-3. VOCON Board Address Bus (HA) Pinouts

| Bus | U201 | U202 | U204 | U205 | U206 | U210 | U405 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| HA0 | 13 | 10 | D2 | 20 | D7 | 20 | E9 |
| HA1 | 11 | 9 | C2 | 19 | C7 | 19 | F8 |
| HA2 | 10 | 8 | C1 | 18 | C8 | 18 | F9 |
| HA3 | 8 | 7 | D1 | 17 | D8 | 17 | -- |
| HA4 | 2 | 6 | E3 | 16 | E6 | 16 | -- |
| HA5 | 7 | 5 | E2 | 15 | -- | 15 | -- |
| HA6 | 6 | 4 | E1 | 14 | -- | 14 | -- |
| HA7 | 5 | 3 | E4 | 13 | -- | 13 | -- |
| HA8 | 27 | 25 | F1 | 3 | F6 | 3 | -- |
| HA9 | 12 | 24 | F3 | 2 | F7 | 2 | -- |
| HA10 | 24 | 21 | F2 | 31 | -- | 31 | -- |
| HA11 | 26 | 23 | G1 | 1 | -- | 1 | -- |
| HA12 | 4 | 2 | F4 | 12 | -- | 12 | -- |
| HA13 | 28 | 26 | G2 | 4 | -- | 4 | -- |
| HA14 | 3 | 1 | H1-In | 5 | H8-In <br> H4-Out | 5 | -- |
| HA15 | -- | -- | H2-In | 11 | H7-In <br> K3-Out | 11 | -- |
| HA16 | -- | -- | -- | 10 | K6 | 10 | -- |
| HA17 | -- | -- | -- | 6 | G5 | 6 | -- |

Table 3-4. VOCON Board Data Bus (D) Pinouts

| Bus | U402 | U403 | U404 | U405 | U406 | U414 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| D0 | B9 | B9 | 21 | G3 | -- | B9 |
| D1 | C8 | C8 | 22 | J1 | -- | C8 |
| D2 | C9 | C9 | 23 | K3 | -- | C9 |
| D3 | D9 | D9 | 25 | L3 | -- | D9 |
| D4 | E8 | E8 | 26 | J3 | -- | E8 |
| D5 | E9 | E9 | 27 | K4 | -- | E9 |
| D6 | F9 | F9 | 28 | H4 | -- | F9 |
| D7 | G9 | G9 | 29 | L2 | -- | G9 |
| D8 | G8 | G8 | -- | K2 | H10 | G8 |

Table 3-4. VOCON Board Data Bus (D) Pinouts (Continued)

| Bus | U402 | U403 | U404 | U405 | U406 | U414 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| D9 | H8 | H8 | -- | J4 | H9 | H8 |
| D10 | J9 | J9 | -- | K5 | H8 | J9 |
| D11 | J8 | J8 | -- | L5 | J8 | J8 |
| D12 | J2 | J2 | -- | J5 | L9 | J2 |
| D13 | J1 | J1 | -- | K6 | K8 | J1 |
| D14 | H2 | H2 | -- | J6 | L8 | H2 |
| D15 | G2 | G2 | -- | H7 | J7 | G2 |
| D16 | G1 | G1 | -- | L9 | K7 | G1 |
| D17 | F1 | F1 | -- | K8 | L7 | F1 |
| D18 | E1 | E1 | -- | K7 | J6 | E1 |
| D19 | E2 | E2 | -- | J7 | K6 | E2 |
| D20 | D1 | D1 | -- | L8 | J5 | D1 |
| D21 | C1 | C1 | -- | K10 | L6 | C1 |
| D22 | C2 | C2 | -- | J9 | L5 | C2 |
| D23 | B1 | B1 | -- | J10 | K5 | B1 |

Table 3-5. VOCON Board Data Bus (HD) Pinouts

| Bus | U201 | U202 | U204 | U205 | U206 | U210 | U405 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| HD0 | 14 | 1 | C6 | 21 | C3 | 21 | C7 |
| HD1 | 15 | 12 | B8 | 22 | B1 | 22 | B8 |
| HD2 | 16 | 13 | C7 | 23 | C2 | 23 | D7 |
| HD3 | 18 | 15 | D5 | 25 | D4 | 25 | A9 |
| HD4 | 19 | 16 | C8 | 26 | C1 | 26 | C9 |
| HD5 | 20 | 17 | D7 | 27 | D2 | 27 | C10 |
| HD6 | 21 | 18 | D6 | 28 | D3 | 28 | D8 |
| HD7 | 23 | 19 | D8 | 29 | D1 | 29 | C8 |

Table 3-6. U204 (MCU)

| U204 <br> Pin \# | Description | To/From |
| :---: | :---: | :---: |
| B1 | PE0 | R260 |
| B2 | PE1 B SENSE/LBAT/PWR DWN | VR214 |
| C3 | PE2 | N/C |
| A3 | PE3 EMERG | J901-4 |
| D3 | PE4 | N/C |
| A2 | PE5 | N/C |
| B3 | PE6 SPKR COMMON | R263 |
| C4 | PE7 EXT SPKR | R261 |
| B7 | 4XECLK (7.3726 MHz) | U206-A3 |
| J7 | PDO BOOT DATA IN (RXD) | J501-17 U206 |
| G6 | PD1 BOOT DATA OUT (TXD) | J501-18 U208 |
| H6 | PD2 MISO | J801-7 |
| J6 | PD3 MOSI | J501-9 J801-8 |
| G5 | PD4 SPI SCK | J501-8 J801-9 |
| H5 | PD5 DA SEL* | J501-13 |
| C5 | MOD A | Q204C |
| B5 | MOD B | Q204C |
| G3 | PAO SCLK | $\begin{aligned} & \text { U405-C6 } \\ & \text { U406-C9 } \end{aligned}$ |
| J2 | PA1 BOOT MODE | U405 |
| H3 | PA2 HREQ* | U405-B10 |
| J3 | PA3 SB9600 BUSY | J501-20 |
| G4 | PA4 IRQA* | $\begin{aligned} & \text { U406-F10 } \\ & \text { U405-H10 } \end{aligned}$ |
| H4 | PA5 BOOTSTRAP* | U206-E5 |
| J4 | PA6 ECLK SHIFT | Q205B |
| F5 | PA7 | N/C |
| E5 | RESET/RESET* | $\begin{aligned} & \text { U201-31 } \\ & \text { U206-E4 } \end{aligned}$ |
| E6 | PG7 CSPROG* | U206-E3 |
| F8 | PG6 CSGEN* | U211-1 |
| G8 | PG5 CS101* | U206-G1 |

Table 3-6. U204 (MCU) (Continued)

| U204 <br> Pin \# | Description | To/From |
| :--- | :--- | :--- |
| G7 | PG4 ADSIC RST* | U406-A8 |
| F7 | PG3 ADSIC SEL* | U406-B8 |
| H8 | PG2 DSP RST* | U405-G9 |
| F6 | PG1 ROSC/PSC CE* | J501-12 |
| H7 | PG0 SYN SEL* | J501-11 |
| B6 | R/W* | U405-D9 |
| A5 | ECLK (1.8432 MHz) | U206-A4 |
| E8 | XIRQ* | R233 |
| E7 | IRQ* | U206-E2 |
| A6 | EXTAL 7.3728 MHz | Y201 |
| A7 | XTAL | Q205C |

Table 3-7. U206 (SLIC)

| U206 <br> Pin \# | Description | To/From |
| :--- | :--- | :--- |
| F3 | PH0 | N/C |
| F4 | PH1 | N/C |
| F2 | PH2 | N/C |
| H1 | PH3 | N/C |
| G3 | PH4 | N/C |
| H2 | PH5 INT PTT* | J501-30 <br> U206-H2 |
| H3 | PH6 EMC REQ | J801-11 |
| K2 | PH7 LOCK DET* | U301-10 <br> CR51 |
| B4 | PJ0 MOB IRQ* | J501-26 |
| D5 | PJ1 VIP IN2 | J501-25 |
| A5 | RS232 DATA OUT | J501-43 |
| B6 | PJ3 CTSOUT* | J501-5 |

Table 3-7. U206 (SLIC) (Continued)

| U206 <br> Pin \# | Description | To/From |
| :---: | :---: | :---: |
| A6 | PJ4 | R268 |
| C6 | PJ5 OPT SEL2 (KEYLOAD*) | R237 |
| A7 | PJ6 VIP IN1 | J501-24 |
| D6 | PJ7 EMC EN* | J801-10 |
| C9 | POR* | U409-2 |
| E4 | HC11RST*/RESET* | $\begin{aligned} & \text { U204-E5 } \\ & \text { U201-31 } \end{aligned}$ |
| C4 | OE* | $\begin{aligned} & \text { U201-25 } \\ & \text { U202-22 } \\ & \text { U205-32 } \\ & \text { U210-32 } \end{aligned}$ |
| B3 | R/W* | $\begin{aligned} & \text { U405-D9 } \\ & \text { U204-B6 } \end{aligned}$ |
| E5 | BOOTSTRAP* | U204-H4 |
| A2 | MEM R/W* | $\begin{aligned} & \text { U201-29 } \\ & \text { U202-27 } \end{aligned}$ |
| E3 | $\mathrm{AV}^{*} / \mathrm{CSPROG} *$ | U204-E6 |
| G1 | CE*/CS101* | U204-G8 |
| G2 | SCNSLB | R252 |
| K5 | ROM1CS* | U205-30 |
| F5 | ROM2CS* | U210-30 |
| J4 | EE1CS* | U201-22 |
| J8 | KEYFAIL* | $\begin{aligned} & \mathrm{J} 801-15 \\ & \mathrm{~J} 501-21 \end{aligned}$ |
| B2 | RS232 DATA IN | J501-50 |
| J2 | BOOT DATA IN | $\begin{aligned} & \text { J501-17 U204- } \\ & \text { J7 } \end{aligned}$ |
| A3 | 4XECLK | U204-B7 |
| A4 | ECLK | U204-A5 |
| J3 | VIP OUT2 | J501-23 |
| G4 | SPKREN* | J501-44 |
| K8 | BUSY OUT* | J501-19 |
| G9 | TXPA EN* | J501-14 |
| F8 | 5V EN* | J501-15 |

Table 3-7. U206 (SLIC) (Continued)

| U206 <br> Pin \# | Description | To/From |
| :--- | :--- | :--- |
| G7 | MICEN | U501-45 <br> Q206B |
| J9 | B+ CNTL | J501-22 |
| E7 | VIP OUT1 | J801-12 |
| K7 | CS3B EMC MAKEUP* | U211-2 |
| G6 | CS2B RAM SEL* | U405-E8 |
| J7 | CS1B HEN* | J601-4 |
| G8 | DISP EN*/LATCH SEL* | N/C |
| H9 | RED LED | N/C |
| E8 | GRN LED | U204-E7 |
| E2 | IRQ* |  |

Table 3-8. VOCON U405 (DSP)

| U405 <br> Pin \# | Description | To/From |
| :--- | :--- | :--- |
| C1 | PS* $^{*}$ | DS* $^{*}$ |
| C3 | RD* $^{*}$ | U404-6 U406-D8 |
| A3 | WR* $^{*}$ |  |
| C4 | X/Y* $^{*}$ | U404-32 U406-F8 |
| B3 | BR $^{*} /$ BS* | U404-7 U406-G10 |
| A4 | MODA/IRQA $^{*}$ | R411 |
| B4 | XTAL | R432 |
| H10 | EXTAL | U204-G4 U406-F10 |
| H9 | STO | R415 |
| J8 | SRO | U406-G9 (DCLK) |
| K9 | SCK | U406-H1 |
| A2 | SC2 | U406-L3 |
| C5 | B6 | U406-G3 |
| B2 | U406-H2 |  |

Table 3-8. VOCON U405 (DSP) (Continued)

| U405 <br> Pin \# | Description | To/From |
| :--- | :--- | :--- |
| B5 | SC1 | U406-J4 |
| B9 | SC0 | U406-K4 |
| C6 | SCLK | U204-G3 <br> U406-C9 |
| A7 | TXD/EMC RXD | J801-3 |
| B7 | RXD/EMC TXD | J801-4 |
| G9 | RESET/DSP RST* | U204-H8 |
| E10 | HACK* | R409 |
| B19 | HREQ* | U204-H3 |
| E8 | HEN* | U206-J7 |
| D9 | HR/W* | U204-B6 |

Table 3-9. VOCON U406 (ADSIC)

| U406 <br> Pin \# | Description | To/From |
| :--- | :--- | :--- |
| D8 | PS* | U404-6 <br> U405-C1 |
| G10 | WR* | U405-C4 <br> U404-7 <br> U402/3/14-K2 |
| F8 | RD* | U405-A3 <br> U404-32 <br> U402/3/14-K6 |
| J9 | RSEL | U403-J3 <br> U414-K3 |
| G2 | TP1 | R407 |
| G1 | TP2 | N/C |
| A4 | AB1 | R402 |
| B8 | SEL*/ADSIC SEL* | U204-F7 |
| A8 | RST*/ADSIC RST* | U204-G7 |
| F10 | IRQA/IRQA* | U204-G4 <br> U405-H10 |
| F9 | IRQB/IRQB* 8 kHz | U405-H9 |

Table 3-9. VOCON U406 (ADSIC) (Continued)

| U406 Pin \# | Description | To/From |
| :---: | :---: | :---: |
| F2 | SSW/EPS* | U404-30 |
| C9 | SCLK/SPI SCK | $\begin{aligned} & \text { U204-G5 } \\ & \text { J501-8 } \\ & \text { J801-9 } \end{aligned}$ |
| C10 | SPO/MOSI | $\begin{aligned} & \text { J501-9 } \\ & \text { J801-8 } \end{aligned}$ |
| C1 | MA1 | U501-39 |
| B5 | SDO | U501-40 |
| B1 | VRO REFMOD | J501-48 |
| B2 | MODIN | J501-49 |
| L3 | RXD SRO 2.4 MHz | U405-C5 |
| J4 | RFS SC1 | U405-B5 |
| K4 | SCKR SCO | U405-B9 |
| H1 | TXD STO | U405-A2 |
| H2 | TFS SC2 48 kHz | U405-B2 |
| G3 | SCKT SCK 1.2 MHz | U405-B6 |
| C8 | DA4 BNK2 | U404-10 |
| C3 | DA7B BNK1 | U404-11 |
| B6 | DA7A BNK0 | U404-5 |
| J1 |  | N/C |
| J2 |  | N/C |
| K1 |  | N/C |
| K2 |  | N/C |
| H3 | DIN*/DOUT* | J501-1 |
| K3 | DIN/DOUT | J501-2 |
| F3 | IDC ODC 2.4 MHz | J501-7 |
| J3 | SBI | J501-6 |
| C7 | XTL 33 MHz | Y401 |
| C6 | EXTL | Y401 |
| K9 | OSC* | CR402 |
| G9 | DCLK | U405-K9 |

### 3.4 ASTRO Spectra Plus VOCON Board

This section of the theory of operation provides a detailed circuit description of an ASTRO Digital Spectra Plus Vocoder/Controller (VOCON) Board. When reading the Theory of Operation, refer to your appropriate schematic and component location diagrams located in "Chapter 7. Schematics, Component Location Diagrams, and Parts Lists" of this manual. This detailed Theory of Operation will help isolate the problem to a particular component. However, first use the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual to troubleshoot the problem to a particular board.

NOTE: The information in this subsection applies to the Plus VOCON Board. Refer to Section 3.3, "ASTRO Spectra VOCON Board," on page 3-15 for information on the ASTRO Spectra VOCON (non Plus) board.

### 3.4.1 General

The ASTRO Spectra Plus VOCON board consists of two subsystems; the vocoder and the controller. Although these two subsystems share the same printed circuit board and work closely together, it helps to keep their individual functionality separate in describing the operation of the radio. The controller section is the central interface between the various subsystems of the radio. It is very similar to the digital logic portion of the controllers on many existing Motorola radios. Its main task is to interpret user input, provide user feedback, and schedule events in the radio operation, which includes programming ICs (Integrated Circuits), steering the activities of the DSP (Digital Signal Processor), and sending messages to the display through the control head. The vocoder section performs functions previously performed by analog circuitry. This includes all tone signaling, trunking signaling, and conventional analog voice, etc. All analog signal processing is done digitally utilizing a DSP56600. In addition it provides a digital voice plus data capability utilizing IMBE voice compression algorithms. Vocoder is a general term used to refer to these DSP based systems and is short for voice encoder. In addition, the ASTRO Spectra Plus VOCON board provides the interconnection between the MCU (microcontroller unit), DSP, command board, and UCM (Universal Encryption Module) on secure-equipped radios.

### 3.4.2 ASTRO Spectra Plus Controller Section

Refer to Figure 3-14 and your specific schematic diagram located in Chapter 7.
The controller section of the ASTRO Spectra Plus VOCON board consists entirely of digital logic comprised of a microcontroller unit core (Patriot IC-U300), and memory consisting of: SRAM (U302), and FLASH ROM (U301). The Patriot IC is a dual-core processor that contains a DSP56600 core, a MCore 210 microcontroller core and custom peripherals. Note: When the Controller Section references the MCU, it will be referencing the Mcore 210 inside the Patriot IC (U300).

The MCU (U300) memory system is comprised of a $256 \mathrm{k} \times 16$ SRAM (U302) and a $2 \mathrm{M} \times 16$ FLASH ROM (U301). The MCU also contains $22.5 \mathrm{k} \times 32$ of internal SRAM. The FLASH ROM contains the programs that the Patriot IC executes, and is used to store customer specific information and radio personality features (i.e. codeplug information). The FLASH ROM allows the controller firmware to be reprogrammed for future software upgrades or feature enhancements. The SRAM is used for scratchpad memory during program execution.
The controller performs the programming of all peripheral ICs. This is done via a serial peripheral interface (SPI) bus, and through General Purpose Input/Outputs (GPIO) from the Patriot IC. ICs programmed through these interfaces include the Synthesizer, Prescaler, DAIC, and KRSIC (U200) and ADDAG (U201).

In addition to the SPI bus, the controller also maintains two asynchronous serial busses; the SB9600 bus and an RS232 serial bus. The SB9600 bus is for interfacing the controller section to different hardware option boards, some of which may be external to the radio. The RS232 is used as a common data interface for external devices.

User input from the control head is sent to the controller through SB9600 bus messages. Feedback to the user is provided by the display on the control head. The display is 2-line 14 characters on the W3 model, 8 characters on W4, W5, and W7 models; and 11 characters on the W9 model.

The controller schedules the activities of the DSP through the host port interface, which is internal to the Patriot IC (the MCU and DSP are both contained within the Patriot IC). This includes setting the operational modes and parameters of the DSP. The controlling of the DSP is similar to programming analog signaling ICs on standard analog radios.


Figure 3-14. ASTRO Spectra Plus VOCON Board - Controller Section

### 3.4.3 ASTRO Spectra Plus Vocoder Section

Refer to Figure 3-15 and your specific schematic diagram in Chapter 7.
The vocoder section of the ASTRO Spectra Plus VOCON board is made up of a digital signal processor (DSP) core, 84Kx24 Program RAM, 2Kx24 Program ROM, and 62Kx16 Data RAM, which are all integrated into the Patriot IC (U300). The vocoder also contains the KRSIC (U200) and ADDAG (U201).
The FLASH ROM (U301) contains both the program code executed by the DSP and the controller firmware. As with the FLASH ROM used in the controller section, the FLASH ROM is reprogrammable so new features and algorithms can be updated in the field as they become available. Depending on the mode and operation of the DSP, corresponding program code is moved from the FLASH ROM into the faster SRAM, where it is executed at the full bus rate.

The KRSIC and ADDAG IC's are the support IC's for the DSP. In the receive mode, the KRSIC (U200) acts as an interface to the ABACUS IC, which can provide data samples directly to the DSP for processing. In the transmit mode, the ADDAG (U201) provides a serial digital-to-analog (D/A) converter. The ADDAG (U201) also has a function in receive mode for special applications. The data generated by the DSP is filtered and reconstructed as an analog signal to present a modulation signal to the VCO (voltage-controlled oscillator). Both the transmit and receive data paths between the DSP and ADDAG are through the DSP SSI port.

When transmitting, the microphone audio is passed from the command board to the MC145483 CODEC (U402), which incorporates an analog-to-digital (A/D) converter to translate the analog waveform to a data stream. The data is made available to the DSP through the Serial Audio Port (SAP) of the Patriot IC. In the converse way, the DSP writes speaker data samples to a D/A in the CODEC (U402) through the SAP. The CODEC (U402) provides an analog speaker audio signal to the audio power amplifier on the command board.


Figure 3-15. ASTRO Spectra Plus VOCON Board - Vocoder Section

### 3.4.4 ASTRO Spectra Plus RX Signal Path

The vocoder processes all received signals digitally. This requires a unique back end from a standard analog radio. This unique functionality is provided by the ABACUS IC with the KRSIC (U200) acting as the interface to the DSP. The ABACUS IC located on the transceiver board provides a digital back-end for the receiver section. It provides a digital output of I (In phase) and Q (Quadrature) data words at a 20 kHz sampling rate (refer to the Receiver Back-End section for more details on ABACUS operation). This data is passed to the DSP through an interface with the KRSIC (U200) for appropriate processing. The KRSIC interface to the ABACUS is comprised of the four signals SBI, DIN, DIN*, and ODC (refer to Figure 3-16).


Figure 3-16. ASTRO Spectra Plus RX Mode
NOTE: An asterisk symbol (*) next to a signal name indicates a negative or NOT logic true signal.
ODC is a clock ABACUS provides to the KRSIC. Most internal KRSIC functions are clocked by this ODC signal at a rate of 2.4 MHz and is available as soon as power is supplied to the circuitry. This signal may initially be 2.4 or 4.8 MHz after power-up. It is programmed by the KRSIC through the SBI signal to 2.4 MHz when the KRSIC is initialized by the MCU (in the Patriot IC) through GPIO. SBI is a programming data line for the ABACUS. This line is used to configure the operation of the ABACUS and is driven by the KRSIC. The MCU programs many of the KRSIC operational features through the GPIO interface. When the KRSIC is programmed properly by the MCU, the KRSIC in turn sends this data to the ABACUS through the SBI.

DIN and DIN* are the data lines on which the I and Q data words are transferred from the ABACUS. These signals make up a differentially encoded current loop. Instead of sending TTL type voltage signals, the data is transferred by flowing current one way or the other through the loop. This helps to reduce internally generated spurious emissions on the RF board. There are single-ended driver circuits between the ABACUS and the KRSIC, which are used to convert the differential current driven by the ABACUS. After the driver circuits, the I and Q samples are detected and transferred to a serial transmitter.

The DSP accesses this data through its SSI port. The SSI port is used by the DSP for both transmit and receive data transferal, but only the receive functions will be discussed in this section. The KRSIC transfers the data to the DSP on the SRDB line at a rate of 1.2 MHz . This is clocked synchronously by the KRSIC which provides a 1.2 MHz clock on SCOB. In addition, a 20 kHz interrupt is provided on SC1B, signaling the arrival of a data packet. This means the I and Q sample data packets are available to the DSP at a 20 kHz rate which represents the sampling rate of the received data. The DSP then processes this data to extract audio, signaling, etc. based on the 20 kHz interrupt.
Speaker audio is processed by the DSP (in the Patriot IC), which outputs the audio data words to the speaker D/A inside the CODEC (U402), and an analog waveform is generated on the SDO (Speaker Data Out) line. In conjunction with the speaker D/A, the CODEC (U402) has the ability to attenuate the receive analog output, using three data bits which provide programmable attenuation to set the rough signal attenuation.

For secure messages, the digital signal data must be passed to the secure module for decryption prior to DSP processing of the speaker data. The DSP transfers the data to and from the secure module through it's SSI port consisting of TXD and RXD. The secure module communicates with the DSP through its SPI bus, therefore a SSI to SPI conversion circuit is on the ASTRO Spectra Plus VOCON board to ensure communication between the DSP and the secure module. Configuration and mode control of the secure module is performed by the MCU through the SSI/SPI bus.

The CODEC presents the analog speaker audio to the command board's audio power amplifier, which drives the external speaker. For more information on this subject, refer to Section 3.2, "Command Board," on page 3-8.

Since all of the audio and signaling is processed in DSP software algorithms, all types of audio and signaling follow this same path. There is, however, one exception. Low-speed trunking data is processed by the host uP through the SCLK port of the DSP. The DSP extracts the low-speed data from the received signal and relays it to the host uP for processing.

### 3.4.5 ASTRO Spectra Plus TX Signal Path

The transmit signal path (refer to Figure 3-17) follows some of the same design structure as the receive signal path described in Section 3.4.4, "ASTRO Spectra Plus RX Signal Path," on page 3-41.


Figure 3-17. ASTRO Spectra Plus TX Mode

The analog microphone signal from the command board is passed to the ASTRO Spectra Plus VOCON on MAI (Mic Audio In). This signal passes through gain and attenuation stages so that the correct amplitude level of the audio is presented to the CODEC input. The CODEC contains a microphone A/D. The microphone A/D converts the analog signal to a digital data stream and transmits them to the SAP of the Patriot IC. The DSP accesses this data through this port. As with the speaker data samples, the DSP reads the microphone samples from registers mapped into its memory space.

As with the received trunking low-speed data, low speed transmit data is processed by the MCU and returned to the DSP. For secure messages, the digital signal data may be passed to the secure module prior to DSP processing before the ADDAG IC. The DSP transfers the data to and from the secure module through it's SSI port consisting of TXD and RXD. The secure module communicates with the DSP through its SPI bus, therefore a SSI to SPI conversion circuit is on the ASTRO Spectra Plus VOCON board to ensure communication between the DSP and the secure module. Configuration and mode control of the secure module is performed by the MCU through the SSI / SPI bus.

The DSP processes these microphone samples, generates and mixes the appropriate signaling, and filters the resultant data. This data is then transferred to the ADDAG IC on the DSP BBP (Baseband Port) - SSI port. The transmit side of the SSI port consists of SC2B, SCKB, and STDB. The DSP BBP-SSI port is a synchronous serial port. SCKB is the 2.4 MHz clock input derived from the ADDAG, which makes it synchronous. The data is clocked over to the ADDAG on STDB at a 2.4 MHz rate. The ADDAG generates a 48 kHz interrupt on SC2B so that a new sample data packet is transferred at a 48 kHz rate, which sets the transmit data sampling rate at 48 Ksp .
Within the ADDAG IC, these samples are then input to a transmit D/A, which converts the data to an analog waveform. This waveform is the modulation out signal from the ADDAG ports, FMOUT, OUTQ, and OUTQB. FMOUT is single-ended, while OUTQ and OUTQB form a differential pair. This pair is then sent to an Op-Amp (U202), which outputs a single-ended waveform. FMOUT is passed through an Op-Amp (U202) for attenuation. These signals are both sent to the command board, where they go through a gain stage and then to the VCO and Synthesizer. MODOUT is used primarily for audio frequency modulation; REFMOD is used to compensate for low-frequency response to pass subaudible modulated signals (such as PL ).

### 3.4.6 ASTRO Spectra Plus Controller Bootstrap and Asynchronous Busses

The SB9600 bus (see Figure 3-18) is an asynchronous serial communication bus, utilizing a Motorola proprietary protocol. It provides a means for the MCU to communicate with other hardware devices. In the ASTRO Digital Spectra Plus radio, it communicates with hardware accessories connected to the accessory connector and the remote interface board.
The SB9600 bus utilizes the UART internal to the MCU, operating at 9600 baud. The SB9600 bus consists of LH / TX_Data (J501-18), LH / RX_Data (J501-17), and BUSY_RTS (J501-20) signals. LH / TX_Data and LH / RX_Data are the UTXD1 (K11) and URXD1 (K12) ports of the Patriot IC (U300), respectively. BUSY_RTS (U300-URTS1- L16) is an active-low signal, which is pulled low when a device wants control of the bus.

The same UART internal to the MCU is used in the controller bootstrap mode of operation. This mode is used primarily in downloading new program code to the FLASH ROM (U301) on the VOCON board. In this mode, the MCU accepts special code downloaded at 115 k baud through the UART instead of operating from program code resident in its ROMs.
A voltage greater than 11 Vdc applied to J501-31 (Vpp) will trip the circuit comprising VR304, Q300, and U307. This circuit sets the MOD pin (J1) of the MCU to bootstrap mode (logic 1). A voltage greater than 7 Vdc applied to J501-31 (Vpp) will trip the circuit comprising VR305 and Q302. This will not put the MCU in Bootstrap mode, but the software will detect this using pin PA7 (G11), which will allow the user to interface with the Customer Programming Software, Tuner, and Flashport.

The ASTRO Digital Spectra Plus radio has an additional asynchronous serial bus, which utilizes the RS232 bus protocol. This bus utilizes the secondary UART in the Patriot IC (U300). It consists of TX / RS232 (J501-43), RX / RS232 (J501-50), CTS / RS232 (J501-5), and RTS / RS232 (J501-42). It is a four-wire duplex bus used to connect to external data devices.


Figure 3-18. ASTRO Spectra Plus Host SB9600 and RS232 Ports

### 3.4.7 ASTRO Spectra Plus Serial Peripheral Interface Bus

This bus is a synchronous serial bus made up of a data, a clock, and an individual IC unique select line. Its primary purpose is to configure the operating state of each IC. ICs programmed by this include: ADDAG, Synthesizer, Prescaler, and the DAIC.

The MCU within the Patriot IC (U300) is configured as the master of the bus. It provides the synchronous clock (SPI_SCK), a select line, and data (MOSI [Master Out Slave In]). In general the appropriate select line is pulled low to enable the target IC and the data is clocked in. The SPI bus is a duplex bus with the return data being clocked in on MISO (Master In Slave Out). The only place this is used is when communicating with the ADDAG. In this case, the return data is clocked back to the MCU on MISO (master in slave out).

### 3.4.8 ASTRO Spectra Plus MCU and DSP System Clocks

The MCU within the Patriot IC (U300) needs two clocks for proper operation. A 16.8 MHz sine-wave reference is provided at the CKIH (A6) pin of the Patriot IC (U300). The source of this clock is a 16.8 MHz oscillator (Y400), and its associated filtering circuitry. This clock is also provided to the KRSIC (U200), and the ADDAG IC (U201). The MCU has the capability of running at higher clock rates, which are programmable and based on this 16.8 MHz reference. The DSP within the Patriot IC (U300) also uses the 16.8 MHz provided at the CKIH (A6) pin as a reference.
The Patriot IC (U300) also requires a 32 kHz square-wave clock, provided at the CKIL (J7) pin. This clock is generated by a 32 kHz crystal (Y401), with supporting circuitry for oscillation. This clock is utilized only for the Patriot IC (U300), and is used for reset capability and other Patriot IC (U300) functions.

### 3.4.9 ASTRO Spectra Plus Voltage Regulators

The ASTRO Spectra Plus VOCON board contains two voltage regulators, a 3-V regulator (U411) and a $1.8-\mathrm{V}$ regulator (U410). SW+5-V, which is routed to the ASTRO Spectra Plus VOCON board from the command board, drives the two regulators. Figure 3-19 shows the DC distribution for the ASTRO Spectra Plus VOCON Board.


Figure 3-19. ASTRO Spectra Plus VOCON DC Distribution
U410 and U411 are on Semiconductor LP2951CD adjustable regulators. The output voltage of these regulators is determined by the resistive divider network between the regulator output and the error amplifier feedback input. The LP2951 has error output lines which are open collector and requires a pull up resistor (R332). The error line is high when the output voltage is high and low otherwise. U 412 is a $4.2-\mathrm{V}$ detect circuit for the SW_5-V line. The output of this detector is tied to the error outputs of the LP2951 regulators as a low voltage detect (LV_detect) circuit. C438 provides delay on the LV_detect line during startup. This is to allow all regulators to settle prior to Patriot U300 coming out of reset.

### 3.4.10 ASTRO Spectra Plus Radio Power-Up/Power-Down Sequence

The radio power-up sequence begins when the user actuates the control head's on/off switch. The control head then produces the switched $\mathrm{B}+(\mathrm{SWB}+)$ output voltage which is routed to the command board. Upon sensing the SWB+ voltage, the command board circuitry powers on the 9.6 V and the SW $+5-\mathrm{V}$ regulated supplies. The ASTRO Spectra Plus VOCON board contains two voltage regulators, a $3-\mathrm{V}$ regulator ( U 411 ) and a $1.8-\mathrm{V}$ regulator (U410). The $\mathrm{SW}+5-\mathrm{V}$ from the command board is routed to the ASTRO Spectra Plus VOCON board via connector P501, and drives the two regulators. When $\mathrm{SW}+5-\mathrm{V}$ increases above 4.2 V and after a delay time chosen by C438, the voltage detector (U412) disables the power-on reset to the Patriot IC (U300), enabling the device.

When the MCU comes out of reset, it fetches the reset vector in ROM at \$FFFE, \$FFFF and begins to execute the code this vector points to. Among other things it enables the correct memory map for the MCU. It configures all the transceiver devices on the SPI bus. The MCU then pulls the ADDAG and KRSIC out of reset. It then configures the ADDAG through the SPI bus configuring among other things, the DSP memory map. While this is happening, the DSP is fetching code from the FLASH (U301) into internal RAM and beginning to execute it. It then waits for a message from the MCU that the ADDAG has been configured, before going on.

During this process, the MCU does power diagnostics. These diagnostics include verifying the MCU system RAM and verifying the data stored in the FLASH ROM. The MCU queries the DSP for proper status and the results of DSP self tests. The DSP self tests include testing the system RAM and verifying the program code. Any failures cause the appropriate error codes to be sent to the display. If everything is OK , the appropriate radio state is configured and the unit waits for user input.

On power-down, the user actuates the radio's on/off switch, removing the SW_B+ signal from the ASTRO Spectra Plus VOCON board. The host processor, after polling ROW3 (G2) and acknowledging the signal loss, begins the power-down sequence. Since the host holds the 9.6-V/ 5 V _EN (enable) line active by controlling the state of the ROW5 / 5_EN line at P501, pin 15, this does not immediately remove power. The host then saves pertinent radio status data to the external FLASH (U301). Once this is done, the ROW5 / 5 V _EN line is released (brought to logical 1), turning off $9.6-\mathrm{V}$ and the SW+5-V regulators on the command board. When the SW_+5-V slumps to about 4.2 Vdc , the voltage detector ( U 412 ) on the ASTRO Spectra Plus VOCON board activates the system reset to the Patriot IC (U300). This turns off the host processor.

### 3.5 Voltage Control Oscillator

This section of the theory of operation provides a detailed circuit description of voltage control oscillator (VCO). When reading the Theory of Operation, refer to your appropriate schematic and component location diagrams located in "Chapter 7. Schematics, Component Location Diagrams, and Parts Lists". This detailed Theory of Operation will help isolate the problem to a particular component. However, first use the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual to troubleshoot the problem to a particular board.

### 3.5.1 VHF Band

### 3.5.1.1 General

The frequency injection string consists of a voltage-controlled oscillator (VCO) constructed on a ceramic substrate and amplifier and divider stages located on the PC board. The components associated with the PC board may be repaired by conventional methods while the VCO substrate should be replaced as a unit.

### 3.5.1.2 DC Voltage Supplies

The 9.6-V supply enters the VCO carrier board at P601-2. It powers the receiver amplifier (Q675) and its associated biasing components. The keyed $9.4-\mathrm{V}$ supply enters the carrier board at J601-5, but only during the transmit mode. K9.4 powers the divider (Q681), and the buffer amplifiers (Q682, Q683). The 8.6-V supply enters through P601-12 and passes to MP652, MP653, and MP654 on the VCO substrate. The 8.6 V supplies the output buffer on the VCO substrate, and supplies Q642 and 0643, the PIN diode drivers.

### 3.5.1.3 VCO

The VCO utilizes a common-gate FET in a Colpitts configuration as the gain device. The LC tank circuit's capacitive portion consists of a varactor bank and a laser-trimmed stub capacitor. The inductive portion consists of microstrip transmission-line resonators. The stub capacitor serves to tune out build variations. Tuning is performed at the factory and is not field adjustable. The varactor network changes the oscillator frequency when the DC voltage of the steering line changes. The microstrip transmission lines are shifted in and out of the tank by PIN diodes for coarse frequency jumps. The varactor bank consists of CR644 CR645 and L648. The positive steering line connects to the cathodes of both varactors through L3647, an RF choke. This line is normally between 0.5 and 8.5 Vdc , depending on the frequency programmed in the synthesizer. The negative steering line connects to the anodes of the varactors through L646 and is normally $3.9( \pm 0.3)$ Vdc.
Diode CR643, a third varactor tapped into the main transmission line resonator, modulates the oscillator during transmit. The 8.6 Vdc supplies bias to the cathode. Modulation is coupled to the anode through C639, R636, C636, and R3637, which also provide filtering and attenuation to the modulation path.
Components CR646, C668, and R655 provide automatic gain control for the FET. A hot carrier diode, CR3646, detects the peak RF voltage swings on the source of the FET. A negative voltage, proportional to the magnitude of the RF voltage swing, is applied to the gate of the FET, thereby lowering its gain and accomplishing automatic gain control. Typical DC value of the gate bias is -0.8 to -1.7 V , depending on the state of the oscillator.

PIN diodes, CR640, CR641, and CR642, serve to couple secondary transmission lines into and out of the main oscillator tank, depending on which range the VCO is operating. AUX 1* controls CR642 and CR643; AUX 2* controls CR3640. When AUX 1* goes high, Q643 turns off and a reverse-bias voltage of about 8.6 Vdc is applied to the PIN diodes to turn them off. When AUX1* goes low, Q643 turns on and a forward-bias current of about 15 mA is supplied to the PIN diodes to turn them on. The other PIN diode driver network operates similarly.

The VCO output is coupled through C672 to Q645 to amplify the signal and provide load isolation for the VCO. The collector voltage of Q645 is normally about 5 Vdc .

### 3.5.1.4 Synthesizer Feedback

The synthesizer locks the VCO on frequency by the VCO feedback to the prescaler IC on the RF board. The output of the VCO goes into a low-pass filter consisting of C685, L676, and C687. After it is filtered, the signal splits into three directions - the majority of which passes to the RX buffer through a 2db attenuator. A smaller portion of the signal passes through C679 to the divider. Finally, another small portion of the signal is fed back to the RF board through C676 to P601-1. Although on a DC connector, P601-1 is an RF-sensitive node. To measure the synthesizer feedback power, use a high-impedance probe, or operate the VCO in an external fixture.

### 3.5.1.5 RX Buffer Circuitry

After the low-pass filtering state, VCO power is attenuated 2 dB by R678, R680, and R679. The RX buffer is a 50 -ohm in-and-out stage that uses L681 and C689 for the input match and C691, L678, C692, and R699 for the output match. The 9.6 Vdc supplies the RX buffer for a gain of about 10 db . Components R677 and C686 help to filter out some of the $9.6-\mathrm{V}$ supply's noise from the RX buffer. Transistors Q677, Q678, and associated resistors set the bias level of the RX buffer device, Q675. The collector voltage and current should be near 6.6 V and 29 mA , respectively. Resistor R682 feeds the base of 0675 while L677 is used as the collector choke; R681, C690, and C688 are added to increase stability. The cable from the RX frontend is plugged into J642.

### 3.5.1.6 Frequency Divider and TX Buffer Circuitry

During transmit, the VCO oscillates at twice the transmit frequency. A frequency-divider circuit following the VCO buffer divides the VCO's output frequency by two. The circuit is known as a "regenerative frequency divider" in which a mixer and a feedback amplifier are used to divide the frequency of the input signal. The divider circuit consists of transformers T601 and T602, diodes CR601, CR602, amplifier Q681, and the associated bias circuitry. The divider action of this circuit can be understood by tracing the signal through the circuit as follows: The 300 MHz range signal from the VCO buffer is fed into the primary of T602. Note that T602, T601, and diodes CR601 and CR602 form a balanced mixer. (CR601 and CR602 are actually two diodes in one SOT-23 package.) To analyze the frequency division action of the circuit, it must be assumed that the divided output frequency of 150 MHz already exists at the secondary of T601. This 150 MHz signal passes through the low-pass filter consisting of L661, L662, and C651. The 150 MHz signal is now at the input of the amplifier device, Q681. The amplified 150 MHz signal is now applied back into the balanced mixer by the center tap of T601. The difference frequency of the two applied signals ( 300 MHz and 150 MHz ) is 150 MHz , which is half the VCO's frequency. The difference frequency is output through the secondary of T601 where it had been previously assumed to exist. This completes the feedback loop.

The 150 MHz signal is tapped off of the emitter resistor of Q681 and is amplified by the buffer stage, Q682. Transistor Q683 amplifies the signal to 10 dBm , which is the level required by the power amplifier. The signal passes through a low-pass filter before exiting the board through J641.

### 3.5.2 UHF Band

### 3.5.2.1 General

The VCO is located on an alumina substrate with a metallic cover. The buffer-doubler-buffer section is located on the PC board and may be repaired using normal repair methods.

### 3.5.2.2 Super Filter 8.6 V

Super-filtered 8.6 V enters the carrier board at J601-12, through an R-C filter, then on to the drain of Q9610 and the collector of Q9635.

### 3.5.2.3 VCO

The oscillator consists of Q9610, the main transmission line (T-line), varactor bank (CR9616-9617, C9616-9617, L9616) and feedback capacitors (C9611-9613). Components CR9610, C9614, and R9613 form an AGC circuit to prevent breakdown of the FET. Components CR9626 and C9626 form a bandshift circuit to shift the oscillator frequency up 50 MHz ; C9630-9631 and CR9630 form the Receive shift circuit which shifts the VCO up 50 MHz . The main modulation circuit consists of C9621 and CR9621 in conjunction with the deviation compensating capacitors (C9622 and C9623). Finally, transistor (Q9635), resistors (R9635-9639), and capacitors (C9635-9636, C9638) form the output buffer.

This VCO utilizes both a positive and negative steering line. The SL- should be $-4.0 \mathrm{~V}( \pm .3 \mathrm{~V})$ at all times. The SL+ will range from 1 to 8 V , depending on frequency and AUX bits.

### 3.5.2.4 Receive Mode (AUX2* Low)

When AUX2* input is low, the receive pin diode, CR9630, is forward biased by $8.6-\mathrm{V}$ supply thru Q5650 and R5652. This current is then sunk into the RF board thru R5654. At this time the voltage divider output of R5649, R5651, and R5653 will keep Q5651 turned off.

### 3.5.2.5 Transmit Mode (AUX2* High)

When AUX2* is high ( 8.4 V ), Q5650 will be off and Q5651 will be on. This puts -8 V on the anode of CR9630 and +8.4 V on its cathode. With approximately $16-\mathrm{V}$ reverse bias on the diode, the receive bandshift T-line is removed from the circuit.

### 3.5.2.6 Bandshift Circuit

R9625, C9625, L9628, and C9628 form a bandshift circuit which shifts the frequency of the oscillator slightly. There is one bandshift in receive and one in transmit. The circuitry works similar to the receive pin circuitry but with the cathode of CR9626 returned to ground. This results in a maximum of $8-\mathrm{V}$ reverse bias on this diode.

### 3.5.2.7 Output Buffer

Transistor (Q9635), resistors (R9635-9639), and capacitors (C9635-9636, C9638) form a simple common-emitter buffer to provide isolation to the VCO and an output power of +10 dBm .

### 3.5.2.8 First Buffer

The VCO output is coupled to the first buffer via blocking capacitor (C5661), resistive pads (R5661 and R5662), and a high-pass filter (L5660 and C5662). Q5660 is a self-biased, common-emitter amplifier which provides approximately +10 dBm drive to the doubler as well as reverse isolation to the VC0.

### 3.5.2.9 Doubler

The first buffer output is coupled to the input of the doubler by C5663. Q5660 doubles the drive frequency and increases power by approximately 3 dB as a result of the high and low impedances presented to its collector at the doubled frequency and drive frequency, respectively. The collector impedances are presented by an elliptical high-pass filter (C5670-C5674, L5670, and L5671). The filter is terminated in a resistive pad (R5676-R5678) which also serves to terminate one end of the elliptical low-pass filter (C5675, C5677, and L5672-L5674). In addition to filtering, the low-pass filter provides part of the impedance match required between the resistive pad and the second buffer. The remaining impedance match is accomplished with L5680 and C5680, configured to provide additional high-pass selectivity.

### 3.5.2.10 Synthesizer Feedback

The base of Q5680 provides the tap location for the synthesizer feedback buffer. C5685-C5686 and L5681 provide low-pass filtering. R5630, R5631 and R5632 is a resistive pad. Q5630 provides approximately -5 dBm to the RF board.

### 3.5.2.11 Second Buffer

The second buffer, Q5680, is a common-emitter amplifier with approximately 12 dB gain. It is biased to 40 mA . with an active current source, Q5681 and R5580-R5587, which ensures saturated operation.

### 3.5.2.12 Receive/Transmit Switch

In the receive mode where K9.4-V is off, Q5640 conducts current to turn on the part of CR5690 (a dual-common cathode pin diode) that is in series with the receive path, and the part of CR5691 that is in shunt with the transmit path. The output of Q5680 is then coupled to a resistive pad R5697-R5699 which sets the power out of J 5642 to approximately +12 dBm .
In the transmit mode, K9.4-V applies 9.4 V to the anode of CR5640, thus turning off Q5640. K9.4-V is also applied to resistors R5688 and R5694 which turn on the parts of CR5690 and CR5691 that are in series with the transmit path. The output of Q5680 is then coupled to a resistive pad (R5689-R5691) which sets the power out of J 5641 to approximately +16 dBm .

### 3.5.3 800 MHz Band

### 3.5.3.1 General

The VCO is located on an alumina substrate with a metal cover. The buffer-doubler-buffer section is located on the PC board and may be repaired using normal repair methods.

### 3.5.3.2 Super Filter 8.6 V

Super filter 8.6 V is applied to the VCO carrier board at J601-12. From there, SF8.6 passes to the drain of Q9641, to the emitters of Q9643 and Q9644, and to the collector of Q9642.

### 3.5.3.3 VCO

Q9641, the main and transmit/TalkAround transmission lines, and the varactors CR9641 through CR9644 form the major circuitry of the oscillator. CR9645, C9648, C9647, and R9641 make up an automatic gain control (AGC) circuit.

The positive steering line connects to the cathodes of the four varactors and the negative steering line connects to the anodes. The negative line should be $-4.0 \pm 0.3 \mathrm{~V}$ and the positive line can go as high as 9 V , allowing a difference of 15 to 16 V between the two. Normally, at room temperature, the positive steering line will be between 1.5 and 5.5 V and will fluctuate with temperature change in the radio. Modulation is connected to the negative steering line via R9651 and C9651.

When the radio is transmitting, the oscillator's frequency will be in the 403 to 412 MHz range. When receiving, the frequency will be between 370.675 and 379.675 MHz . If the radio is in the TalkAround mode, the frequency will be between 425.5 and 434.5 MHz . The transmit and TalkAround ranges are produced by coupling an additional length of transmission line to the main transmission line and is done by a high or low on the AUX $1^{*}$ or AUX $2^{*}$ input lines.

### 3.5.3.4 Receive Mode-AUX 1* and AUX 2* High

When AUX 1* is HIGH, 8.6 V is applied to the cathode of CR9646. Q9643 is turned off and Q9647 is turned on placing approximately -6.2 V at the anode of CR9646 reverse biasing it. Likewise with AUX 2* high the same occurs except CR9647 is reversed biased with Q9644 off and Q9646 on. This isolates the TRANSMIT/TALKAROUND transmission line from the MAIN transmission line.

### 3.5.3.5 Transmit Mode-AUX 1* High; AUX 2* Low

When AUX 1* is high, the same occurs as mentioned above, however, with AUX 2* low, CR9647 is forward-biased, connecting the TRANSMIT/TALKAROUND transmission line through C9658 and C9657 to the MAIN transmission line.

### 3.5.3.6 TalkAround Mode-AUX 1* Low; AUX 2* Low

With AUX 1* and AUX 2* low, CR9647 and CR9646 are forward-biased, connecting the TRANSMIT/ TALKAROUND transmission line through C9656, C9655, C9657, and C9658 to the MAIN transmission line.

### 3.5.3.7 VCO Buffer

Q9642 amplifies and provides reverse isolation to the oscillator. The frequency is then applied to the buffer-doubler-buffer section of the VCO carrier board.

### 3.5.3.8 First Buffer Circuit

The VCO output is coupled to the first buffer section through C9677. Q9660 amplifies and provides additional isolation between the doubler and the VCO.

### 3.5.3.9 Doubler

The first buffer output is coupled to the doubler section through C9662 and a lowpass input match circuitry (C9675, L9675, C9676, and L9676), which serves two purposes: it matches the input of the doubler to 50 ohms, and improves isolation between the VCO and doubler. It also keeps the desired doubler output frequency from getting to the synthesizer. The synthesizer feedback frequency is via C9674 and R9669.

Q9675 doubles the frequency applied to its base. The components on the collector are built so that a 400 MHz signal is effectively shorted to ground, while the 800 MHz signal sees high impedance to ground. The doubler is coupled to the buffer through C9681, and into a 50 -ohm matching network made up of C9683 and L9680.

Doubler-biasing differs between receive mode and transmit mode. For receive, R9677, R9678, and R9676 (in parallel to dissipate power) plus R9679 and R9680 bias the base of Q9675 to 0.7-V potential, if NO input RF power is applied to the base. For transmit mode, keyed 9.4 V is fed through CR9694 and another parallel resistor network R9674 and R9675. This raises the current to the collector of Q9675 via L9678, producing more power out.

### 3.5.3.10 Second Buffer

The second buffer circuit is Q9676 with a 460 MHz trap, made up of L9682 and C9686, on the collector. The signal is coupled by series LC network of L9683 and C9687. For the receive mode, Q9676's gain is approximately 1 to 4 dB ; in transmit, its gain is approximately 7 dB .
In receive mode, K9.4-volts is off so that the base voltage of Q9692 is controlled by voltage divider, R9694 an R9695. With temperature changes, the emitter-base junction of Q9691 tracks that of Q9692's, stabilizing the collector current and collector voltage of Q9676. R9690, R9691, and R9692 set the current level to the collector of Q9676 in receive.

In transmit mode, K9.4-volts is applied to CR9693 and through R9697, R9699, and R9693, increasing the current flow to Q9676. K9.4-volts on the anode of CR9690 increases the voltage on the base of Q9692. This increases voltage at Q9692's emitter and Q9676's collector. In the transmit mode, the buffer draws approximately 60 mA .

### 3.5.3.11 K9.4 V Switch

In the receive mode, K9.4-volts is off. CR9691 is reverse-biased, CR9692 is forward-biased; therefore Q9693 conducts to produce 9.4 V on the collector. This forward-biases CR9678 and CR9677, allowing RF to pass through C9688. R9687, R9688, and R9689 drop the 12 dBm signal on the anode of CR9678 down to 0 to 5 dBm . This is the receiver injection signal which is applied to the first mixer in the front end of the radio.

In the transmit mode, K9.4-volts is on. Q9693 turns off, reverse biasing CR9678 and CR9677. However, CR9675 and CR9676 are forward-biased, allowing the RF signal to pass through C9689. The signal (approximately 20 dBm ) at the junction of CR9675 and CR9677, is attenuated about 1 dB across the diodes. The transmit signal, at approximately 18 to 23 dBm , is applied to the power amplifier via C9689.

### 3.6 Receiver Front-End

This section of the theory of operation provides a detailed circuit description of receiver front-end (RXFE). When reading the Theory of Operation, refer to your appropriate schematic and component location diagrams located in "Chapter 7. Schematics, Component Location Diagrams, and Parts Lists". This detailed Theory of Operation will help isolate the problem to a particular component. However, first use the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual to troubleshoot the problem to a particular board.

### 3.6.1 VHF Band

### 3.6.1.1 General

The Receiver Front-End (RXFE) performs the first conversion of the received signal. The inbound signal is mixed with the high side injection signal, to produce the 109.65 MHz first IF. The pre-amp/ mixer configuration of the RXFE includes a preamplifier, a factory-tuned, 5 -pole L.C. preselector unique for two ranges, a fixed injection filter, and a double balanced mixer.

### 3.6.1.2 Theory of Operation

The RF input from the PA first enters the high pass filter consisting of components L3200, L3201, L3202, C3200, C3201, C3209, and C3210. The high pass filter attenuates signals below the receiver passband for both RF frequency ranges.

A pair of Schottky diodes (CR3200) located before the high pass filter and after the 5-pole L.C. preselector, limit the signal amplitude going into the preamplifier. A second pair of Schottky diodes (CR3201) located after the 5-pole L.C. preselector, further provide signal protection to the mixer.

The RF board supplies DC voltage to the pre-amp. Transistors Q3200 and Q3201 stabilize the bias for pre-amp device Q3202 through temperature changes. R3206, R3200, R3210, R3208, and R3209 are adjusted to meet radio performance specifications for High or Low sensitivity.

The factory-tuned preselector filter accepts RF input frequencies ranging from 136-162 MHz (Range 1) or $146-174 \mathrm{MHz}$ (Range 2). L3100, L3101, L3102, L3103, L3104 comprise the set of inductors which are tuned by the factory.

The double-balanced mixer has an injection level of +20 dBm , common for both ranges; at its output, a diplexer helps terminate the IF port at all frequencies of interest, and forms the bandpass filter.

From the pre-amp input to the IF output, there should be a conversion gain of -1.5 to +3.5 dB for high sensitivity, and +7.0 to +10 dB for low sensitivity specifications.

### 3.6.2 UHF Band

### 3.6.2.1 General

The receiver ceramic filter has a typical insertion loss of about 0.5 to 1.5 dB ; it should not have a loss greater than 2.0 dB . If any soldering must be done on the filter, be very careful not to get any solder on the filter tuning pads.

The injection filter is a printed pattern on the substrate which is laser-tuned at the factory. The insertion loss of this filter is about 3 dB .

### 3.6.2.2 Theory of Operation

The factory-tuned ceramic preselector filter accommodates RF input frequencies ranging from 438 to 470 MHz (Range 2), 450 to 482 MHz (Range 3), or 482 to 512 MHz (Range 4). The injection filter is tuned to pass frequencies from 549 to 580 MHz for Range 2, 559 to 592 MHz for Range 3, or 592 to 622 MHz for Range 4. Each frequency is connected at a node just before C9138 via a transmission line which acts as a high impedance input to the other frequency.

The RF board supplies DC voltage to bias the mixer Q125. Transistor Q126 controls the voltage to the base of Q125. The voltage at the collector of Q125 should be approximately 10 V .

### 3.6.3 800 MHz Band

### 3.6.3.1 General

The receiver ceramic filter has a typical insertion loss of about 1.6 to 1.7 dB ; it should not have a loss greater than 2.5 dB . If any soldering must be done on the filter, be very careful not to get any solder on the filter tuning pads.

The injection filter is a printed pattern on the substrate which is laser-tuned at the factory. The insertion loss of this filter is about 3 dB .

### 3.6.3.2 Theory of Operation

The factory-tuned ceramic prescaler filter accommodates RF input frequencies ranging from 851 to 870 MHz . The injection filter is tuned to pass frequencies from 741 to 760 MHz . Each frequency is connected at a node just before C8126 via a transmission line which acts as a high impedance input to the other frequency.
DC voltage, supplied from the RF board, biases the mixer Q8126. Transistors Q8127 and Q8128 control the voltage to the base of Q8126. Q8128 acts as a diode to maintain a voltage on the base of Q8127, which keeps the bias of Q8126 stable through temperature changes. The voltage for the collector of Q8126, which passes through R8128, L8131, and L8130 should be approximately 8 volts.

C8129, L8129, C8131, and L8130 form the output network for the mixer. C8131 is a large capacitor that appears as a short to all frequencies of interest. The remaining components form a bandpass filter centered at the IF frequency.

R8130, R8129, and R8131 form an attenuator on the output path to stabilize both the mixer output impedance and the source impedance for the IF amplifier.
From the input to the ceramic filter to the IF output, there should be an 8 dB power gain presented to the IF. If the beta of Q8126 falls below 60, the mixer (Q8126) is probably bad and must be replaced.

### 3.7 Power Amplifiers

This section of the theory of operation provides a detailed circuit description of the power amolifiers. When reading the Theory of Operation, refer to your appropriate schematic and component location diagrams located in "Chapter 7. Schematics, Component Location Diagrams, and Parts Lists". This detailed Theory of Operation will help isolate the problem to a particular component. However, first use the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual to troubleshoot the problem to a particular board.

### 3.7.1 VHF Band Power Amplifiers

### 3.7.1.1 High-Power Amplifier

### 3.7.1.1.1 Transmitter

The high-power ASTRO Spectra amplifier is discussed in the following text. A block diagram of the circuit is shown on the foldout drawing.

## Transmit Low Level Amplifier (LLA)

The LLA is the first stage of the PA and provides a gain that is a function of the control voltage. This control voltage comes from the Regulator Power Control IC (RPCIC) on the command board. The magnitude of the control voltage depends on PA output power, temperature, and final amplifier current drain. See Section 3.7.1.1.3, "Power Control Circuitry," on page 3-57 for a detailed explanation of the power control circuitry.
The LLA, Q3801, is unique in that its gain is controlled by varying the collector's current rather than its voltage. Q3801 and associated circuitry (Q3806 Q3802, R3804, and R3818) are best described as a voltage-controlled current source. This means that the collector current of Q3801 is controlled by the magnitude of the control voltage.

## Second Amplifier Stage

The second stage of the PA, Q3804, amplifies the output of the LLA to a level sufficient to drive the third stage device, Q3805. Q3804 amplifies the LLA output from approximately 300 mW to 3.0 Watts.

## Driver Stage (Q3805)

The third stage uses a 3.0-Watt input to 30 -Watt output device. It is driven by the second stage through a matching circuit that consists of C3824, L3808, C3819, and C3820. L3812 and L3809 give the device a zero-Vdc base bias (required for Class-C operation). The network of L3811, L3810, R3819, and C3821 provide A+ to the collector.

## Final Stage (Q3870 AND Q3871)

The final amplifier stage is the parallel combination of two 15 -Watt input to 75 -Watt output RF transistors. The matching network, from the collector of the driver device Q3805 to the bases of the final devices Q3870 and Q3871, utilizes transmission lines as part of a combination matching network and power splitter. The capacitors C3860, C3861, C3862, and C3863 are on the bottom side of the PC board underneath the base leads of Q3870 and Q3871.

The DC bias path for the base of Q3870 is via L3930 and L3931. Q3871 has a similar network. R4007, R4008, and R3859 improve division of driver power between the final devices Q3870 and Q3871.

A feedback network consisting of C3870, R3870, and L3870 suppresses parasitic oscillations in Q3870. Q3871 has a similar network.

The final stage output network serves the dual purpose of impedance matching and power combining of the two final devices. R3872 and R3873 help balance the load impedances presented to the collectors of the final devices. Filtered A+ is routed to the final amplifier devices via the current sense resistor R3841, the ferrite bead L3881, and the coil L3880. The final stage output network terminates at C3889, which is the input to the antenna switch. The circuit impedance is 50 ohms at this point.

### 3.7.1.1.2 Antenna Switch and Harmonic Filter

## Antenna Switch

The antenna switch utilizes PIN diodes to form a low loss, high isolation RF relay. During transmit, PIN diodes CR3901, CR3902, and CR3903 are forward biased during transmit via the K9.4 supply and resistors R3900, R3901, R3902, and R3903. In this state, a low loss path exists from the final amplifier through PIN diode CR3901 and into the harmonic filter. PIN diodes CR3902, and CR3903 effectively shunt the path to the receiver front-end which protects the preamp or mixer device from excessive RF levels. A properly functioning switch will pass less than 10 mW of transmit power to the receiver front-end.

During receive, all three PIN diodes remain unbiased. This opens a low loss path from the harmonic filter to the receiver.

## Harmonic Filter

The harmonic filter is a 7-pole low-pass filter consisting of screened plate capacitors and air-wound coils on a 0.035 inch thick ceramic substrate. The filter's primary function is to.attenuate harmonic energy generated by the amplifier stages. The filter also adds some selectivity for the receiver.

### 3.7.1.1.3 Power Control Circuitry

## Command Board Circuitry

Inside U500, the Regulator Power Control IC (see Figure 3-20) is an operational amplifier that has four inverting inputs, and one non-inverting input (at pin 44) which is the reference input for the entire power control loop of the power amplifier. The 3.2-V reference voltage at U500-44 is produced by dividing SW $+5-\mathrm{V}$ with the voltage-divider circuit, R514 and R515.


Figure 3-20. RPCIC Block Diagram
The power control loop is controlled by the microprocessor U204 on the VOCON board. Through the SLIC IC U206, the microprocessor enables the RPCIC by pulling TX PA ENABLE (U500 pin 33) low while the radio synthesizer is locked (U500 pin 35). U520 writes data to a digital-to-analog converter, U502, to change and control the power-set voltage from pin 10 of U502 to pin 6 of U500. The voltage on this line, 1.5 to 5 V , will be inversely proportional to the power out of the PA , with 5 V producing the lowest power output. This voltage may be set with RSS (Radio Service Software) or CPS (Customer Programming Software). On U500, the voltage at pin 6 is buffered internally and exits on pin 7. Through R507, it is connected to pin 2 of U500. Note that pin 2 of U 500 is the summing point of three voltages: forward detect voltage, power set voltage, and temp-sense.

## Control Voltage Limiter

R3807 and R3808 form a voltage divider that connects to control voltage drive. The output of this voltage divider is connected to the control-voltage-limit input (pin 4) of the RPCIC. If the voltage at this input reaches 3.2 V , then the control voltage will be clamped to a maximum value. For the high-power VHF PA, this maximum value is 9 V . This voltage control limit is set by the values of R3807 and R3808.

## Current Limiter

U204, the processor on the VOCON board, sends data to U502, the digital-to-analog converter, to properly set the voltage on U502, pin 15, which is the TX CURRENT LIMIT control line to the RPCIC (U500, pin 40). Sixteen different voltages, ranging from 1.5 to 4.5 V , can be programmed from U502.
The collector current of the 110-Watt amplifier is monitored by sensing the voltage across R3849. CURRENT SENSE + connects to one end of R3849; CURRENT SENSE - connects to the other end. These lines connect to the command board on U500 pins 37 and 38 , respectively. If the TX CURRENT LIMIT is set for 1.5 V , then the voltage difference between U500 pins 37 and 38 must be 0.1 V before the current through R3849 is reduced. If U 500 pin 40 is programmed for 4.5 V , then the difference of potential between pins 37 and 38 must exceed 0.3 V before current limiting begins. The voltage across R3849, where current sense occurs, can be determined by multiplying the voltage on U500 pin 40, by 0.067 . When current is being limited, the output of the op-amp (U500, pin 42) begins shutting down the conduction of Q503 and Q504, reducing PA control voltage, and reducing drive to the final amplifier to, effectively, control the final amplifier's maximum current.

## Forward Power Limiter

After the harmonic filter a parallel pair of microstrip lines form a forward power sensing directional coupler and detector. The output of this directional coupler/detector is a DC voltage that is proportional to the forward RF power from the final amplifier. During normal transmission, the DC voltage from the forward detect line to the RPCIC ranges from 2 to 5.0 V . This voltage connects to U500 pin 9, the directional coupler buffer input.

The directional coupler's buffered output, U500 pin 8, is summed to pin 2 with the digital/analog buffer's output through R509 and R507, respectively. In typical operation, the closed loop operation of the circuit attempts to keep the voltage at U500 pin 2 a constant value of 3.2 V . The control amp will maintain this condition by increasing or decreasing the control amp output voltage. This control amp output voltage is routed to the LLA via transistors Q503 and Q504. The output of Q504 is designated "control voltage drive" and is routed to J 1 pin 2 of the PA board.
Since control voltage drive controls the gain of the LLA, it determines the drive level to the following stages and thus the output power of the final amplifier. The output power of the final stage is detected by the directional coupler and is routed back to U 500 pin 2 via the buffer and R507. Thus the loop is complete and forward power is maintained a constant value. The voltage at pin 2 will drop below 3.2 V during low line voltage conditions where the PA cannot produce rated power. Current limit and voltage control limit circuits will also affect the voltage at pin 2 as described in the following.

## Temperature Sensing

The temperature-sensing circuit of the PA works with the RPCIC to protect the PA devices from excessively high temperatures. On the PA board, this circuit (formed by resistors R3916, R3841, and thermistor RT3842), provides a temperature dependent voltage to the RPCIC via J1 pin 6. As the PA temperature increases, the resistance of RT3842 decreases, causing the voltage at pin 6 to increase. This voltage is routed to the RPCIC, U500 pin 13, which is the input to the thermistor buffer. The buffer's output on pin 12 is connected to pin 2 via resistor R508. Note that pin 2 is the control amp input and is a summing point for temperature, forward-power detect, and power set signals. If the PA temperature becomes high enough so that the voltage at pin 7 exceeds 3.2 V , the thermistor buffer starts supplying current to the node at pin 2 . Due to the fixed output current of the power-set buffer, the control loop can maintain 3.2 V at pin 2 only by reducing the forward-power detect voltage and, therefore, reducing the PA output power. Since power output is reduced, the generated heat is reduced to a safe level. If temperature decreases, the power output of the PA gradually increases to its nominal value.

NOTE: Under severe environmental conditions, more than one circuit may be attempting to reduce power output at the same time (i.e., during high VSWR conditions, the current limiter may initially reduce power, but eventual heat buildup will cause further power reduction by the thermal cut-back circuit).

The temperature sense circuitry can easily be tested by placing an ordinary leaded 4.7 k ohm resistor across RT3842. PA output power should drop significantly if this circuit is working properly.

### 3.7.1.2 25/10-Watt Power Amplifier

## Transmitter

The 25/10-Watt Spectra power amplifier is discussed in the following text.

## Transmit Low Level Amplifier (LLA)

NOTE: The minimum input drive level to the PA into J 3850 is 10 mW . Refer to the synthesizer section if input drive is less than 10 mW .

The Low Level Amplifier, the first stage of the PA, provides a gain that is a function of a control voltage. This control voltage comes from the Regulator Power Control IC (RPCIC) on the command board. The magnitude of the control voltage depends on PA output power, temperature, and final amplifier current drain.

The LLA, Q3801, is unique in that its gain is controlled by varying the collector's current rather than its voltage. Q3801 and associated circuitry (Q3806, Q3802, R3804, and R3818) are best described as a voltage-controlled current source. This means that the collector current of Q3801 is controlled by the magnitude of the control voltage. Proper operation of the LLA can be checked by monitoring the voltage across the resistor R 3804 . The voltage should measure in the range of 0.1 V to 1.0 V , depending on the value of control voltage. A $0.1-\mathrm{V}$ reading corresponds to a low control voltage ( 1 to 5 V ) and a 1.0 V reading corresponds to a high control voltage (up to control voltage limit).

## Driver Stage

The second stage of the PA, Q3804, is the driver. The purpose of this stage is to amplify the output of the LLA to a level sufficient to drive the final device, Q3850. Input power to this stage is approximately 100 mw ; output power from this stage is 3.5 Watts.

## Final Stage

The final device is a 3- to 33-Watt device and is driven by the driver through a low-pass matching circuit that consists of C3815, C3816, C3817, L3811, C3819, C3821, C3822, C3823 and associated transmission lines. Base network, L3852, L3851, and R3815, R3819 provide the zero-DC bias required by the final device's Class-C operation. L3852 and L3851 provide the DC path from base to ground. R3815 and R3819 help lower the network's Q at low frequencies. The collector DC network consists of L3875, L3876, R3876, R3877, C3880, C3885, C3881, C3882, and CR3875. This network provides the A+ voltage to the final while blocking RF from getting up the DC line. L3875 and L3876 provide the DC path and block RF. R3876 and R3877 resistively load down the final's collector at low frequencies and prevent unwanted oscillations. C3881, C3882, C3880, and C3885 are all bypass capacitors ranging from very low frequencies up to VHF frequencies. R3875 is the current-sense resistor. CR3875 protects against reverse polarity. Finally, the RF signal goes through a low-pass matching network (C3875, C3877, C3878, C3879, L3877, and associated transmission lines) to the rest of the output network (Directional Coupler, Antenna Switch, and Harmonic Filter).

### 3.7.1.2.1 Antenna Switch and Harmonic Filter

## Antenna Switch

The antenna switch's impedance inverter circuit, made up of C3920 and L3920, takes the place of a quarter-wave microstrip line. During transmission, Keyed 9.4 V forward-biases CR3921, producing low impedance on CR3921's anode and high impedance on the C3920/L3920 node. Effectively, this isolates the transmitted power from the receiver. C3910 couples the power to the harmonic filter and on to the antenna.

Total TX to RX isolation exceeds 50 dB from $136-174 \mathrm{MHz}$. The impedance inverter contributes approximately 30 db to transmit isolation. A second shunt switch, made up of CR3922, L3921, C3922, and C3921, provide additional isolation. C3926 and C3923 block DC.

During RX, CR3920 has an OFF capacitance of approximately 1 pF. CR3921 and CR3922, incorporated in the RX match, have similar OFF capacitance.

## Harmonic Filter

The 25/10-Watt harmonic filter is a 7-pole, low-pass filter, consisting of high-Q chip capacitors (C3911, C3913, C3912, and C3914) and discrete inductors (L3911, L3912, and L3913). The filter's primary function is to attenuate harmonic spurs generated by the transmitter. It also adds low-pass selectivity for the receiver. L3914 protects the PA from static discharge.

### 3.7.1.2.2 Power Control Circuitry

## Command Board Circuitry

Inside U500, the Regulator Power Control IC (see Figure 3-21), is an operational amplifier that has four inverting inputs, and one non-inverting input (at pin 44) which is the reference input for the entire power control loop of the power amplifier. The 3.2-V reference voltage at U500-44 is produced by dividing SW $+5-\mathrm{V}$ with the voltage-divider circuit, R514 and R515.

The power control loop is controlled by the microprocessor U204 on the VOCON board. Through the SLIC IC U206, the microprocessor enables the RPCIC by pulling TX PA ENABLE (U500 pin 33) low while the radio synthesizer is locked (U500 pin 35). U520 writes data to a digital-to-analog converter, U502, to change and control the power-set voltage from pin 10 of U502 to pin 6 of U500. The voltage on this line, 1.5 to 5 V , will be inversely proportional to the power out of the PA , with 5 V producing the lowest power output. This voltage may be set with RSS (Radio Service Software) or CPS (Customer Programming Software).


REGULATOR/POWER CONTROL IC U500

Figure 3-21. Regulator/Power Control IC Block Diagram

## Control Voltage Limiter

R3813 and R3814 form a voltage divider that connects to control voltage drive. The output of this voltage divider is connected to the control-voltage-limit input ( pin 4) of the RPCIC. If the voltage at this input reaches 3.2 V , then the control voltage will be clamped to a maximum value. For the $25 / 10-$ Watt VHF PA, this maximum value is 9.2 V . This voltage-control limit is set by the values of R3813 and R3814.

## Current Limiter

U204, the processor on the VOCON board, sends data to U502, the digital-to-analog converter, to properly set the voltage on U502, pin 15, which is the TX CURRENT LIMIT control line to the RPCIC (U500, pin 40). Sixteen different voltages, ranging from 1.5 to 4.5 V , can be programmed from U502.

The collector currents of the 25/10-Watt amplifier is monitored by sensing the voltage across R3875. CURRENT SENSE + connects to one end of R3875; CURRENT SENSE - connects to the other end. These lines connect to the command board on U500, Pins 37 and 38 , respectively. If the TX CURRENT LIMIT is set for 1.5 V , then the voltage difference between U500, Pins 37 and 38 must be 0.1 V before the current through R 3875 is reduced. If U 500 , pin 40 is programmed for 4.5 V , then the difference of potential between Pins 37 and 38 must exceed 0.3 V before current limiting begins. The voltage across R3875, where current sense occurs, can be determined by multiplying the voltage on U500, pin 40, by 0.067 . When current is being limited, the output of the op-amp (U500, pin 42) begins shutting down the conduction of Q503 and Q504, reducing PA control voltage, and reducing drive to the final amplifier to control the final amplifier's maximum current.

## Forward Power Limiter

After the final amplifier, a parallel pair of non-symmetrical microstrip lines form a forward power-sensing directional coupler. Because of increased coupling with frequency, C3902 is used to compensate and filter out harmonics. R3905, R3906, C3903, and L3903 provide DC bias to CR3900, which rectifies the signal. During normal transmission, the DC voltage from the forward-detect line to the RPCIC ranges from 1.5 to 5.0 V . This voltage connects to U500, pin 9, the directional coupler buffer input.

The directional coupler's output, U500 pin 8, is summed to pin 2 with the digital/analog buffer's output through R509 and R507, respectively.
Closed loop operation reduces the control amp's output ( pin 42), reduces the power amplifier's gain, and reduces power output to maintain the coupler buffer output (U500, pin 2) at 3.2 V regardless of the $\mathrm{D} / \mathrm{A}$ voltage level. If the $\mathrm{D} / \mathrm{A}$ voltage is high $(4.5 \mathrm{~V})$, little detected voltage is needed to keep pin 2 at 3.2 V , and the power, consequently, is low. If the D/A voltage is low $(1.5 \mathrm{~V})$, a large forward detected voltage is needed to keep pin 2 at 3.2 V and power, consequently, is at maximum value. The voltage at pin 2 drops below 3.2 V under proper operation during low line voltage conditions where the PA cannot produce rated power, or if, under any conditions, the control voltage, or the final device current exceeds safe levels.

## Temperature Sensing

The temperature-sensing circuit of the PA works with the RPCIC to protect the PA devices from excessively high temperatures. On the PA board, this circuit, formed by resistors R3878, R3879, and thermistor RT3876, provides a temperature-dependent voltage to the RPCIC via P0853, pin 7. As the PA temperature increases, the resistance of RT3876 decreases, causing the voltage at pin 7 to increase. This voltage is routed to the RPCIC, U500, pin 13, which is the input to the thermistor buffer. The buffer's output on pin 12 is connected to pin 2 via resistor R508. Note that pin 2 is the control amp input and is a summing point for temperature, forward-power detect, and power set signals. If the PA temperature becomes high enough so that the voltage at pin 7 exceeds 3.2 V , the thermistor buffer starts supplying current to the node at pin 2 . Due to the fixed output current of the power-set buffer, the control loop can maintain 3.2 V at pin 2 only by reducing the forward-power detect voltage and, therefore, reducing the PA output power. Since power output is reduced, the generated heat is reduced to a safe level. If temperature decreases, the power output of the PA gradually increases to its nominal value. Temperature cutback should occur at about $140^{\circ} \mathrm{F}\left(60^{\circ} \mathrm{C}\right)$.

The temperature sense circuitry can easily be tested by placing an ordinary leaded 4.7 k ohm resistor across RT3876. PA output power should drop significantly if this circuit is working properly.

NOTE: Under severe environmental conditions, more than one circuit may he attempting to reduce power output at the same time (i.e., during high VSWR conditions, the current limiter may initially reduce power, but eventual heat buildup will cause further power reduction by the thermal cut-back circuit).

### 3.7.1.3 50-Watt Power Amplifiers

### 3.7.1.3.1 Transmitter

The 50-Watt ASTRO Spectra power amplifiers (PA's) are discussed in the following text. A block diagram of the circuit is shown in Figure 3-22.


Figure 3-22. 50-Watt Power Amplifier Block Diagram

## Transmit Low Level Amplifier (LLA)

NOTE: The minimum input drive level to the PA into J3850 is 10 mW . Refer to the synthesizer section if input drive is less than 10 mW .

The LLA, the first stage of the of the PA, provides a gain that is a function of a control voltage. This control voltage comes from the Regulator Power Control IC (RPCIC) on the command board. The magnitude of the control voltage depends on PA output power, temperature, and final amplifier current drain.

The LLA, Q3801, is unique in that its gain is controlled by varying the collector's current rather than its voltage. Transistor Q3801 and associated circuitry (Q3806, Q3802, R3804, and R3818) are best described as a voltage-controlled current source. This means that the collector current of Q3801 is controlled by the magnitude of the control voltage. Proper operation of the LLA can be checked by monitoring the voltage across the resistor R3804. The voltage should measure in the range of 0.1 V to 1.0 V , depending on the value of control voltage. A $0.1-\mathrm{V}$ reading corresponds to a low control voltage ( 1 to 5 V ) and a $1.0-\mathrm{V}$ reading corresponds to a high control voltage (up to control voltage limit).

## Predriver Stage

The second stage of the PA, Q3804, is the predriver. The purpose of this stage is to amplify the output of the LLA to a level sufficient to drive the driver device, Q3850. Input power to this stage is approximately 100 mW ; output power from this stage is 1.0 Watt.

## Driver Stage

The driver is a 1.2 - to 15 -Watt device. It is driven by the predriver device through a matching circuit that consists of C3815, C3816, C3817, C3818, and L3811. A ferrite bead L3810, and a parallel resistor, R3815, give the driver a zero-DC bias required for the driver's Class C operation, and provides a low Q network to prevent unwanted oscillations. The network of L3851, L3854, C3858, C3856, C3855, and R3850 provide A+ to the collector. L3851 and L3854 provide the DC path and block RF from coming up the DC line. R3850 resistively loads down the collector at low frequencies, preventing unwanted oscillations. C3856, C3855, C3858, and C3855 are bypass capacitors.

## Final Stage

The final device is a 12 - to $75-$ Watt device and is driven by the driver through a low pass matching circuit that consists of C3850 through C3854 and associated transmission lines. Base network, L3852, L3853, and R3851, provide the zero-DC bias required by the final device's Class C operation. L3852 and L3851 provide the DC path from base to ground. R3851 helps lower the network's Q at low frequencies. The collector DC network consists of L3875, L3876, R3876, C3880, C3885, C3881, C3882, and CR3875. This network provides the A+ voltage to the final stage while blocking RF from getting up the DC line. L3875 and L3876 provide the DC path and block RF. R3850 resistively loads down the final stage's collector at low frequencies and prevents unwanted oscillations. C3881, C3882, C3880, and C3885 are all bypass capacitors ranging from very low frequencies up to VHF frequencies. R3875 is the current sense resistor. CR3875 protects against reverse polarity. Finally, the RF signal goes through a low pass matching network (C3875, C3876, C3877, C3878, C3879, L3877, and associated transmission lines) to the rest of the output network (directional coupler, antennal switch, and harmonic filter).

### 3.7.1.3.2 Antenna Switch and Harmonic Filter

## Antenna Switch

The antenna switches impedance inverter circuit, made up of C3920 and L3920, takes the place of a quarter-wave microstrip line. During transmission, keyed 9.4 V forward biases CR3921, producing a low impedance on CR3921's anode and a high impedance on the C3920/L3920 node. Effectively, this isolates the transmitted power from the receiver. C3910 couples the power to the harmonic filter and on to the antenna.

Total TX to RX isolation exceeds 55dB from $136-174 \mathrm{MHz}$. The impedance inverter contributes approximately 35dB to transmit isolation. A second shunt switch made up of CR3922, L3921 and C3921, provide additional isolation. Capacitors C3922 and C3923 block DC.

During RX, CR3920 has an OFF capacitance of approximately 1 pF. CR3921 and CR3922 incorporated in the RX match have a similar OFF capacitance.

## Harmonic Filter

The 50-Watt harmonic filter is a 7-pole, low-pass filter, consisting of high Q chip capacitors (C3911 thru C3914) and discrete inductors (L3911 thru L3913). The filter's primary function is to attenuate harmonic spurs generated by the transmitter, It also adds low-pass selectivity for the receiver. L3914 protects the PA from static discharge.

### 3.7.1.3.3 Power Control Circuitry

## Command Board Circuitry

Inside U500, the Regulator Power Control IC (Figure 3-23), is an operational amplifier that has four inverting inputs, and non-inverting input at pin 44 which is the reference input for the entire power control loop of the power amplifier. The $3.2-\mathrm{V}$ reference voltage at $\mathrm{U} 500-44$ is produced by dividing SW $+5-\mathrm{V}$ with the voltage-divider circuit, R514 and R515.


REGULATOR/POWER CONTROL IC U500
Figure 3-23. Regulator/Power Control IC Block Diagram
The power control loop is controlled by the microprocessor U204 on the VOCON board. Through the SLIC IC U206, the microprocessor enables the RPCIC by pulling TX PA ENABLE (U500 pin 33) low while the radio synthesizer is locked (U500 pin 35). U520 writes data to a digital-to-analog converter, U502, to change and control the power-set voltage from pin 10 of U502 to pin 6 of U500. The voltage on this line, 1.5 to 5 V , will be inversely proportional to the power out of the PA , with 5 V producing the lowest power output. This voltage may be set with RSS (Radio Service Software) or CPS (Customer Programming Software).

## Control Voltage Limiter

R3807 and R3808 form a voltage divider that connects to control voltage drive. The output of this voltage divider is connected to the control-voltage-limit input, pin 4 of the RPCIC. If the voltage at this input reaches 3.2 V , then the control voltage will be clamped to a maximum value. For the $50-\mathrm{Watt}$ VHF PA, this maximum value is 8 V . This voltage control limit is set by the values of R3807 and R3808.

## Current Limiter

U204, the processor on the VOCON board, sends data to U502, the digital-to-analog converter, to properly set the voltage on U502, pin 15, which is the TX CURRENT LIMIT control line to the RPCIC, U500, pin 40 . Sixteen different voltages, ranging from 1.5 to 4.5 V , can be programmed from U502.
The collector current of the amplifier is monitored by sensing the voltage across R3875; CURRENT SENSE + connects to one end of R3875; CURRENT SENSE - connects to the other end. These lines connect to the command board on U500, pins 37 and 38 respectively. If the TX CURRENT LIMIT is set for 1.5 V , then the voltage difference between U500, pins 37 and 38 must be 0.1 V before the current through R 3875 is reduced. If U 500 , pin 40 is programmed for 4.5 V , then the difference of potential between pins 37 and 38 must exceed 0.3 V before current limiting begins, The voltage across R3875, where current sense occurs, can be determined by multiplying the voltage on U500, pin 40 , by 0.067 V . When current is being limited, the output of the operational amplifier, U500, pin 42 begins shutting down the conduction of Q503 and Q504, reducing PA control voltage, and reducing drive to the final amplifier to effectively control the final amplifier's maximum current.

## Forward Power Limiter

After the final amplifier, a parallel pair of non-symmetrical microstrip lines form a forward power-sensing directional coupler. Because of increased coupling with frequency, C3902 is used to compensate and filter out harmonics. R3905, R3906, C3902, and L3903 provide DC bias to CR3900, which rectifies the signal. During normal transmission, the DC voltage from the forward-detect line to the RPCIC ranges from 2 to 4.5 V . This voltage connects to U500, pin 9 , the directional coupler buffer input.

The directional coupler's output, U500 pin 8, is summed to pin 2 with the digital/analog buffer's output through R509 and R507 respectively.

Closed loop operation reduces the control amplifier's output pin 42, reduces the power module's gain, and reduces power output to maintain the coupler buffer output U500, pin 2 at 3.2 V regardless of the $\mathrm{D} / \mathrm{A}$ voltage level. If the $\mathrm{D} / \mathrm{A}$ voltage is high ( 4.5 V ), little detected voltage is needed to keep pin 2 at 3.2 V , and the power, consequently, is low. If the $\mathrm{D} / \mathrm{A}$ voltage is low ( 1.5 V ), a large forward detected voltage is needed to keep pin 2 at 3.2 V and power, consequently, is at maximum value. The voltage at pin 2 drops below 3.2 V under proper operation during low line voltage conditions where the PA cannot produce rated power, or if, under any conditions, the control voltage, or the final device current exceeds safe levels.

## Temperature Sensing

The temperature-sensing circuit of the PA works with the RPCIC to protect the PA devices from exclusively high temperatures. On the PA board, this circuit, formed by resistors R3878 thru R3880 and thermistor RT3877, provides a temperature-dependent voltage to the RPCIC via P0853, pin 7. As the PA temperature increases, the resistance of RT3875 decreases, causing the voltage at pin 7 to increase. This voltage is routed to the RPCIC, U500, pin 13, which is the input to the thermistor buffer. The buffer's output on pin 12 is connected to pin 2 via resistor R508. Note that pin 2 is the control amplifier input and is a summing point for temperature, forward-power detect, and power set signals. If the PA temperature becomes high enough so that the voltage at pin 7 exceeds 3.2 V , the thermistor buffer starts supplying current to the node at pin 2 . Due to the fixed output current of the power-set buffer, the control loop can maintain 3.2 V at pin 2 only by reducing the forward-power detect voltage and therefore, reducing the PA output power. Since power output is reduced, the generated heat is reduced to a safe level, If temperature decreases, the power output of the PA gradually increases to its nominal value. Temperature cutback should occur at about 140 degrees $F$ (60 degrees C).

The temperature sense circuitry can easily be tested by placing an ordinary leaded 4.7 k ohm across RT3875, PA output power should drop significantly if this circuit is working properly.

NOTE: Under severe environmental conditions, more than one circuit may be attempting to reduce power output at the same time (i.e. during high VSWR conditions). The current limiter may initially reduce power, but eventual heat buildup will cause further power reduction by the thermal cut-back circuit.

### 3.7.2 UHF Band Power Amplifiers

### 3.7.2.1 High-Power Amplifier

### 3.7.2.1.1 Transmitter

The high-power Spectra amplifier is discussed in the following text. A block diagram of the circuit is shown in.


Figure 3-24. UHF High-Power, Power Amplifier Block Diagram

## Transmit Low Level Amplifier (LLA)

The LLA is the first stage of the PA and provides a gain that is a function of a control voltage. This control voltage comes from the Regulator Power Control IC (RPCIC) on the command board. The magnitude of the control voltage depends on PA output power, temperature, and final amplifier current drain.

The LLA, Q5801, is unique in that its gain is controlled by varying the collectors current rather than its voltage. Q5801 and associated circuitry (Q5806, Q5800, R5805, and R5818) are best described as a voltage-controlled current source. This means that the collector current of Q5801 is controlled by the magnitude of the control voltage.

## Second Amplifier Stage

The second stage of the PA, Q5803, amplifies the output of the LLA to a level sufficient to drive the third stage device, Q5850. Q5803 amplifies the LLA output from approximately 250 mW to 2.5 Watts.

## Third Amplifier Stage

The third stage uses a 2.5 -Watt input to 16 -Watt output device. It is driven by the second stage through a matching circuit that consists of C5851, C5852 C5850, C5858, and L5850. L5851 and L 5852 give the device a zero-Vdc base bias (required for Class-C operation). The network of L5853, L5854, C5856, C5857, and R5850 provide A+ to the collector.

## Driver Stage

The driver stage uses a 15 -Watt input to 50 -Watt output device. It is driven by the third stage through the matching network consisting of C5853, C5854, C5855, C5861, C5862, and associated transmission lines. The DC bias path for the base is provided by L5855 and L5857. C5859, R5851, and C5860 are for the purpose of suppressing parasitic oscillations. Note that the capacitors C5861, C5862, C5863, and C5864 are placed on the bottom side of the PC board.

## Final Stage

The final amplifier stage is the parallel combination of two 25 -Watt input to 75 -Watt output RF transistors. The matching network from the collector of the driver device Q5851 to the bases of the final devices Q5875 and Q5876 utilizes transmission lines as part of a combination matching network and power splitter. The capacitors C5885, C5886, C5887, and C5888 are on the bottom side of the PC board underneath the base leads of Q5875 and Q5876.

The DC bias path for the base of Q5875 is via L5877 and L5879. Q5876 has a similar network. R5878 improves division of driver power between the final devices Q5875 and Q5876.
A feedback network consisting of C5890, R5879, and L5881 suppresses parasitic oscillations in Q5875. Q5876 has a similar network.

The final stage output network serves the dual purpose of impedance matching and power combining of the two final devices. C5891, C5892, C5893, and C5894 are on the bottom side of the PC board underneath the collectors of the final devices. These capacitors are especially critical in terms of their exact physical placement.

R5881 and R5882 help balance the load impedances presented to the collectors of the final devices. Filtered A+ is routed to the final amplifier devices via the current sense resistor R5875, the ferrite bead L5884, and the coil L5882. The final stage output network terminates at C5900 which is the input to the antenna switch. The circuit impedance is 50 ohms at this point.

### 3.7.2.1.2 Antenna Switch and Harmonic Filter

## Antenna Switch

The antenna switch utilizes PIN diodes to form a low loss, high isolation RF relay. During transmit, PIN diodes CR5900, CR5902, CR5904, and CR5905, are forward biased during transmit via the K9.4 supply and resistors R5901, R5900, R5908, and R5909. In this state, a low loss path exists from the final amplifier through PIN diode CR5900 and into the harmonic filter. PIN diodes CR5902, CR5904, and CR5905 effectively shunt the path to the receiver front-end, which protects the preamp or mixer device from excessive RF levels. A properly functioning switch will pass less than 10 mW of transmit power to the receiver front-end.
During receive, all four PIN diodes remain unbiased. This opens a low loss path from the harmonic filter to the receiver

## Harmonic Filter

The harmonic filter is a 9-pole low-pass filter consisting of screened plate capacitors and air-wound coils on a 0.035 inch thick ceramic substrate. The filter's primary function is to attenuate harmonic energy generated by the amplifier stages. The filter also adds some selectivity for the receiver.

### 3.7.2.1.3 Power Control Circuitry

## Command Board Circuitry

Inside U500, the Regulator Power Control IC (Figure 3-25) is an operational amplifier that has four inverting inputs, and one non-inverting input (at pin 44) which is the reference input for the entire power control loop of the power amplifier. The 3.2-V reference voltage at U500-44 is produced by dividing SW $+5-\mathrm{V}$ with the voltage-divider circuit, R514 and R515.

The power control loop is controlled by the microprocessor U204 on the VOCON board. Through the SLIC IC U206, the microprocessor enables the RPCIC by pulling TX PA ENABLE (U500 pin 33) low while the radio synthesizer is locked (U500 pin 35). U520 writes data to a digital-to-analog converter, U502, to change and control the power-set voltage from pin 10 of U502 to pin 6 of U500. The voltage on this line, 1.5 to 5 V , will be inversely proportional to the power out of the PA , with 5 V producing the lowest power output. This voltage may be set with RSS (Radio Service Software) or CPS (Customer Programming Software).


REGULATOR/POWER CONTROL IC U500
Figure 3-25. RPCIC Block Diagram

## Control Voltage Limiter

R5807 and R5808 form a voltage divider that connects to control voltage drive. The output of this voltage divider is connected to the control-voltage-limit input (pin 4) of the RPCIC. If the voltage at this input reaches 3.2 V , then the control voltage will be clamped to a maximum value. For the high-power UHF PA, this maximum value is 10 V . This voltage control limit is set by the values of R5807 and R5808.

## Current Limiter

U204, the processor on the VOCON board, sends data to U502, the digital-to-analog converter, to properly set the voltage on U502, pin 15, which is the TX CURRENT LIMIT control line to the RPCIC (U500, pin 40). Sixteen different voltages, ranging from 1.5 to 4.5 V , can be programmed from U502.

The collector current of the high-power amplifier is monitored by sensing the voltage across R5875. CURRENT SENSE + connects to one end of R5875; CURRENT SENSE - connects to the other end. These lines connect to the command board on U500 pins 37 and 38 , respectively. If the TX CURRENT LIMIT is set for 1.5 V , then the voltage difference between U500 pins 37 and 38 must be 0.1 V before the current through R5875 is reduced. If U 500 pin 40 is programmed for 4.5 V , then the difference of potential between pins 37 and 38 must exceed 0.3 V before current limiting begins. The voltage across R5875, where current sense occurs, can be determined by multiplying the voltage on U500, pin 40 by 0.067 . When current is being limited, the output of the op-amp (U500, pin 42) begins shutting down the conduction of Q503 and Q504, reducing PA control voltage, and reducing drive to the final amplifier to, effectively, control the final amplifier's maximum current.

## Forward Power Limiter

After the harmonic filter a parallel pair of microstrip lines form a forward power sensing directional coupler and detector. The output of this directional coupler/detector is a DC voltage that is proportional to the forward RF power from the final amplifier. During normal transmission, the DC voltage from the forward detect line to the RPCIC ranges from 2 to 5.0 V .

This voltage connects to U500 pin 9, the directional coupler buffer input.
The directional coupler's buffered output, U500 pin 8, is summed to pin 2 with the digital/analog buffer's output through R509 and R507, respectively. In typical operation, the closed loop operation of the circuit attempts to keep the voltage at U500 pin 2 a constant value of 3.2 V . The control amp will maintain this condition by increasing or decreasing the control amp output voltage. This control amp output voltage is routed to the LLA via transistors Q503 and Q504. The output of Q504 is designated "control voltage drive" and is routed to J 1 pin 2 of the PA board.
Since control voltage drive controls the gain of the LLA, it determines the drive level to the following stages and thus the output power of the final amplifier. The output power of the final stage is detected by the directional coupler and is routed back to U 500 pin 2 via the buffer and R507. Thus the loop is complete and forward power is maintained a constant value. The voltage at pin 2 will drop below 3.2 V during low line voltage conditions where the PA cannot produce rated power. Current limit and voltage control limit circuits will also affect the voltage at pin 2 as described in the following discussion on temperature sensing.

## Temperature Sensing

The temperature-sensing circuit of the PA works with the RPCIC to protect the PA devices from excessively high temperatures. On the PA board, this circuit, (formed by resistors R5857, R5843, R5858, and thermistor RT5875), provides a temperature dependent voltage to the RPCIC via J1 pin 6. As the PA temperature increases, the resistance of RT5875 decreases, causing the voltage at pin 6 to increase. This voltage is routed to the RPCIC, U500 pin 13, which is the input to the thermistor buffer. The buffer's output on pin 12 is connected to pin 2 via resistor R508. Note that pin 2 is the control amp input and is a summing point for temperature, forward-power detect, and power set signals. If the PA temperature becomes high enough so that the voltage at pin 7 exceeds 3.2 V , the thermistor buffer starts supplying current to the node at pin 2 . Due to the fixed output current of the power-set buffer, the control loop can maintain 3.2 V at pin 2 only by reducing the forward-power detect voltage and, therefore, reducing the PA output power. Since power output is reduced, the generated heat is reduced to a safe level. If temperature decreases, the power output of the PA gradually increases to its nominal value.
NOTE: Under severe environmental conditions, more than one circuit may be attempting to reduce power output at the same time (i.e., during high VSWR conditions, the current limiter may initially reduce power, but eventual heat buildup will cause further power reduction by the thermal cut-back circuit).

### 3.7.2.2 40-Watt Power Amplifier

### 3.7.2.2.1 Transmitter

The 40-Watt ASTRO Spectra power amplifier is discussed in the following text.

## Transmit Low Level Amplifier (LLA)

NOTE: The minimum input drive level to the PA into P5850 is 30 mW . Refer to the synthesizer section if input drive is less than 30 mW .

The Low Level Amplifier, the first stage of the PA, provides a gain that is a function of a control voltage. This control voltage comes from the Regulator Power Control IC (RPCIC) on the command board. The magnitude of the control voltage depends on PA output power, temperature, and final amplifier current drain.
The LLA, Q5801, is unique in that its gain is controlled by varying the collector's current rather than its voltage. Q5801 and associated circuitry (Q5806, Q5800, R5805, and R5818) are best described as a voltage-controlled current source. This means that the collector current of Q5801 is controlled by the magnitude of the control voltage. Proper operation of the LLA can be checked by monitoring the voltage across the resistor R5805 The voltage should measure in the range of 0.1 to 1.0 V , depending on the value of control voltage. A $0.1-\mathrm{V}$ reading corresponds to a low control voltage ( 1 to 5 V ) and a $1.0-\mathrm{V}$ reading corresponds to a high control voltage (up to control voltage limit).

## Predriver Stage

The second stage of the PA, Q5803, is the predriver which amplifies the output of the LLA to a level sufficient to operate the driver device, Q5850. This stage amplifies the LLA output from, approximately, 250 mW in to 2.0 Watts out.

## Driver Stage

The driver is a six-leaded 2.5 - to 16 -Watt device. It is driven by the predriver device through a matching circuit that consists of C5851, C5852, C5850, C5858, and L5850. L5851 and L5852 give the driver a zero-DC bias (required for the driver's Class-C operation). L5852, a ferrite bead, helps lower the driver base Q and prevent unwanted oscillations. The network of L5853, L5854, C5856, C5857, and R5850 provide A+ to the collector. L5853 and L5854 provide the DC path and block RF from coming up the DC line. R5850 resistively loads down the collector at low frequencies, preventing unwanted oscillations. C5856 and C5857 are bypass capacitors.

## Final Stage

The final device is a six-leaded 15 - to 50 -Watt device and is driven by the driver through a quasi-low pass matching circuit that consists of C5853, C5854, C5855, C5875, C5876, and associated transmission lines. Base network, L5875, L5876, L5883, C5891, R5881, and R5882, provide the zero-DC bias required by the final device's Class-C operation. L5875, L5876, and L5883 provide the DC path from base to ground. C5891, in parallel with L5875, presents a high impedance at UHF frequencies, thus minimizing RF losses in the base network. R5881, R5882, and L5883 resistively load down the base at low frequencies, thus preventing unwanted oscillations. The collector DC network consists of L5878, L5879 R5879, R5880, R5883, R5884, R5875, C5881, C5883 C5884, C5885, C5886, C5893, and CR5875. This network provides the A+ voltage to the final while blocking RF from getting up the DC line. L5878 and L5879 provide the DC path and block RF. R5879, R5880, R5883, and R5884 resistively load down the final's collector at low frequencies and prevent unwanted oscillations. C5881, C5883, C5884, C3885, C5886 and C5893 are all bypass capacitors ranging from very low frequencies up to UHF frequencies. R5875 is the current-sense resistor. CR5875 protects against reverse polarity. Finally, the power goes through a low-pass matching network (C5877, C5878, C5887, C5892, C5880, and associated transmission lines) to the rest of the output network (Directional Coupler, Antenna Switch, and Harmonic Filter).

### 3.7.2.2.2 Antenna Switch and Harmonic Filter

## Antenna Switch

The antenna switch's impedance inverter circuit, made up of C5923 and L5921, takes the place of a quarter-wave microstrip line. During transmission, Keyed 9.4 V forward-biases CR5921, producing low impedance on CR5921's anode and high impedance on the C5923/L5921 node. Effectively, this isolates the transmitted power from the receiver, C5922 couples the power to the harmonic filter and on to the antenna.

Total TX to RX isolation exceeds 45 dB from $450-512 \mathrm{MHz}$. The impedance inverter contributes approximately 35 dB to transmit isolation. A second shunt switch, made up of CR5922, L5922, and C5924, provides additional isolation. C5926 and C5927 block DC.

During RX, CR5920 has an OFF capacitance of approximately 1 pF , which is tuned out by L5904. CR5921 and CR5922, incorporated in the RX match, have similar OFF capacitances.

## Harmonic Filter

The 40-Watt harmonic filter is a 7 -pole, low-pass filter, consisting of screened plate capacitors and discrete inductors ( 1,5924, L5925, and L5926) on a 35 -mil alumina substrate. The filter's ground plane is attached to the PA printed circuit board with solder, while input and output connections are made via MP5901 and MP5902. The filter's primary function is to attenuate harmonic spurs generated by the transmitter. It also adds low-pass selectivity for the receiver. L5910, grounded through MP5903, protects the PA from static discharge.
NOTE: When removing any of the discrete coils, take care to avoid leaching the plate capacitor metallization. Removal of the entire hybrid is best accomplished by heating the hybrid/PC board assembly with a heat gun or heat blower until the solder joint reflows.

### 3.7.2.2.3 Power Control Circuitry

## Command Board Circuitry

Inside U500, the Regulator Power Control IC (Figure 3-26), is an operational amplifier that has four inverting inputs, and one non-inverting input (at pin 44) which is the reference input for the entire power control loop of the power amplifier. The 3.2-V reference voltage at U500-44 is produced by dividing SW $+5-\mathrm{V}$ with the voltage-divider circuit, R514 and R515.


Figure 3-26. RPCIC Block Diagram
The power control loop is controlled by the microprocessor U204 on the VOCON board. Through the SLIC IC U206, the microprocessor enables the RPCIC by pulling TX PA ENABLE (U500 pin 33) low while the radio synthesizer is locked (U500 pin 35). U520 writes data to a digital-to-analog converter, U502, to change and control the power-set voltage from pin 10 of U502 to pin 6 of U500. The voltage on this line, 1.5 to 5 V , will be inversely proportional to the power out of the PA , with 5 V producing the lowest power output. This voltage may be set with RSS (Radio Service Software) or CPS (Customer Programming Software).

## Control Voltage Limiter

R5807 and R5808 form a voltage divider that connects to control voltage drive. The output of this voltage divider is connected to the control-voltage-limit input ( pin 4) of the RPCIC. If the voltage at this input reaches 3.2 V , then the control voltage will be clamped to a maximum value. For the $40-$ Watt UHF PA, this maximum value is 10 V . This voltage-control limit is set by the values of R5807 and R5808.

## Current Limiter

U204, the processor on the VOCON board, sends data to U502, the digital-to-analog converter, to properly set the voltage on U502, pin 15, which is the TX CURRENT LIMIT control line to the RPCIC (U500, pin 40). Sixteen different voltages, ranging from 1.5 to 4.5 V , can be programmed from U502.
The collector current of the 40-Watt amplifier is monitored by sensing the voltage across R5875. CURRENT SENSE + connects to one end of R5875; CURRENT SENSE - connects to the other end. These lines connect to the command board on U500, Pins 37 and 38 , respectively. If the TX CURRENT LIMIT is set for 1.5 V , then the voltage difference between U500, Pins 37 and 38 must be 0.1 V before the current through R5875 is reduced. If U500, pin 40 is programmed for 4.5 V , then the difference of potential between Pins 37 and 38 must exceed 0.3 V before current limiting begins. The voltage across R5875, where current sense occurs, can be determined by multiplying the voltage on U500, pin 40, by 0.067 . When current is being limited, the output of the op-amp (U500, pin 42) begins shutting down the conduction of Q503 and Q504, reducing PA control voltage, and reducing drive to the final amplifier to, effectively, control the final amplifier's maximum current.

## Forward Power Limiter

After the final amplifier, a parallel pair of microstrip lines form a forward power-sensing directional coupler. Because of increased coupling with frequency, C5903, L5902, C5904, L5903, and C5905 are used to compensate and filter out harmonics. CR5900 rectifies the signal. R5904, R5905, and RT5904 provide thermal compensation. During normal transmission, the DC voltage from the forward-detect line to the RPCIC ranges from 2 to 4.5 V . This voltage connects to U500, pin 9 , the directional coupler buffer input.
The directional coupler's output, U500 pin 8, is summed to pin 2 with the digital/analog buffer's output through R509 and R507, respectively. Closed loop operation reduces the control amp's output ( pin 42), reduces the power module's gain, and reduces power output to maintain the coupler buffer output (U500, pin 2) at 3.2 V regardless of the D/A voltage level. If the D/A voltage is high ( 4.5 V ), little detected voltage is needed to keep pin 2 at 3.2 V , and the power, consequently, is low. If the D/A voltage is low ( 1.5 V ), a large forward detected voltage is needed to keep pin 2 at 3.2 V and power, consequently, is at maximum value. The voltage at pin 2 drops below 3.2 V under proper operation during low line voltage conditions where the PA cannot produce rated power, or if, under any conditions, the control voltage or the final device current exceeds safe levels.

## Temperature Sensing

The temperature-sensing circuit of the PA works with the RPCIC to protect the PA devices from excessively high temperatures. On the PA board, this circuit, formed by resistors R5878, R5876, R5877, and thermistor RT5875, provides a temperature-dependent voltage to the RPCIC via P0853, pin 7. As the PA temperature increases, the resistance of RT5875 decreases, causing the voltage at pin 7 to increase. This voltage is routed to the RPCIC, U500, pin 13, which is the input to the thermistor buffer. The buffer's output on pin 12 is connected to pin 2 via resistor R508. Note that pin 2 is the control amp input and is a summing point for temperature, forward-power detect, and power set signals. If the PA temperature becomes high enough so that the voltage at pin 7 exceeds 3.2 V , the thermistor buffer starts supplying current to the node at pin 2 . Due to the fixed output current of the power-set buffer, the control loop can maintain 3.2 V at pin 2 only by reducing the forward-power detect voltage and, therefore, reducing the PA output power. Since power output is reduced, the generated heat is reduced to a safe level. If temperature decreases, the power output of the PA gradually increases to its nominal value. Temperature cutback should occur at about 140 F (60 C).

The temperature sense circuitry can easily be tested by placing an ordinary leaded 6.8 k ohm resistor across RT5875. PA output power should drop significantly if this circuit is working properly.

NOTE: Under severe environmental conditions, more than one circuit may be attempting to reduce power output at the same time (i.e., during high VSWR conditions, the current limiter may initially reduce power, but eventual heat buildup will cause further power reduction by the thermal cut-back circuit).

### 3.7.3 $\mathbf{8 0 0}$ MHz Band Power Amplifiers

### 3.7.3.1 15- and 35-Watt Amplifiers

### 3.7.3.1.1 Transmitter

The 15-Watt and 35 -Watt ASTRO Spectra power amplifiers are discussed in the following text.

## Transmit Buffer

The PA receives 18 to 23 dBm ( 60 to 200 mW ) at the transmit injection (TX INJ) coax. The first stage, TX BUFFER, uses adaptive biasing which varies the base voltage inversely proportional to the input drive level. With Keyed $9.4 \mathrm{~V}(\mathrm{K9.4)}$ ) ON and NO DRIVE, Q9800 base voltage should equal the voltage drop across CR9800. R9801 sets the diode current, and R9802 sets the base voltage referenced from CR9800. At the input, L9804, C9801, and C9800 are for matching while C9808 and R9806 prevent interfacing instability. L9800 is the base feed choke and L9801 is the collector choke. R9800 parallels L9800 for added stability. L9805, and C9803 are on the buffer's supply (K9.4) for stability. C9802 and L9802 are for output matching. L9803 and C9804 are added as a "suckout" for half carrier. Like the input, C9807 and R9805 were added at the output to help prevent interfacing instability. The power output of this stage should be greater than 325 mW ( 25 dBm ).

The TX Buffer applies the modulated RF signal to pin 1 of U9850, the Power Amplifier Module, which is a 5 -pin, 20-Watt, three-stage amplifier. The control voltage from the power control series-pass transistor, Q9500, controls the gain of the first two amplifier stages of U9850, through pin 2 and pin 3. Battery voltage ( $\mathrm{A}+$ ), connected to pin 4, powers the third stage.

## Power Module

The power module (U9850) is the major gain block for both the 15 - and 35 -Watt amplifiers. The 50 -ohm input and output impedances connect to adjacent power stages via 50 -ohm microstrip lines. The parallel resistor, R9805, and capacitor C9807, on the input, reduce circuit response at lower frequencies and improve stability. The 350 mW (typical) input power is increased to approximately 15 Watts. The amplifier power is monitored by the power control IC on the command board and adjusted by controlling the voltage on U9850, Pins 2 and 3 . A+ is applied directly to the final stage inside the power module via pin 4 . No repairs can be made to the module; damaged or failed units must be replaced.

The power module leads will not tolerate undue stress; handle carefully when repairing.

## Final Stage (35-Watt Only)

On the 15 -Watt radio, the transmit RF signal from U9850, pin 5 , is applied to the 50 -ohm microstrip directional coupler. On the $35-\mathrm{Watt}$ radio, the transmit RF signal is applied to the emitter of the final power amplifier Q9880 through the coupling capacitor C9856, the 50 -ohm quarter-wave matching transmission line, and the matching capacitors C9875 and C9876. The 100-ohm coupling line, L9930, R9930, R9931, CR9930, and C9930 form an interstage power detector between U9850 and Q9880 to limit the drive into Q9880 to about 17 Watts. L9875, the emitter choke, is also the emitter DC return. The final power amplifier, Q9880, is a $45-$ Watt, 800 MHz , common-base NPN devise. The Q9880 output match consists of C9877, C9878, a section of the 50-ohm microstrip line, C9879 and the DC blocking capacitor, C9883. L9876 isolates the RF signal from A+. C9880 and C9884 are signal frequency bypass capacitors. L9877 presents a high impedance at low RF frequencies; therefore the collector of Q9880 is resistively loaded by R9876 at low frequencies where the gain is much greater. C9881 and C9882 are low frequency bypass capacitors.

### 3.7.3.1.2 Antenna Switch and Harmonic Filter

## Antenna Switch

35-Watt Power Amplifier:
The antenna switch's impedance inverter circuit, made up of C9922 and L9921, takes the place of a quarter-wave microstrip line. During transmission, K9.4-V forward-biases CR9921, producing a low impedance at its anode end, and a high impedance at the node of C9922 and L9921, to effectively isolate the transmitted power from the receiver. C9921 couples the power to the harmonic filter and on to the antenna.

The impedance inverter contributes approximately 30 dB to transmit isolation. Additional isolation is obtained by the series switch made up of CR9922, L9923, and associated DC bias components. During transmit, CR9922 is reverse-biased, thus creating a small series capacitor that is tuned out by L9923. C9925 is a DC blocking capacitor. The high impedance of the series arm works against the low impedance of the shunt arm (CR9921) to provide approximately 10 to 15 dB additional isolation. Total TX to RX isolation is in excess of 45 dB from $851-870 \mathrm{MHz}$. The preselector provides over 50 dB isolation from $806-824 \mathrm{MHz}$.

When receiving, CR9920 has an off capacitance of approximately 1 pF , which is tuned out by L9926. CR9921, with similar off capacitance, is incorporated in the RX match. CR9922 is forward-biased with an ON resistance of approximately 1 ohm. The signal passes CR9922 and through L9922, a series inductor used to complete the RX match. Capacitor C9929 blocks DC.

L9910, at the node of the antenna and harmonic filter, protects the PA from static discharge.

## 15-Watt Power Amplifier:

The theory for the 15 -Watt antenna switch is exactly the same as the 35 -Watt except that some of the components are labeled with different numbers. C9921, in the 15-Watt PA, is located after the harmonic filter.

L9922, at the node of the antenna and capacitor C9921, protects the PA from static discharge.

## Harmonic Filter

The 15- and 35 -Watt harmonic filters are 7 -pole, low-pass filters implemented with screened plate capacitors and discrete inductors (L9911, L9912, and L9913) on a 35 mil ( 0.035 ") alumina substrate. The filter's ground plane is attached to the PA printed circuit board with solder, while input and output connections are made via "J"-straps MP9856 and MP9857. The filter's primary function is to attenuate harmonic spurs generated by the transmitter and to provide additional low-pass selectivity for the receiver.

NOTE: When removing any of the discrete coils, take care to avoid leaching the plate capacitor metallization. Removal of the entire hybrid is best accomplished by heating hybrid/PC board assembly with a heat gun or heat blower until solder joint reflows.

### 3.7.3.1.3 Power Control Circuitry

## Command Board Circuitry

Inside U500, the Regulator Power Control IC (Figure 3-27), is an operational amplifier that has four inverting inputs, and one non-inverting input (at pin 44) which is the reference input for the entire power control loop of the power amplifier. The $3.2-\mathrm{V}$ reference voltage at U500, pin 44, is produced by dividing SW +5-V with the voltage-divider circuit, R514 and R515.
The power control loop is controlled by the microprocessor U204 on the VOCON board. Through the SLIC IC U206, the microprocessor enables the RPCIC by pulling TX PA ENABLE (U500 pin 33) low while the radio synthesizer is locked (U500 pin 35). U520 writes data to a digital-to-analog converter, U502, to change and control the power-set voltage from pin 10 of U502 to pin 6 of U500. The voltage on this line, 1.5 to 5 V , will be inversely proportional to the power out of the PA, with 5 V producing the lowest power output. This voltage may be set with RSS (Radio Service Software) or CPS (Customer Programming Software).


Figure 3-27. RPCIC Block Diagram

## Power Module Control Voltage Limiter

R9562 and R9563 connect in series to the emitter of Q9500. The ratio of R9563 and R9562 feed a portion of the control voltage (U9850, Pins 2 and 3 ) to U500, pin 4 . When pin 4 exceeds 3.2 V , the output of the control op-amp (U500, pin 42) is reduced. Eventually, this reduces the control voltage available to the power module (U9850).

The input RF power to the 45-Watt amplifier Q9880) must stay below 17 Watts. Power is coupled from the inter-stage 50 -ohm transmission line to a 100 ohm transmission line and rectified by CR9930 on the PA, producing a DC voltage on U500, pin 4. IF this voltage exceeds 3.2 V , the output voltage on U500, pin 42, is reduced, lowering the control voltage and reducing U9850's gain until its RF output power is approximately 17 Watts.

## Current Limiter

U204, the processor on the VOCON board, sends data to U502, the digital to analog converter, to properly set the voltage on U502, pin 15 , which is the TX CURRENT LIMIT control line to the RPCIC (U500, pin 40). Sixteen different voltages, ranging from 1.5 to 4.5 V , can be programmed from U502.

The collector current of the 45 -Watt final amplifier (in the 35 -Watt PA only) is monitored by sensing the voltage across R9875. CURRENT SENSE + connects to one end of R9875 and CURRENT SENSE - connects to the other end. These lines connect to the command board on U500, Pins 37 and 38 , respectively. If the TX CURRENT LIMIT is set for 1.5 V , then the voltage difference between U500, Pins 37 and 38 must be 0.1 V before the current through R9785 is reduced. If U500, pin 40 is programmed for 4.5 V , then the difference of potential between Pins 37 and 38 must exceed 3 V before current limiting begins. The voltage across R9875, where current sense occurs, can be determined by multiplying the voltage on U500, pin 40 , by 0.067 . When current is being limited, the output of the op-amp (U500, pin 42), begins shutting down the conduction of Q503 and Q504, reducing base drive to Q9500, reducing drive to the final amplifier to, effectively, control the final amplifier's maximum current.

## Forward Power Limiter

The parallel pair of microstrip lines after the final amplifier, form a forward power sensing directional coupler. Because the coupling increases with frequency, the compensation network of L9806 and C9901 is used. CR9900 rectifies the signal, C9900 filters it, and R9905 and R9904 form a voltage divider. During normal transmission, the DC voltage from the forward detect line to the RPCIC ranges from 2 to 4.5 V . This voltage connects to U 500 , pin 9 , the input to the directional coupler buffer.

The directional coupler's output, U 500 , pin 8 , is summed to pin 2 with the digital/analog buffer's output through R509 and R507, respectively. Closed loop operation reduces the control amp's output ( pin 42), reduces the power module's gain, and reduces power output to maintain the coupler buffer output U 500 , pin 2 ) at 3.2 V regardless of the $\mathrm{D} / \mathrm{A}$ voltage level. If the $\mathrm{D} / \mathrm{A}$ voltage is high ( 4.5 V ), little detected voltage is needed to keep pin 2 at 3.2 V , and the power, consequently, is low. If the D/A voltage is low ( 1.5 V ), a large forward detected voltage is needed to keep pin 2 at 3.2 V and power, consequently, is at maximum value. The voltage at pin 2 drops below 3.2 V under proper operation during low line voltage conditions where the PA cannot produce rated power, or if, under any conditions, either the inter-stage power (in 35-Watt models only), the control voltage, or the final device current exceeds safe levels.

### 3.7.3.1.4 Temperature Sensing

When the radio is keyed, K9.4-V is applied to pin 5 of the PA connector and on one side of thermistor RT9560. As the temperature increases, the resistance of RT9560 decreases, creating more voltage across R9561. This temperature voltage is routed via PA connector pin 7 back to U500, pin 13, which is the input to a thermistor buffer. The thermistor buffer's output on pin 12 is summed to U500, pin 2, and passes through its scaling resistor, R508. When the temperature of the RT9560 causes its value to change enough that the voltage exceeds 3.2 V , the thermister buffer starts supplying current to the node at pin 2 . Due to the fixed output of the D/A, the control loop can maintain 3.2 V at pin 2 only by reducing power out and reducing the forward detected voltage. Since output is reduced, the generated heat is held to a safe level. As temp decreases, the power output of the PA gradually increases to its nominal value.

Q9515 and Q9510 switch A+ to one side of R9513. R9513 sums the A+ voltage into the same node as TEMPSENSE. Together with temp-sense the circuitry protects the power amplifier from unsafe operating conditions of high line and high temp.

NOTE: Under severe environmental conditions more than one circuit may be attempting to reduce power output at the same time (i.e., during high VSWR conditions, the inter-stage power limit may initially reduce power, but eventual heat build-up will cause further power reduction by the thermal cut-back circuit).

## This Page Intentionally Left Blank

## Chapter 4 Troubleshooting Procedures

### 4.1 ASTRO Spectra Procedures

This section will aid you in troubleshooting a malfunctioning ASTRO Digital Spectra radio. It is intended to be detailed enough to localize the malfunctioning circuit and isolate the defective component.

NOTE: Refer to "4.2 ASTRO Spectra Plus Procedures" on page 4-10 for troubleshooting information specific to the ASTRO Spectra Plus radio.


## Caution

Most of the ICs are static-sensitive devices. Do not attempt to troubleshoot or disassemble a board without first referring to the following Handling Precautions section.

### 4.1.1 Handling Precautions

Complementary metal-oxide semiconductor (CMOS) devices and other high-technology devices, are used in this family of radios. While the attributes of these devices are many, their characteristics make them susceptible to damage by electrostatic discharge (ESD) or high-voltage charges. Damage can be latent, resulting in failures occurring weeks or months later. Therefore, special precautions must be taken to prevent device damage during disassembly, troubleshooting, and repair. Handling precautions are mandatory for this radio, and are especially important in lowhumidity conditions. DO NOT attempt to disassemble the radio without observing the following handling precautions.

1. Eliminate static generators (plastics, Styrofoam, etc.) in the work area.
2. Remove nylon or double-knit polyester jackets, roll up long sleeves, and remove or tie back loose hanging neckties.
3. Store and transport all static-sensitive devices in ESD-protective containers.
4. Disconnect all power from the unit before ESD-sensitive components are removed or inserted unless otherwise noted.
5. Use a static-safeguarded workstation, which can be accomplished through the use of an antistatic kit (Motorola part number 01-80386A82). This kit includes a wrist strap, two ground cords, a static-control table mat and a static-control floor mat.
6. Always wear a conductive wrist strap when servicing this equipment. The Motorola part number for a replacement wrist strap that connects to the table mat is 42-80385A59.

### 4.1.2 Voltage Measurement and Signal Tracing

In most situations, the problem circuit may be identified using a dc voltmeter, RF millivoltmeter, and oscilloscope (preferably with 100 MHz bandwidth or more). The "Recommended Test Equipment, Service Aids, and Tools" section in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20) outlines the recommended tools and service aids which would be useful. Of special note are:

- 30-80370E06 Extender Cable which provides an extension cable for VOCON board connector J501 and command board connector P501.
- RPX-4725A Command and Control Service Cable Kit which provides extension cables for servicing digital and analog circuits.
- RPX-4724A RF Service Cable Kit which provides interface cables needed to service the RF boards.

In some cases dc voltages at probe points are shown in red on the schematics. In other areas diagrams are included to show time-varying signals, which should be present under the indicated circumstances. It is recommended that a thorough check be made prior to replacement of any IC or part. If the probe point does not have a signal reasonably close to the indicated one, a check of the surrounding components should be made prior to replacing any parts.


Caution

When checking a transistor or module, either in or out of circuit, do not use an ohmmeter having more than 1.5 Vdc appearing across test leads or use an ohms scale of less than $\times 100$.

### 4.1.3 Power-Up Self-Check Errors

Each time the radio is turned on the MCU and DSP perform some internal diagnostics. These diagnostics consist of checking the programmable devices such as the FLASH ROMs, internal and external EEPROMs, SRAM devices, and ADSIC configuration bus checksum. At the end of the power-up self-check routines, if an error exists, the appropriate error code is shown on the display. Self-test errors are classified as either "fatal" or "non-fatal." Fatal errors will inhibit user operation; non-fatal errors will not. For non-display radios, the error codes may be read using the Radio Service Software (RSS) from the SB9600 bus on the universal connector. Table 4-1 lists self-check error codes, describes the codes, and recommends troubleshooting charts for investigating the cause of the failure.

Table 4-1. Power-Up Self-Check Error Codes

| Error <br> Code | Description | Troubleshooting Chart |
| :--- | :--- | :--- |
| $01 / 02$ | External EEPROM checksum non-fatal error | Chart C.2 (p. 4), C.7 (p. 8) |
| $01 / 81$ | ROM checksum failure | Chart C.6 (p. 7) |
| $01 / 82$ | External EEPROM checksum failure | Chart C.2 (p. 4), C.7 (p. 8) |
| $01 / 84$ | EEPROM is blank | Chart C.2 (p. 4), C.8 (p. 8) |
| $01 / 88$ | RAM failure - Note: Not a checksum failure | Chart C.2 (p. 4), C. 9 (p. 9) |
| $01 / 90$ | General hardware failure | Chart C.2 (p. 4), C. 5 (p. 7) |

Table 4-1. Power-Up Self-Check Error Codes (Continued)

| Error Code | Description | Troubleshooting Chart |
| :---: | :---: | :---: |
| 01/92 | Internal EEPROM checksum failure | Chart C. 10 (p.9) |
| 02/81 | DSP ROM checksum failure | Chart C. 12 (p. 10) |
| 02/82 | DSP RAM 1 failure | Chart C. 15 (p. 12) |
| 02/84 | DSP RAM 2 failure | Chart C. 14 (p.11) |
| 02/88 | DSP RAM failure - Note: Not a checksum failure | Chart C. 13 (p. 11) |
| 02/90 | General DSP hardware failure (DSP start-up message not received correctly) | Chart C. 16 (p. 12) |
| 02/A0 | ADSIC checksum failure | Chart C. 11 (p. 10) |
| 09/10 | Secure option not communicating with radio | Chart C. 17 (p. 13) |
| 09/90 | Secure hardware failure | Chart C. 18 (p.13) |

In the case of multiple errors, the codes are logically OR'd and the results displayed. As an example, in the case of an ADSIC checksum failure and a DSP ROM checksum failure, the resultant code would be 02/A1. Following is a series of troubleshooting flowcharts which relate to each of these failure codes.

### 4.1.3.1 Power-Up Sequence

Upon RESET* going active, the MCU begins to execute code which is pointed to by the vector stored at \$FFFE, \$FFFF in the FLASH ROM. The execution of this code is as follows:

1. Initialize the MCU (U204).
2. The control head's MCU turns on the:

- Green LED for the W3 model.
- TX and Busy LEDs for the W4, W5, W7 and W9 models.

3. Initialize the SLIC (U206).
4. CONFIG register check. If the CONFIG register is not correct, the MCU will repair it and loop.
5. Start ADSIC/DSP:

- Bring the ADSIC reset line high.
- Wait 2 ms .
- Bring the DSP reset line high.

6. Start EMC:

- Set the EMC wake-up line low (emc irq line).
- Wait 5 ms .
- Set the EMC wake-up line high.
- Wait 10 ms .
- Set the EMC wake-up line low (emc irq line).
- Wait 5ms.
- Set the EMC wake-up line high.

7. Begin power-up self-tests.
8. Begin RAM tests:

- External RAM (\$1800-3FFF).
- Internal RAM (\$1060-\$1300).
- External RAM (\$0000-\$0DFF).
- Display $01 / 88$ if failure.

The radio will get stuck here if the internal RAM is defective. The radio uses the internal RAM for stack. The RAM routines use subroutines. Thus, if the internal RAM is defective, the radio will get lost testing the external RAM.
9. Begin MCU (host $\mu \mathrm{C}$ ) ROM checksum test.

- Fail 01/81 if this routine fails.

10. Begin DSP power-up tests. The MCU will try this five times before it fails the DSP test.

- Check for HF2.
- Fail 02/90 if 100 ms .
- Program the ADSIC.
- Wait for the DSP power-up message.
- Fail 02/A0 if 300 ms .
- Fail 02/A0 if wrong message from the DSP.
- Wait for the DSP status information.
- Fail 02/90 if 100 ms .
- Fail 02/88 if DSP RAM (U414) fails.
- Fail 02/84 if DSP RAM U403 fails.
- Fail 02/82 if DSP RAM U402 fails.
- Fail 02/81 if DSP RAM fails.
- Wait for the ADSIC checksum.
- Fail 02/A0 if 100 ms .
- Fail 02/A0 if failure.
- Wait for the first part of the DSP version number.
- Fail 02/90 if 100 ms .
- Wait for the second part of the DSP version number.
- Fail 02/90 if 100 ms .

11. Checksum the codeplug.

- Test internal codeplug checksums.
- Fail 01/92 if failure.
- Test external codeplug checksums.
- Error 01/02 if non-fatal error; fail 01/82 if fatal error.

12. Power-up the EMC (if it is enabled in the codeplug).
13. Turn off the green LED.
14. Start up operating system.
15. Display for one second:

- "SELF TEST" for the W3, model.
- "SELF CHK" for the W4, W5, and W7 models.
- "SELF CHECK" for the W9 models.

16. Turn off the green LED in the W3 model, or the TX and Busy LEDs in the W4, W5, W7, and W9 models.

Display errors if a fatal error exists at this time.

### 4.1.4 RF Board Troubleshooting

This information will help you troubleshoot the ASTRO Spectra Radio RF board. Use this information, along with the Theory of Operation, to diagnose and isolate the cause of failures. The principal tools needed to troubleshoot a circuit to the component level are the schematic and the Theory of Operation.

In addition to the schematic and theory, the following troubleshooting information identifies tests and checks designed to help isolate problems.
Prior to troubleshooting, it is important to review the Theory of Operation, including specific precautions and troubleshooting methods. Because much of the radio's circuitry operates at high frequencies, measurements must be taken very carefully. Notes and cautions are added to the text to alert the reader to this need in areas of greatest sensitivity. However, the need for extreme care does exist in all measurements and tests.

### 4.1.4.1 Display Flashes "FAIL 001"

This display indicates a synthesizer "out-of-lock" condition. Check the dc power supplies for the correct voltages at the following locations:

Table 4-2. Voltage by Location

| VOLTAGE | LOCATION | REMARKS |
| :--- | :--- | :--- |
| +5 Vdc | Q602 Collector |  |
| +8.6 Vdc | Q603 Collector |  |
| +5 Vdc <br> +3.25 Vdc | J500 pin 1 <br> J500 pin 2 | Power from command board to <br> reference oscillator |

1. If any of the dc voltages are not correct, troubleshoot the source of the supplied power and correct the problem. If the voltages are correct, continue with the following checks.
2. Check U602, pin 19 for reference frequency, 0 - to $9-\mathrm{V}$, square wave. If not correct, go to "Incorrect Values at U602, pin 19"; otherwise, continue with the following checks.
3. Check U602, pin 25 for reference frequency, 0 - to $9-\mathrm{V}$, square wave. If not correct, go to "Incorrect Values at U602, pin 25 (MODULUS CONTROL)"; otherwise, continue with the following checks.
4. Check the negative steering line, J601, pin 4. If correct, continue with the following checks.
5. Check the positive steering line, J601, pin 1 or 2 for positive voltage between 1.0 and 8.0 V . If not correct, go to "Incorrect Voltage at Positive Steering Line"; otherwise, continue with the following checks.

NOTE: It is common for both steps 3 and 5 to be incorrect in an "out-of-lock" condition.
6. Check U602, pin 27 for a $1.5 \mathrm{Vp}-\mathrm{p}$ square wave whose frequency is determined in the following equation. If the values are not correct, go to "Incorrect Values at U602, pin 27."

Freq into U601-1 / Prescaler Modulus;
for example, $\mathrm{F}_{\text {in }} / \mathrm{P}=455 \mathrm{MHz} / 255=1.77 \mathrm{MHz}$, or
$\mathrm{F}_{\text {in }} /(\mathrm{P}+1)=455 \mathrm{MHz} / 256=1.76 \mathrm{MHz}$.
NOTE: The frequency at U601, pin 40, is seldom exactly equal to " $F_{\text {in }}$ " divided by " $P$ " or " $P+$ 1 " because the prescaler is continuously changing from one division to the other. In the above example, P is 255 and $\mathrm{P}+1$ is 256 .
4.1.4.1.1 Incorrect Values at U602, Pin 19

1. If the reference frequency is not equal to $6.25 \mathrm{kHz}(800 / 900 \mathrm{MHz})$ or 5.0 kHz (VHF/UHF), check U602-7 for $300 \mathrm{kHz}, 0-$ to $9-\mathrm{V}$ square wave. Then:
a. If 300 kHz is good, check the power to U602, pins 30 and 37. Also, check the serial data programming by pressing and holding the mode select button and probing pins 11, 12, and 13. The $0-$ to $5-\mathrm{V}$ logic waveforms should appear similar to the following:


NOTE: The above waveforms are crude representations.
b. If the programming appears normal and the power supplies have all checked out correctly, the out-of-lock condition is caused by a defective synthesizer IC (U602).
2. If 300 kHz is not present, check U602, pin 16 , for $2.1 \mathrm{MHz}, 1.5 \mathrm{Vp}-\mathrm{p}$ square wave.
a. If the signal is present and the power to the chip is normal, the condition is caused by a defective synthesizer IC (U602).
b. If the signal is not present, check for the same signal at U601, pin 18. If not on pin 18, check the reference oscillator output signal at U601, pin 21; it should be 16.8 MHz , $300 \mathrm{mVp}-\mathrm{p}$. If the reference oscillator signal is present and the prescaler power supply voltages are normal, the prescaler IC(U601)is defective.
c. If the reference oscillator signal ( 16.8 MHz ) is not present on U601, pin 21 , check U600, pin 1 for 3.25 Vdc , pin 2 for ground, pin 3 for 16.8 MHz at $300 \mathrm{mVp}-\mathrm{p}$, and pin 4 for 5 Vdc .

NOTE: Before concluding that the reference oscillator is defective, remove it from the board, power it up externally, and test it as an independent circuit.
4.1.4.1.2 Incorrect Values at U602 Pin 25 (MODULUS CONTROL)

If the frequency is not 6.25 kHz (or 5.0 kHz for VHF), verify the proper VCO pin-shift logic. See VCO block diagram (Figure 4-1) for pin-shift logic. Also, check the VCO feedback for approximately -10 to 5 dBm at proper VCO frequency. Use the following table:

Table 4-3. Feedback Frequency Ranges

| Band | VCO Feedback Frequency |
| :--- | :--- |
| VHF | TX Freq $\times 2$ or <br> RX Freq +109.65 MHz |
| UHF | TX Freq or <br> RX Freq +109.65 MHz |
| 800 MHz | $\left.\begin{array}{l}\text { TX Freq } / 2 \text { or } \\ (R X ~ F r e q ~\end{array} 109.65 \mathrm{MHz}\right) / 2$ |

If the VCO is running at approximately the correct level and frequency, proceed to "Incorrect Values at U602, pin 27."
4.1.4.1.3 Incorrect Voltage at Positive Steering Line

Verify that the VCO is running; check VCO feedback for -10 to 0 dBm . Verify that the feedback buffer (if used) is working check U601-1.
4.1.4.1.4 Incorrect Values at U602, pin 27

Check prescaler (U601) operation; U601-40 should be:
EQUATION: $\mathrm{F}=\mathrm{F}_{\mathrm{vco}} /(\mathrm{P}$ or $\mathrm{P}+1)$

### 4.1.4.2 Review of Synthesizer Fundamentals

1. The synthesizer is a phase-locked loop system with a sample-and-hold phase detector.
2. In a locked system, the prescaler, in conjunction with the counters in the synthesizer chip, counts the VCO frequency down to the reference frequency. Think of this division process as a time domain function rather than frequency domain.
3. For each reference period (if using 6.25 kHz reference), you have 160 microseconds in which the VCO frequency is divided by N . Recall the equations:

EQUATION: $N=F_{\text {vco }} / F_{r}$
EXAMPLE: $\mathrm{N}=\mathrm{F}_{\mathrm{vco}} / \mathrm{F}_{\mathrm{r}}=450 \mathrm{MHz} / 6.25 \mathrm{kHz}$ or 72,000
EQUATION: A = (fractional remainder of $\mathrm{N} / \mathrm{P}$ ) ( P )
EXAMPLE: $A=N / P=72,000 / 255=282.3529 ; .3529 \times 255$ Or A=90
EQUATION: $B=[N-\{A x(P+1)\}] / P$
EXAMPLE: $\mathrm{B}=[72,000-\{90 \times(255+1)\}]$ or 192

At 450 MHz , there are 72,000 counts of 2.22 nanoseconds each per reference period. When modulus control (MCT) is high, the VCO output is prescaled by 255 (see the diagram below). The output frequency of the prescaler is 1.765 MHz which corresponds to a period, per-cycle, of 567 nanoseconds. The "A" counter runs long enough to count down 90 cycles which equals 51 microseconds. When MCT is low, the prescaled output equals 1.758 MHz which corresponds to a period of 569 nanoseconds. The "B" counter counts 192 cycles which takes 109 microseconds. The total time required for proper loop division is thus 160 microseconds (the reciprocal of 6.25 kHz ).

| MODULUS CONTROL: | HI | LOW | HI | LOW | HI | LOW |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |
| COUNTER: | A | B | A | B | A | B |
| COUNTER RESET: | 90 | 192 | 90 | 192 | 90 | 192 |
| PRESCALER DIVIDES BY: | 255 | 256 | 255 | 256 | 255 | 256 |
| TIME (Microseconds): | 51 | 109 | 51 | 109 | 51 | 109 |
| LOOP DIV. TIME ( Sec): |  | -- |  | - |  |  |

### 4.1.4.3 Second VCO Checks

1. Check for 300 kHz reference frequency at U601, pin 31.
2. Check for 0.5 to $4.0-\mathrm{V}$ phase detector output at U601, pin 30.
3. Check for -12 to -16 dBm at 109.2 MHz feedback (U601, pin 26).
4. Check the divide-by-N test point for a $700-\mathrm{mV}$ p-p waveform at 300 kHz (the second VCO frequency divided by 364). See the example below.


NOTE: The second VCO circuit is external to U601 and, while it does depend on U601 for proper phase-locking, it should free-run, open-loop, at some frequency, if U601 fails. If the 8.8-V super filter and the oscillator are "dead," U601 is defective.

### 4.1.4.4 Troubleshooting the Back-End

Refer to "Chart C. 1 RF Board Back-End," on page 5-3.

### 4.1.5 Standard Bias Table

Table 4-4, below, outlines some standard supply voltages and system clocks which should be present under normal operation. These should be checked as a first step to any troubleshooting procedure.

Table 4-4. Standard Operating Bias

| Signal Name | Nominal Value | Tolerance | Source |
| :---: | :---: | :---: | :---: |
| UNSW_B+ | 13.8 Vdc | 11.0-16.6 Vdc | J501 |
| SW_B+ | 13.8 Vdc | 11.0-16.6 Vdc | J501 |
| +5V | 5.0 Vdc | $\pm 10 \%$ | J501 |
| +5VA | 5.0 Vdc | $\pm 10 \%$ | J501 |
| RESET | 5.0 Vdc | +0.7, - 1.0 Vdc | J501 |
| POR* | 5.0 Vdc | +0.7, - 1.0 Vdc | J501 |
| DSP_RST* | 5.0 Vdc | +0.7, -1.0 Vdc | U204 |
| ADSIC_RST* | 5.0 Vdc | +0.7, -1.0 Vdc | U204 |
| DCLK | $33.0000 \mathrm{MHz}^{\text {a }}$ | $\pm 500 \mathrm{ppM}$ | U406 |
| ODC | 2.4 MHz | $\pm 30 \mathrm{ppM}$ | ABACUS |
| ECLK | 1.8432 MHz | $\pm 500 \mathrm{ppM}$ | U204 |
| IRQB* | $8 \mathrm{kHz}{ }^{\text {b }}$ | $\pm 500 \mathrm{ppM}$ | U406 |
| +5V | 5.0 Vdc | $\pm 10 \%$ | U202 |
| RX_5V ${ }^{\text {c }}$ | 5.0 Vdc | $\pm 10 \%$ | U106 |

a. This number may vary due to the operating mode of the radio when it is measured. The ADSIC contains a divider which may divide the clock by a modulus of 2 . Therefore, the actual frequency measured may be clock/ $2^{\text {n }}$. The most common frequency will be 16.5000 MHz nominal.
b. This 8 kHz clock will be present only after the MCU has successfully programmed the ADSIC after power-up. This is a good indication that the ADSIC is at least marginally operational.
c. Receive mode only.

### 4.2 ASTRO Spectra Plus Procedures

This section will aid you in troubleshooting a malfunctioning ASTRO Digital Spectra Plus radio. It is intended to be detailed enough to localize the malfunctioning circuit and isolate the defective component.


## Caution

Most of the ICs are static-sensitive devices. Do not attempt to troubleshoot or disassemble a board without first referring to the following Handling Precautions section.

Please review sections 4.1.1 Handling Precautions on page 4-1 and 4.1.2 Voltage Measurement and Signal Tracing on page 4-2 before continuing. Also, for information on troubleshooting the RF board, refer to Section 4.1.4 RF Board Troubleshooting on page 4-5.

### 4.2.1 ASTRO Spectra Plus Power-Up Self-Check Errors

Each time the radio is turned on the MCU and DSP perform some internal diagnostics. These diagnostics consist of checking the programmable devices such as the FLASH ROMs and SRAM devices. At the end of the power-up self-check routines any errors produced are recorded. If an error exists, use the Customer Programming Software (CPS) from the RS232 bus on front and rear of the radio to read the error code. Table $4-5$ lists self-check error codes, describes the codes, and gives the recommended corrective action.

Table 4-5. ASTRO Spectra Plus Power-Up Self-Check Error Codes

| Error Code | Description | Corrective Action |
| :---: | :---: | :---: |
| 01/02 | FLASH ROM codeplug Checksum Non-Fatal Error | Reprogram the codeplug |
| 01/12 | Security Partition Checksum Non-Fatal Error | Send radio to depot |
| 01/20 | ABACUS Tune Failure Non-Fatal Error | Turn radio off, then on |
| 01/22 | Tuning Codeplug Checksum Non-Fatal Error | Send radio to depot |
| 01/81 | Host ROM Checksum Fatal Error | Send radio to depot |
| 01/82 | FLASH ROM codeplug Checksum Fatal Error | Reprogram the codeplug |
| 01/88 | External RAM Fatal Error --Note: Not a checksum error | Send radio to depot |
| 01/90 | General Hardware Failure Fatal Error | Turn radio off, then on |
| 01/92 | Security Partition Checksum Fatal Error | Send radio to depot |
| 01/93 | FLASHport Authentication Code Failure | Send radio to depot |
| 01/98 | Internal RAM Fail Fatal Error | Send radio to depot |
| 01/A2 | Tuning Codeplug Checksum Fatal Error | Send radio to depot |
| 02/81 | DSP ROM Checksum Fatal Error | Send radio to depot |

Table 4-5. ASTRO Spectra Plus Power-Up Self-Check Error Codes (Continued)

| Error Code | Description | Corrective Action |
| :--- | :--- | :--- |
| $02 / 88$ | DSP RAM Fatal Error --Note: Not a checksum <br> error | Turn radio off, then on |
| $02 / 90$ | General DSP Hardware Failure (DSP startup <br> message not received correctly) | Turn radio off, then on |
| $09 / 10$ | Secure Hardware Failure | Turn radio off, then on |
| $09 / 90$ | Secure Hardware Fatal Error | Turn radio off, then on |

NOTE: In cases of multiple errors, the codes are logically OR'd and the results displayed.

### 4.2.2 ASTRO Spectra Plus Power-Up Self-Check Diagnostics and Repair

The following are additional action items to be utilized for the diagnosis and resolution of the error codes shown in Table 4-5:

| Error Code 01/02 | This non fatal error will likely recover if the radio's power is cycled. In the event that <br> this does not resolve the issue, the radio should be reflashed. As a last resort, the <br> FLASH ROM U301 should be replaced. |
| :--- | :--- |
| Error Code 01/12 | The radio should be sent to the depot for reflahing of the security codeplug. |
| Error Code 01/20 | Cycling radio power should resolve this issue. |
| Error Code 01/22 | The radio should be sent to the depot for reflash of the tuning codeplug followed by <br> re-tuning of the radio. |
| Error Code 01/81 | The radio should be sent to the depot for reflashing of the host code. |
| Error Code 01/82 | The radio should be sent to the depot for reflashing of the radio codeplug. |
| Error Code 01/88 | Reflashing of the radio should first be performed. If this fails to resolve the issue, <br> then replacement of the SRAM U302 is necessary. |
| Error Code 01/90 | Cycle power to radio. Continued failure indicates a likely IC failure. In this event, <br> radio should be sent to the depot for isolation and repair of the problem IC. |
| Error Code 01/92 | The radio should be sent to the depot for reporgramming of the security codeplug. |
| Error Code 01/93 | The radio should be sent to the depot for reflashing of the host code. |
| Error Code 01/98 | Send radio to the depot for replacement of the SRAM U302. |
| Error Code 01/A2 | The radio should be sent to the depot for reflashing of the tuning codeplug followed <br> by re-tuning of the radio. |
| Error Code 02/81 | The radio should be sent to the depot for examination and/or replacement of either <br> the FLASH U301, or the PATRIOT MCU/DSP U300. |
| Error Code 02/88 | Cycle power to the radio. If this does not fix the problem, then the radio should be <br> sent to the depot for reflashing of the DSP code. Continued failure requires <br> examination and/or replacement of the SRAM U302. |
| Cycle power to the radio. If this fails to fix the problem, then the radio should be |  |
| sent to the depot for reflashing of the DSP code. Continued failure may require |  |
| replacement of U300, the PATRIOT MCU/DSP. |  |

Error Code 09/10 Cycle power to the radio. If this fails then follow instructions as per troubleshooting chart C. 32

Error Code 09/90 Cycle power to the radio. If this fails then follow instructions as per troubleshooting chart C. 32

### 4.2.3 ASTRO Spectra Plus Standard Bias Table

Table 4-6 outlines some standard supply voltages and system clocks which should be present under normal operation. These should be checked as a first step to any troubleshooting procedure.

Table 4-6. ASTRO Spectra Plus Standard Operating Bias

| Signal Name | Nominal <br> Value | Tolerance | Probe Point |
| :--- | :--- | :--- | :--- |
| SINE32K | 32.768 kHz | $+/-400 \mathrm{ppm}$ | R428 |
| CKIH | 16.8 MHz |  | C 326 |
| $16 \_8 \mathrm{MHz}$ | 16.8 MHz |  | TP401 |
| POR | 3.0 V | $+/-5 \%$ | $\mathrm{~J} 501-29$ |
| RESET_OUT | 3.0 V | $+/-5 \%$ | J 501 |
| VCC1.8 | 1.80 Vdc | $+/-5 \%$ | R419 |
| VCC3.0 | 3.0 Vdc | $+/-5 \%$ | R 420 |
| SW_B+ | 13.8 Vdc | $11.0-16.6 \mathrm{Vdc}$ | $\mathrm{J} 501-35$ |
| VCC5 | 5.0 V | $+/-10 \%$ | $\mathrm{~J} 501-34$ |

### 4.3 VCO Procedures

This section provides band-specific troubleshooting procedures for the VCO.

### 4.3.1 VHF Band

Use these instructions along with the Theory of Operation, the block diagram, and the schematic to help isolate failures: first, to the individual circuits, and finally, to the failing piece part.

### 4.3.1.1 VCO Hybrid Assembly

The VCO hybrid substrate is glued to the carrier board. The hybrid is not a field-repairable assembly. If a failure is indicated in this assembly, replace the complete hybrid. You will need a hot-air source to heat and soften the glue to separate the hybrid from the carrier board. If no hot-air source is available, replace the entire carrier board.

### 4.3.1.2 Out-of-Lock Condition

The probable cause of an out-of-lock condition is a failure in the synthesizer circuit. (See Section 4.1.4.2 Review of Synthesizer Fundamentals on page 4-7.) If the voltages on the AUX $1^{*}$ and AUX $2^{*}$ lines do not conform to Table 4-7, troubleshoot the synthesizer.

If the AUX 1* and AUX 2* voltages are correct but the synthesizer feedback level is not within the range indicated, troubleshoot the first buffer on the VCO carrier board. If no problem is found with the first buffer and the level out of the VCO is below that indicated on the block diagram, then replace the VCO assembly.

If the AUX $1^{*}$ and AUX 2* voltages are correct and the synthesizer feedback level is correct but an out-of-lock condition persists, troubleshoot the synthesizer.


Figure 4-1. VCO Block Diagram - VHF Band

Table 4-7. VCO Frequency

| Mode | AUX 1 | AUX 2 | Radio Freq (MHz) | VCO Freq (MHz) | Port Freq (MHz) | Port |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| VHF RANGE 1 |  |  |  |  |  |  |
| RX | HIGH | HIGH | $136.00-158.35$ | $245.65-268.00$ | $245.65-268.00$ | $($ (RX) |
| RX | LOW | HIGH | $158.35-162.00$ | $268.00-271.65$ | $268.00-271.65$ | (RX) |
| TX | LOW | HIGH | $136.00-145.20$ | $272.00-290.40$ | $136.00-145.20$ | (TX) |
| TX | HIGH | LOW | $145.20-157.00$ | $290.40-314.00$ | $145.20-157.00$ | (TX) |
| TX | LOW | LOW | $157.00-162.00$ | $314.00-324.00$ | $157.00-162.00$ | (TX) |
|  |  |  |  |  |  |  |
| VHF RANGE 2 |  |  |  |  |  |  |
| RX | HIGH | HIGH | $146.00-166.15$ | $255.65-275.80$ | $255.65-275.80$ | (RX) |
| RX | LOW | HIGH | $166.15-174.00$ | $275.80-283.65$ | $275.80-283.65$ | (RX) |
| TX | LOW | HIGH | $146.00-150.00$ | $292.00-300.00$ | $146.00-150.00$ | (TX) |
| TX | HIGH | LOW | $150.00-162.00$ | $300.00-324.00$ | $150.00-162.00$ | (TX) |
| TX | LOW | LOW | $162.00-174.00$ | $324.00-348.00$ | $162.00-174.00$ | (TX) |

### 4.3.1.3 No or Low Output Power (TX or RX Injection)

Use the test cables listed in the Service Aids section in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20). Measure the power at the synthesizer feedback port - if it is not within the range specified in the block diagram, troubleshoot the first buffer. If failure is found in the first buffer, replace the defective component. If no failure is found in the first buffer and the level out of the VCO (measured with an RF millivoltmeter) is below that indicated in the block diagram, then replace the VCO assembly.
If the level at the synthesizer feedback port is within the indicated range, then troubleshoot the divider, RX, and TX buffer.

### 4.3.1.4 No or Low Modulation

Under standard test conditions with a 1 kHz tone injected and $4.5 \mathrm{kHz}( \pm 50 \mathrm{OHz})$ deviation, there should be at least 0.8-V peak-to-peak present on J601, pin 10 (modulation input). (See the circuit board overlay for location.) If this level is not present, troubleshoot the audio circuitry, if it is present, check the VCO modulation circuitry.

### 4.3.2 UHF Band

Use these instructions along with the Theory of Operation, the VCO block diagram, and the schematic to help isolate failures, first to the individual circuits, and finally to the failing piece part.

### 4.3.2.1 VCO Hybrid Assembly

The VCO hybrid substrate is glued to the carrier board. The hybrid is not a field-repairable assembly. If a failure is indicated in this assembly, replace the complete hybrid. You will need a hot air source for heating and softening the glue to separate the hybrid from the carrier board. If no hot air source is available, replace the entire carrier board.

### 4.3.2.2 Out-of-Lock Condition

The probable cause of an out-of-lock condition is a failure in the synthesizer circuit. (See Section 4.1.4.2 Review of Synthesizer Fundamentals on page 4-7.) If the voltages on the AUX 1*, AUX 2*, or -8 V lines at P0601 do not conform to the values shown in Figure 4-2, check the pin shift circuitry on the carrier board for proper operation. If no trouble is found, troubleshoot the synthesizer.

If the AUX1*, AUX2*, and $-8-\mathrm{V}$ voltages are correct at P0601, check the pin shift circuitry on the carrier board for proper operation. If no problem is found, probe the level of the synthesizer feed back at P0601-1 using an RF millivoltmeter. The meter should indicate greater than -15 dBm . If it does not, troubleshoot the synthesizer feedback circuitry; then troubleshoot the first buffer on the VCO carrier board. If no trouble is found and the level out of the VCO is below that indicated on the block diagram, then replace the VCO assembly.

If the AUX $1^{*}, ~ A U X 2^{*}$, and $-8-V$ voltages are correct and the synthesizer feedback level is correct but an out-of-lock condition persists, troubleshoot the synthesizer.

### 4.3.2.3 No or Low Output Power (TX or RX Injection)

Using an RF millivoltmeter, probe the synthesizer feedback level at P0601-1. If the meter indication is not greater than -15 dBm , troubleshoot the first buffer. If no failure is found and the level out of the VCO (measured into 50 ohms at the RF output of the hybrid) is below that indicated in the block diagram, then replace the VCO assembly.

If the level of synthesizer feedback at P0601-1 is correct, troubleshoot the doubler, second buffer, and then the RX/TX pin diode switch.

### 4.3.2.4 No or Low Modulation

Under standard test conditions with a 1 kHz tone injected and 4.5 kHz deviation, there should be 700 $m V$ (RMS) $\pm 20 \%$ present on P0601-10. If this level is not present, troubleshoot the modulation circuit on the carrier board and then troubleshoot the audio circuitry. If the proper level is present, troubleshoot the modulation circuitry on the VCO kit. If no failure exists, replace the VCO.


Figure 4-2. VCO Block Diagram - UHF Band

### 4.3.3 800 MHz Band

Use these instructions along with the Theory of Operation, the block diagram, and the schematic to help isolate failures, first, to the individual circuits, and finally to the failing piece part.

### 4.3.3.1 VCO Hybrid Assembly

The VCO hybrid substrate is glued to the carrier board. The hybrid is not a field-repairable assembly. If a failure is indicated in this assembly, replace the entire carrier board.

### 4.3.3.2 Out-of-Lock Condition

The probable cause of an out-of-lock condition is a failure in the synthesizer circuit. (See Section 4.1.4.2 Review of Synthesizer Fundamentals on page 4-7.) If the voltages on the AUX 1* and AUX 2* lines do not conform to the table in Figure 4-3, troubleshoot the synthesizer.
If the AUX 1* and AUX 2* voltages are correct but the synthesizer feedback level is not within the range indicated, troubleshoot the first buffer on the VCO carrier board. If no problem is found with the first buffer and the level out of the VCO is below that indicated on the block diagram, check J straps MP9656-MP9668. If no problem is found with these, replace the entire carrier board.

If the AUX 1* and AUX 2* voltages are correct and the synthesizer feedback level is correct but an out-of-lock condition persists, troubleshoot the synthesizer.


Figure 4-3. VCO Block Diagram - 800 MHz Band

### 4.3.3.3 No or Low Output Power (TX or RX Injection)

Use the test cables listed in the "Service Aids" in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20). Measure the power at the synthesizer feedback port-if it is not within the range specified in the block diagram, troubleshoot the first buffer. If failure is found in the first buffer, replace the defective component. If no failure is found in the first buffer and the level out of the VCO (measured with an RF millivoltmeter) is below that indicated in the block diagram check J straps MP9656-MP9668. If no problem is found with these, replace the entire carrier board.

If the level at the synthesizer feedback port is within the indicated range, then troubleshoot the doubler, second buffer, and PIN diode switch.

### 4.3.3.4 No or Low Modulation

Under standard test conditions with a 1 kHz tone injected and $4.6 \mathrm{kHz}( \pm 250 \mathrm{~Hz})$ deviation, there should be between 500 and 1000 mV present on J601, pin 10 (modulation input). (See the circuit board overlay for location.) If this level is not present, troubleshoot the audio circuitry. If it is present, check J601, pin 4 (NEG S.L.). The negative steering line should be $-4.0 \mathrm{~V}( \pm 0.3 \mathrm{~V})$. If this is not correct, check the negative steering line circuitry on the RF board and/or check R9651 and C9651 on the carrier board. If no problem is found, check J straps MP9656-MP9668. If no problem is found with these, replace the entire carrier board.

### 4.4 Receiver Front-End (RXFE)

This section provides band-specific troubleshooting procedures for the receiver front-end.

### 4.4.1 VHF Band

This information will help you troubleshoot the Spectra radio. Use this information, along with the Theory of Operation, to diagnose and isolate the cause of failures. The principle tools needed to troubleshoot a circuit to the component level are the schematic and the Theory of Operation.

In addition to the schematic and theory, this section includes a troubleshooting chart that will guide you through a sequence of tests and checks designed to isolate problems.
Prior to troubleshooting, it is important to review the Theory of Operation including specific precautions and troubleshooting methods.

### 4.4.2 UHF Band

This information will help you troubleshoot the Spectra radio. Use this information, along with the Theory of Operation, to diagnose and isolate the cause of failures. The principle tools needed to troubleshoot a circuit to the component level are the schematic and the Theory of Operation.

In addition to the schematic and theory, this section includes a troubleshooting chart that will guide you through a sequence of tests and checks designed to isolate problems.

Prior to troubleshooting, it is important to review the Theory of Operation including specific precautions and troubleshooting methods. Because much of the radio's circuitry operates at 500 MHz , measurements must be taken carefully.

### 4.4.3 800 MHz Band

This information will help you troubleshoot the Spectra radio. Use this information, along with the Theory of Operation, to diagnose and isolate the cause of failures. The principle tools needed to troubleshoot a circuit to the component level are the schematic and the Theory of Operation.

In addition to the schematic and theory, this section includes a troubleshooting chart that will guide you through a sequence of tests and checks designed to isolate problems.

Prior to troubleshooting, it is important to review the Theory of Operation including specific precautions and troubleshooting methods. Because much of the radio's circuitry operates at 800 MHz , measurements must be taken carefully.

### 4.5 Power Amplifier Procedures

This section provides band-specific troubleshooting procedures for the power amplifier.

### 4.5.1 VHF Band

### 4.5.1.1 High-Power Amplifier

This information will help you troubleshoot the Spectra radio. Use this information, along with the Theory of Operation, to diagnose and isolate the cause of failures. This section includes troubleshooting information that will help you test and check the circuits to localize and isolate problems.

Prior to troubleshooting, it is important to review the Theory of Operation, including specific precautions and troubleshooting methods. Because much of the radio's circuitry operates at VHF frequencies, measurements must be taken very carefully. Notes and cautions are added to the text to alert the reader to this need in areas of greatest sensitivity. However, the need for extreme care does exist in all measurements and tests at VHF frequencies.

### 4.5.1.1.1 General Troubleshooting and Repair Notes

Most of the common transmitter symptoms are not necessarily caused by failure of circuits on the PA board. Failure of command board or synthesizer circuits can disable the transmitter. The initial troubleshooting effort should be toward isolating the problem to one of these areas. If either the control voltage or keyed 9.4 V are zero, then the problem is likely to be in the control circuit or synthesizer. If those voltages are present, then the problem is more likely in the power amplifier circuit.

If, for diagnostic reasons, a chip component needs to be removed to facilitate testing, such as a series capacitor removed to allow for signal insertion, then the component(s) returned to the circuit should be new parts. The application of a soldering iron to many chip components will tend to cause leaching which could lead to failure.

If the harmonic filter is damaged and needs to be replaced, then removal and replacement requires the use of a hot-air source capable of reflowing the solder beneath the filter hybrid. When replacing it, add small amounts of fresh solder paste to the silver regions beneath the ceramic to assure adequate electrical ground contact. Save the original input and output connectors (J-straps); these are not included with the replacement kit. No tuning is required. The harmonic filter may be ordered separately, but if the PA kit is ordered a filter kit comes with the PA kit.
After a PA board is replaced, or if any power control circuitry components are replaced, readjust the power according to instructions in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20).
Due to high operating frequencies, you must use specified Motorola parts when component replacement is necessary. Substitute components may not work. It is also critical that you use great care when replacing parts. Excessive solder or flux, longer than original leads on coax connectors, misorientation of parts and other commonly benign imperfections, may cause the radio's performance to degrade.

Bench testing the high-power Spectra PA is most easily accomplished if a Spectra control head, control cable, and power cable are available on the test bench. This greatly simplifies the troubleshooting as several supply voltages are provided by the command board. Proper operation of the command board circuitry can be simultaneously verified.

Begin troubleshooting by connecting an RF power meter and appropriate power load to the antenna connector. Connect the control cable and the power cable. Make sure the ignition sense lead is also connected to the positive lead of the power supply. Note that a regulated DC power supply capable of at least 30 A . is necessary to power a high-powered Spectra transmitter. Remove the radio bottom cover. Remove the PA shield by pulling straight up on the plastic handle. This must be done carefully, as the edge of the PA shield can damage components on the PA board if it is removed unevenly. Set the power supply to 13.4 V . The radio may now be turned on. All critical voltages may be measured at connector J 1 from the top side of the PA board. A diagram of the connector pin-out, as viewed from the top side of the PA board, is shown below.

Pin Configuration of J1 As Viewed From Top of PA Board


1 Control Voltage Limit
2 Control Voltage Drive
3 Current Sense +
4 Key 9.4V
5 Filtered A+
6 Temp-Sense
7 Not Connected
8 Forward Power Detect
9 9.6V
10 Current Sense -
11 Not Connected

Figure 4-4. Connector Pin-Out - High-Power Amplifier
Key the transmitter. The RF power meter should read at least 100 Watts if it is calibrated. If power is low, the power set must be checked first before suspecting a defective PA or command board. This may be checked using a PC and RSS software. Alternatively, front panel programming may be used. Please refer to the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20) for programming instructions.

If correct power output can not be obtained by following the power set procedure outlined in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20), it is possible that current limit may be improperly set. This can not be adjusted using front panel programming. A PC with RSS must be used. A simple way to check for current limit engagement is to temporarily short out the current sense resistor R3849 with a piece of 12- or 14gauge wire. If full power is restored, then RSS must be used to properly set current limit.

If it is verified that both power set and current limit are not related to the power problem, then the synthesizer output must be checked. A milliwatt meter connected to the TX injection cable should indicate at least 10 mW of injection power during key-up. If this is not the case, refer to the RF board and VCO sections of this manual for troubleshooting procedures.

Table 4-8. Power Control DC Voltage Chart

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Low | TYP | Hi | LOW | TYP | HI |  |
| J1 |  |  |  |  |  |  |  |
| 1 |  | 0 |  | 0 | 2.0 | 3.2 | Control Voltage Limit |
| 2 |  | 0 |  | 2.0 | 7.0 | 10.0 | Drive Voltage |
| 3 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | Current Sense + |
| 4 | 0 | 0 | 0 | 9.2 | 9.4 | 9.8 | Keyed 9.4 |
| 5 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | A+ to Command Board |
| 6 |  | 0 |  |  | 1.2 |  | Temp Sense (cutback begins at 3.3-V) |
| 7 | - | - | - | - | - | - | Key (no pin) |
| 8 |  | 0 |  | 13.0 | 9.3 | 5.0 | Forward Detect Voltage |
| 9 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | A+ to Command Board |
| $\begin{aligned} & 10 \\ & 11 \end{aligned}$ | $\begin{gathered} \hline 9.4 \\ 10.8 \end{gathered}$ | $\begin{gathered} 9.6 \\ 13.6 \end{gathered}$ | $\begin{gathered} 9.9 \\ 16.5 \end{gathered}$ | $\begin{aligned} & 9.4 \\ & 9.8 \end{aligned}$ | $\begin{gathered} 9.6 \\ 12.8 \end{gathered}$ | $\begin{gathered} 9.9 \\ 15.8 \end{gathered}$ | 9.6-V Supply from Command Board Current Sense - (voltage delta 150 mV ) |
| 12 | - | - | - | - | - | - | Key (no pin or wire) |
| U500 |  |  |  |  |  |  |  |
| 1 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 2 |  | 0 |  |  | 3.2 |  | Control AMP Input |
| 3 | 0 | 0 | 0 | 0 | 0 | 0 | Control AMP Input (not used) |
| 4 |  | 0 |  | 0 | 2 | 3.2 | Control Voltage Limit (cutback at 3.3 V ) |
| 5 |  | 9 |  |  | 0 |  | N.C. |
| 6 | 1.5 | 3.0 | 4.5 | 1.5 | 3.0 | 4.5 | Power Set from D-A (max power at 1.5 V ) |
| 7 |  | 0 |  | 1.5 | 3.0 | 4.5 | Power Set Buffer Out |
| 8 |  | 0 |  | 1.3 | 3.5 | 6.0 | Coupler Buffer Out |
| 9 |  | 0 |  | 1.3 | 3.5 | 6.0 | Forward Detect Volt |
| 10 |  | 0 |  |  | 0 |  | Reflected Power Detect (not used) |
| 11 |  | 0 |  | 1.3 | 3.5 | 6.0 | Same as pin 8 (not used) |
| 12 |  | 0 |  | 0 | 1.2 | 6.0 | Thermister Buffer out (increases as PA gets hot) |

Table 4-8. Power Control DC Voltage Chart (Continued)

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Low | TYP | Hi | Low | TYP | Hi |  |
| J1 |  |  |  |  |  |  |  |
| 13 |  | 0 |  | 0 | 1.2 | 6.0 | Thermister Buffer in |
| 14 |  | 5.0 |  |  | 5.0 |  | 5 -V Sense Input (follows pin $20 \pm 0.1 \mathrm{~V}$ ) |
| 15 | 4.9 | 5.0 | 5.7 | 4.9 | 5.0 | 5.7 | 5-V Current Limit (limits at 5.7 V ) |
| 16 | 5.0 | 5.7 | 6.4 | 5.0 | 5.7 | 6.4 | 5-V Series Pass Drive (6.4 at max current) |
| 17 | 9.5 | 9.6 | 9.9 | 9.5 | 9.6 | 9.9 | 9.6-V Sense Input |
| 18 |  | 7 |  |  | 7 |  | 5-V Reg. Compensation Capacitor |
| 19 |  | 5.7 |  |  | 5.7 |  | N.C. |
| 20 | 4.9 | 5.0 | 5.1 | 4.9 | 5.0 | 5.1 | 5-V Reference Input (UNSW5V) |
| 21 |  | 1.2 |  |  | 1.2 |  | 9.6-V Reg. Compensation Capacitor |
| 22 |  | 0 |  |  | 0 |  | N.C. |
| 23 |  | 0.9 | 9.6 |  | 1.2 | 9.6 | 9.6-V Series Pass Drive |
| 24 |  | 2.9 |  |  | 3.3 |  | Regulator Enable/Compensation |
| 25 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 26 |  | 0 |  |  | 0 |  | N.C. |
| 27 |  | 13.6 |  |  | 13.6 |  | N.C. |
| 28 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 29 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 30 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 31 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 32 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | Decoupled A+ |
| 33 | 4.0 | 5.0 |  |  | 0 | 0.2 | TX PA Enable (from U520-25) |
| 34 |  | 0 |  |  | 1.3 |  | Control AMP one-shot |
| 35 |  | 0 |  |  | 0 |  | $\overline{\text { Lock ( } 5 \mathrm{~V} \text { of Synth Out of Lock) }{ }^{\text {a }} \text { ( }{ }^{\text {a }} \text { ( }}$ |
| 36 |  | 0 |  |  | 0.8 |  | Control AMP one-shot |
| 37 | 10.8 | 13.6 | 16.3 | 10.0 | 13.0 | 16.0 | A+ (Current Sense + |
| 38 | 10.8 | 13.6 | 16.3 | 10.0 | 13.0 | 16.0 | Current Sense - Voltage Delta 150 mV |
| 39 |  | 0 |  | 9.2 | 9.4 | 9.8 | Keyed 9.4-V in |
| 40 | 1.5 | 3.0 | 4.5 | 1.5 | 3.0 | 4.5 | Current Limit D-A (max current at 4.5 V ) |

Table 4-8. Power Control DC Voltage Chart (Continued)

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Low | TYP | HI | Low | TYP | HI |  |
| J1 |  |  |  |  |  |  |  |
| 41 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 42 |  | 0 |  |  | 2.2 | 9.6 | Control AMP Output (Approx 1/2-V Control) |
| 43 |  | 1.3 |  |  | 7.0 |  | Loop Integrator Capacitor |
| 44 |  | 2.1 |  |  | 3.2 |  | Control AMP Reference |
| Q0500E |  | 13.0 |  |  | 13.0 |  | A+ - CR0500 Drop |
| Q0501C |  | 12.3 |  |  | 12.3 |  | VQ0500E - B/E Drop |
| Q0501E |  | 0.2 |  |  | 0.2 |  | $\checkmark$ pin 23 - B/E Drop |
| Q0503E |  | 0 |  |  | 1.5 |  | $V$ pin $42-\mathrm{B} / \mathrm{E} \operatorname{Drop}(\mathrm{TX})$ |
| Q0503C |  | 13.6 |  |  | 9.0 |  |  |
| Q0504B |  | 13.6 |  |  | 12.9 |  | A+ - B/E Drop (TX) |

If the command board and synthesizer are functioning properly, the PA must be defective. Details on troubleshooting each circuit of the PA follow.

### 4.5.1.1.2 PA Functional Testing

NOTE: When setting or measuring RF power at VHF follow these guidelines to avoid measurement errors due to cable losses or non-50-ohm connector VSWR:

- All coaxial cables should be low loss and as short as possible.
- Attenuators and 50 -ohm loads should have at least 25 dB return loss.
- Mini UHF to 'N' adapter, P/N 58803671321, should be used at the antenna connector. All other connectors should be ' N ' type. No other adapters, barrel connectors, etc. should be used.
Maximum input level to the PA is 20 mW . Too much input power could result in damage to the LLA stage.
Methods of analyzing individual stages of the power amplifiers are detailed below. Most of the stages are Class-C and must be analyzed under relatively high RF power levels. The following information should help in isolation and repair of the majority of transmitter failures.


## Testing Low-Level Amplifier (LLA) Circuitry

Proper operation of the LLA can be checked by monitoring the voltage across resistor R3804. The voltage should measure in the range of 0.4 V to 1.0 V , depending on the value of control voltage. A $0.4-\mathrm{V}$ reading corresponds to a low control voltage ( 4 to 5 V ) and a $1.0-\mathrm{V}$ reading corresponds to a high control voltage (up to control voltage limit).

Measure LLA voltages according to Table 4-9. If the DC bias conditions are correct, check to see if the LLA is providing drive power to Q3804. Do so by checking Q3804's collector current under normal drive conditions, as follows:

- Remove L3806 (be sure to reinstall after testing).
- Solder wires to the remaining pads. Place an ammeter in series with Q3804 collector.
- Check for 0.2 to 0.5 A . (depending on control voltage).

NOTE: With no RF drive to the input of the PA, Q3804 collector current should be zero.
Table 4-9. LLA and 2nd Stage Typical Voltages

| CONTROL <br> VOLTAGE | RF DRIVE OFF |  | RF DRIVE ON |  |
| :--- | :---: | :---: | :---: | :---: |
|  | $\mathbf{8 . 0} \mathbf{~ V}$ | $\mathbf{6 . 0} \mathbf{~ V}$ | $\mathbf{8 . 0} \mathbf{~ V}$ | $\mathbf{6 . 0} \mathbf{~ V}$ |
| Q3801 | - | - | - | - |
| Base |  |  |  |  |
| Collector |  |  |  |  |

NOTE: The LLA voltages change with different control voltages. An example of LLA voltages with control voltage equal to 8.0 V and 6 V is shown.

If Q3804 draws no current under normal conditions, then check for short or open input cable, or for defective parts in the matching circuitry between Q3801 and Q3804.

## Testing Second Stage Circuitry Q3804

The second stage is a typical Class-C stage, except the base is biased with resistors R3809 and R3810. The necessary conditions for proper operation of this stage are input drive power, and bias conditions as shown in Table 4-9.

NOTE: If it is necessary to replace Q3804, use a hot-air blower to remove and replace the part. It is important that the replacement device's case be properly soldered to its heatsink. Do so by flowing a small bead of solder around the rim of the device while it is clamped in the hot-air soldering device. The base and collector leads must be hand-soldered on the bottom side of the board.

## Troubleshooting the Driver Stage (Q3805)

- Make sure $\mathrm{A}+$ is at the collector.
- Check for shorts and/or opens in the matching circuitry. Also look for faulty components (cracked parts or parts not properly soldered).
- Measure the DC resistance from base to emitter. It should be less than 1 -ohm. If not, check L3812 and L3809 for proper soldering, and replace if faulty.
- Check the current drain of Q3805. Remove L3811 and R3819 and solder wires to the pads. With an ammeter connected to these wires, check the collector current drain during transmit. It should be around 2.0 to 4.0 A. If current drain is low, go to next step.
- Desolder the base of Q3805 and bend its lead slightly so it does not contact the PC board. Check the base-emitter and base-collector junction diode voltages using the diode check function of a multimeter. Normal voltage drop should be near 0.6 V . If either junction is open or short circuited replace the device.


## Analysis of the Final Amplifier Stage (Q3870 and Q3871)

Extreme care must be taken when troubleshooting the final amplifier due to the high RF currents and voltages present.

A visual inspection of the matching networks should be done first. Check for defective solder joints or burned components. Good soldering of the transistor device leads is essential. Make sure A+ voltage is reaching the collector of each final device.

Check the base-emitter and base-collector junctions of the final devices by removing L3930, L3933, R3859, and R4007. Using the diode check function of a multimeter, the junctions should have a forward voltage drop close to 0.6 V . Replace a final device if it has an open or shorted junction.

Capacitors C3860, C3861, C3862, and C3863 are placed on the bottom side of the PA board underneath the base leads of the final devices. Extreme care should be used when replacing these parts. Exact positioning is critical. Inspect for solder shorts on these capacitors before installing the PA board in the radio chassis.

Installation of the PA board into the radio chassis must be done carefully. The PC board's screws use a T-15 Torx bit and should be torqued to 12 to 14 inch-pounds. The device screws use a T-8 Torx bit and should be torqued to 12 to 14 inch-pounds. Always apply thermal compound to the area under the device flanges before installing the PA board.

Current drain of the final amplifier may be checked by measuring the voltage across R3849 during transmit. A voltage drop of 0.10 V to 0.15 V indicates the finals are drawing 10 to 15 A ., which is within the acceptable range.

## Testing the Antenna Switch and Harmonic Filter

Use care when replacing the harmonic filter. Removal of the filter is best accomplished by heating the filter/PC board assembly with a heat gun or heat blower until the solder joint reflows.

Verify that the receive path of the antenna switch and the harmonic filter are functioning by testing the receiver insertion loss as follows:

- Apply a low-level signal source at the antenna connector.
- Verify the conditions indicated in Table 4-8 for RX tests.
- Measure the power at the receive coax.
- If the difference between the input and output (insertion loss) is less than 1 db , then the circuitry is functioning properly.

Additional antenna switch tests are:

- Check CR3901, CR3902, and CR3903 using the diode check function of a multimeter. Note that CR3903 is on the bottom side of the board. This diode affects the receive path only and is unrelated to transmitter problems.
- Check for proper DC current through the PIN diodes; correct current is indicated if approximately 1.5 V is present at the junction of R3900 and L3900 during transmit.


DO NOT measure bias directly at the PIN diodes while in transmit mode unless TX injection is removed.
WARNING

### 4.5.1.1.3 Power Control and Protection Circuitry

## Localizing Problems to a Circuit

Power leveling and current limiting are set to values detailed in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20). These values will vary from unit to unit, depending on the unique variations of each unit. If symptoms indicate that either of these circuits have failed, verify that the radio has been properly aligned before investigating the circuitry.

Temperature sense and control voltage limit are fixed by design and are not influenced by the alignment of the radio. If symptoms indicate that these circuits have failed, then troubleshoot the circuit.

The tests that follow are intended to provide a convenient means of verifying that a particular circuit is functioning properly. These tests will isolate the failure to a minimum number of components. Refer to the Theory of Operation and the schematic for information needed to identify the failed component(s).

## Temperature Sense Circuit Test

Temporarily install a 2.2 k ohm resistor in parallel with RT3842. Key the transmitter and monitor the output power. The power meter should read approximately one-half the rated power.

## Control-Voltage-Limit Circuitry Test

Disconnect the transmitter injection cable from J3850. With all other connections in normal condition, key the transmitter and monitor the control voltage at J 1 pin 2. If the voltage exceeds 9.0 V , troubleshoot the control voltage limit circuitry.

## Current-Limiting Circuitry Test

When ready to adjust current limit, decrease the relative current limit value with the keyboard per instructions. After several decrements, the current limit should begin to reduce power in 0.5 to 1.0 Watt increments. After this test, reset the current limit to its original value. If the circuitry does not perform as indicated, troubleshoot the current limit circuitry.

## Directional Coupler and Power-Leveling Test

The directional coupler combined with the RPCIC form a closed-loop power leveling circuit. This circuit keeps forward power essentially constant under variations of line voltage, frequency, and VSWR.

The directional coupler samples a small amount of forward power during transmit. This power is rectified by a detector diode CR3904. This rectified DC voltage is fed back to the RPCIC where it is compared to a reference voltage. An error voltage is generated which is ultimately translated into the control voltage via RPCIC circuitry and amplifiers Q503 and Q504 on the command board. Control voltage is routed to the LLA stage, thereby completing the feedback loop. In operation, the control loop tends to maintain the forward detected voltage constant versus frequency and line voltage variations. Proper operation can be observed by monitoring the forward detected voltage while varying the supply voltage from 13.4 to 16.1 V . Forward detected voltage should not change more than a few hundreths of a volt. Note that the forward power may not necessarily be level if one of the other protection circuits such as temp-sense or current limit is engaged.
NOTE: If any part of the power leveling circuitry is replaced, perform the power set procedure. See the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20) for details.

## Miscellaneous Circuits and Notes

Diode CR3840 acts as a reverse protect diode. This diode also protects from over-voltage conditions, as it has a Zener breakdown voltage of approximately 28 V . When replacing this diode, care must be taken to place the diode with the cathode marking ring down (towards the PC board)
NOTE: The control voltage drive and K9.4 supplies from the command board are not current limited. A momentary short on either of these supplies will cause damage to transistors on the command board. Use caution when troubleshooting circuits that use these.

### 4.5.1.2 25/10 Watt Power Amplifier

This information will help you troubleshoot the Spectra radio. Use this information, along with the Theory of Operation, to diagnose and isolate the cause of failures. The principle tools needed to troubleshoot a circuit to the component level are the schematic and the Theory of Operation.

In addition to the schematic and theory, this section includes troubleshooting information that will help you test and check the circuits to localize and isolate problems.
Prior to troubleshooting, it is important to review the Theory of Operation, including specific precautions and troubleshooting methods. Because much of the radio's circuitry operates at high frequency, measurements must be taken very carefully. Notes and cautions are added to the text to alert the reader to this need in areas of greatest sensitivity
However, the need for extreme care does exist in all measurements and tests at high frequency.

### 4.5.1.2.1 General Troubleshooting and Repair Notes

Most of the common transmitter symptoms are caused by either failure of the power amplifier or a failure in the control circuitry. The initial troubleshooting effort should be toward isolating the problem to one of those two areas. If either the control voltage or keyed 9.4 V are zero, then the problem is likely to be in the control circuit. If those voltages are present, then the problem is more likely in the power amplifier circuit.
If, for diagnostic reasons, a chip component needs to be removed to facilitate testing, such as a series capacitor removed to allow for signal insertion, then the component(s) returned to the circuit should be new parts. The application of a soldering iron to many chip components will tend to cause leaching which could lead to failure.

After a PA board is replaced, or if any power control circuitry components are replaced, readjust the power according to instructions in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20).

NOTE: Due to high operating frequencies, you must use specified Motorola parts when component replacement is necessary. Substitute components may not work. It is also critical that you use great care when replacing parts. Excessive solder or flux, longer than original leads on coax connectors, misorientation of parts, and other commonly benign imperfections may cause the radio's performance to degrade.

### 4.5.1.2.2 PA Functional Testing

To test the PA assembly for proper operation, perform the following steps:

1. Disassemble the PA assembly from the radio, leaving the power cable connected to the rear connector. Replace the PA shield and cover. Disconnect the coax connectors and the ribbon cable. Connect a power meter to the antenna port using minimum cable length.
a. When setting or measuring RF power, follow these guidelines to avoid measurement errors due to cable losses or non 25/10-ohm connector VSVVR:

- All cables should be very short and have Teflon dielectric.
- Attenuators and 25/10-ohm loads should have at least 25 dB return loss.
- Mini UHF to 'N' adapter, P/N 58-80367B21, should be used at the antenna connector. All other connectors should be ' N ' type. No other adapters, barrel connectors, etc. should be used.
b. Maximum input level to the PA is 20 mW . Too much input power could result in damage to the LLA stage.

2. Apply the input power and DC voltages indicated in Table 4-10 to the power amplifier assembly. To make the DC connections, use small spring-clips or make a test adapter similar to that shown in Figure 4-5.

Table 4-10. DC Voltages and Input Power Chart

| Test | Keyed 9.4 V | CONTROL <br> VOLTAGE <br> DRIVE | POWER IN <br> $(\mathbf{m W})$ | A+.V |
| :---: | :---: | :---: | :---: | :---: |
| Transmit | 9.4 | See note $^{\mathrm{a}}$ | 10 | 13.4 |
| Receive | 0 | 0 | 0 | 13.4 |

a. Set initially to zero. Increase value until power equals 28 Watts or 9.2 V maximum. Do NOT exceed 9.2 V.
3. Apply the required input power via an adapter cable. For this application, non ' N ' type connectors are acceptable.


Figure 4-5. PA Test Adapter, 25/10 Watt Power Amplifier
4. With the applied control voltage drive initially at 0 V , slowly increase the voltage until power out equals 28 Watts. Power should rise smoothly with control voltage once the turn-on threshold is reached. Control voltage drive should not exceed 9.2 V.
5. If 9.2 V does not produce 28 Watts , then a failure exists in the power amplifier circuit.
6. Refer to the voltage chart (see Table 4-11). Measure the indicated voltages. If they are not within the limits shown in the chart, then a failure exists in the PA assembly.
7. If the voltages in the chart are correct, verify that the injection is at least 10 mW . (See the VCO troubleshooting section.)
8. If no failure is located from the previous checks, troubleshoot the power control circuitry.

Table 4-11. Power Control DC Voltage Chart

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :--- |
|  | LOW | TYP | HI | LOW | TYP | HI |  |
|  |  |  |  |  |  |  |  |
| 1 | - | - | - | - | - | - | Key (no pin or wire) |
| 2 |  | 0 |  | 0 | 2.0 | 3.2 | Control Voltage Limit |
| 3 |  | 0 |  | 2.0 | 7.0 | 9.2 | Control Drive Voltage |
| 4 | 10.8 | 13.8 | 16.6 | 10.4 | 13.4 | 16.2 | Current Sense + |
| 5 | 0 | 0 | 0 | 9.2 | 9.4 | 9.8 | Keyed 9.4 |
| 6 | 10.8 | 13.8 | 16.6 | 10.4 | 13.4 | 16.2 | A+ to Command Board |
| 7 |  | 0 |  |  | 1.2 |  | Temp Sense (cutback begins at 3.3 V) |
| 8 | - | - | - | - | - | - | Key (no pin) |
| 9 |  | 0 |  | 1.5 | 3.5 | 5.0 | Forward Detect Volt |

Table 4-11. Power Control DC Voltage Chart (Continued)

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Low | TYP | HI | Low | TYP | HI |  |
| 10 | 10.8 | 13.8 | 16.6 | 10.4 | 13.4 | 16.2 | A+ to Command Board |
| 11 | 9.4 | 9.6 | 9.9 | 9.4 | 9.6 | 9.9 | 9.6-V Supply from Command Board |
| 12 | 10.8 |  |  | 9.8 | 13.1 | 15.9 | Current Sense - (voltage delta 150 mV ) |
| U0500 |  |  |  |  |  |  |  |
| 1 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 2 |  | 0 |  |  | 3.2 |  | Control AMP Input |
| 3 | 0 | 0 | 0 | 0 | 0 | 0 | Control AMP Input (not used) |
| 4 |  | 0 |  | 0 | 2 | 3.2 | Control Voltage Limit (cutback at 3.3 V ) |
| 5 |  | 0 |  |  | 0 |  | N.C. |
| 6 | 1.5 | 3.0 | 4.5 | 1.5 | 3.0 | 4.5 | Power Set from D-A (max power at 1.5 V ) |
| 7 |  | 0 | 0 | 1.5 | 3.0 | 4.5 | Power Set Buffer Out |
| 8 |  | 0 |  | 1.3 | 3.5 | 6.0 | Coupler Buffer Out |
| 9 |  | 0 |  | 1.3 | 3.5 | 6.0 | Forward Detect Voltage |
| 10 |  | 0 |  |  | 0 |  | Reflected Power Detect (not used) |
| 11 |  | 0 |  | 1.3 | 3.5 | 6.0 | Same as pin 8 (not used) |
| 12 |  | 0 |  | 0 | 1.2 | 6.0 | Thermister Buffer out (increases as PA gets hot) |
| 13 |  | 0 |  | 0 | 1.2 | 6.0 | Thermister Buffer in |
| 14 |  | 5.0 |  |  | 5.0 |  | $5-\mathrm{V}$ Sense Input (follows pin $20 \pm 0.1 \mathrm{~V}$ ) |
| 15 | 4.9 | 5.0 | 5.7 | 4.9 | 5.0 | 5.7 | 5-V Current Limit (limits at 5.7 V ) |
| 16 | 5.0 | 5.7 | 6.4 | 5.0 | 5.7 | 6.4 | 5-V Series Pass Drive (6.4 at max current) |
| 17 | 9.5 | 9.6 | 9.9 | 9.5 | 9.6 | 9.9 | 9.6-V Sense Input |
| 18 |  | 7 |  |  | 7 |  | 5-V Reg. Compensation Capacitor |
| 19 |  | 5.7 |  |  | 5.7 |  | N.C. |
| 20 | 4.9 | 5.0 | 5.1 | 4.9 | 5.0 | 5.1 | 5-V Reference Input (UNSW5-V) |
| 21 |  | 1.2 |  |  | 1.2 |  | 9.6-V Reg. Compensation Capacitor |
| 22 |  | 0 |  |  | 0 |  | N.C. |
| 23 |  | 0.9 | 9.6 |  | 1.2 | 9.6 | 9.6-V Series Pass Drive |
| 24 |  | 2.9 |  |  | 3.3 |  | Regulator Enable/Compensation |
| 25 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |

Table 4-11. Power Control DC Voltage Chart (Continued)

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Low | TYP | HI | Low | TYP | Hi |  |
| 26 |  | 0 |  |  | 0 |  | N.C. |
| 27 |  | 13.6 |  |  | 13.6 |  | N.C. |
| 28 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 29 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 30 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 31 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 32 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | Decoupled A+ |
| 33 | 4.0 | 5.0 |  |  | 0 | 0.2 | TX PA Enable (from U520-25) |
| 34 |  | 0 |  |  | 1.3 |  | Control AMP one-shot |
| 35 |  | 0 |  |  | 0 |  | Lock (5-V of Synth Out of Lock) |
| 36 |  | 0 |  |  | 0.8 |  | Control AMP one-shot |
| 37 | 10.8 | 13.6 | 16.3 | 10.0 | 13.0 | 16.0 | A+ (Current Sense + ${ }^{\text {a }}$ |
| 38 | 10.8 | 13.6 | 16.3 | 10.0 | 13.0 | 16.0 | Current Sense - Voltage Delta 150 mV (30 Watt only) |
| 39 |  | 0 |  | 9.2 | 9.4 | 9.8 | Keyed 9.4-V in |
| 40 | 1.5 | 3.0 | 4.5 | 1.5 | 3.0 | 4.5 | Current Limit D-A (max current at 4.5 V ) |
| 41 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 42 |  | 0 |  |  | 2.2 | 9.6 | Control AMP Output (Approx 1/2-V Control) |
| 43 |  | 1.3 |  |  | 7.0 |  | Loop Integrator Capacitor |
| 44 |  | 2.1 |  |  | 3.2 |  | Control AMP Reference |
| Q0500E |  | 13.0 |  |  | 13.0 |  | A+ - CR0500 Drop |
| Q0501C |  | 12.3 |  |  | 12.3 |  | VQ0500E - B/E Drop |
| Q0501E |  | 0.2 |  |  | 0.2 |  | $V$ pin 23 - B/E Drop |
| Q0503E |  | 0 |  |  | 1.5 |  | $V$ pin $42-\mathrm{B} / \mathrm{E}$ Drop (TX) |
| Q0503C |  | 13.6 |  |  | 9.0 |  |  |
| Q0504B |  | 13.6 |  |  | 12.9 |  | A + - B/E Drop (TX) |

NOTE: For antenna switch transmit bias conditions, RF drive must be removed from PA.

Table 4-12. Antenna Switch DC Voltage Chart

| LOCATION |  | TYPICAL RX | TYPICAL TX <br> NO PRE- <br> DRIVE | COMMENTS |
| :--- | :---: | :---: | :---: | :---: |
| CR3920 | ANODE | 0 | 1.6 |  |
|  | CATHODE | 0 | 0.8 |  |
|  | ANODE | 0 | 0.8 |  |
|  | CATHODE | - | - |  |
| CR3922 | ANODE | 0 | $<0.8$ |  |
|  | CATHODE | - | - |  |

### 4.5.1.2.3 Localizing Problems

Failure locations often can be determined by externally measured symptoms. Basic symptoms are noted below with probable failure locations.

1. Low Power and High Current

- Check for improper load conditions caused by high VSWR external to the radio.
- Check output coax and mini-UHF connector.
- Check harmonic filter.
- Check output impedance-matching circuitry from the final device to the harmonic filter.

2. Low Power and Low Current

- If control voltage drive is equal to 9.2 V , then check per the above.
- If control voltage drive is less than 9.2 V , then check the control circuitry.

3. Power Intermittently Low (or Zero) and Current Less than 1 A. When Power Drops

- Check LLA stage.

4. Power Zero and Current Greater Than 3 A.

- Check harmonic filter, antenna switch, and matching circuits beyond final stage.

5. Power Zero and Current Between 1 and 3 A.

- Check driver and/or final stages.

6. Power Zero and Current Less Than 1 A.

- Check LLA/driver circuitry.


### 4.5.1.2.4 Isolating Failures

Methods of analyzing individual stages of the Power Amplifiers are detailed below. Most of the stages are Class C and must be analyzed under relatively high RF power levels. Generators capable of such levels may not be available in all service shops, therefore the tests below are arranged in order of ascending power. This tends to allow the preceding stage to be the source of RF power for testing the next stage.

## Testing Low-Level Amplifier (LLA) Circuitry

The required DC and RF conditions are defined in Table 4-10. Measure LLA voltages according to Table 4-13.

If the above DC bias conditions are correct, check to see if the LLA is providing drive power to the pre-driver, Q3804. Do so by checking Q3804's collector current under normal drive conditions, as follows:

- Remove R3810 and L3806 (Be sure to reinstall after testing.)
- Solder wires to the remaining pads.
- Place an ammeter in series with Q3804 collector.
- Check for 0.1 to 0.5 A . (depending on control voltage).

NOTE: With no RF drive to the input of the PA, Q3804's collector current should be zero.
Table 4-13. LLA and Driver Typical Voltages

| CONTROL VOLTAGE | RF DRIVE OFF |  | RF DRIVE ON |  |
| :---: | :---: | :---: | :---: | :---: |
|  | 9.2 V | 6.0 V | 9.2 V | 6.0 V |
| Q3801 | - | - | - | - |
| Base | 0.7 | 0.7 | 0.7 | 0.5 |
| Collector | 8.3 | 9.0 | 8.0 | 8.8 |
| Q3802 | - | - | - | - |
| Base | 7.7 | 8.4 | 7.5 | 8.2 |
| Collector | 2.0 | 1.4 | 2.3 | 1.2 |
| Emitter | 8.3 | 9.0 | 8.0 | 8.8 |
| Q3806 | - | - | - | - |
| Base | 5.1 | 4.1 | 5.1 | 4.1 |
| Collector | 7.7 | 8.4 | 7.5 | 8.2 |
| Emitter | 4.5 | 3.4 | 4.5 | 3.4 |
| Q3804 | - | - | - | - |
| Base | 0.5 | 0.5 | 0.0 | 0.2 |
| Collector | 13.8 | 13.8 | 13.3 | 13.4 |

If Q3804 draws no current under normal conditions, then check for shorted or open input cable, or for defective parts in the input network or matching circuitry between Q3801 and Q3804. If all of the above check out OK, then replace Q3801.

## Testing Driver Circuitry

The driver is a typical Class-C stage, except the base is biased with resistors R3809 and R3810. The necessary conditions for proper operation of this stage are input drive power, and bias conditions as shown in Table 4-13.

NOTE: If it is necessary to replace Q3804, use a hot-air blower to remove and replace the part. It is important that the replacement device's case be properly soldered to its heatsink. Do so by flowing a small bead of solder around the rim of the device while it is clamped in the hot-air soldering device. The base and collector leads must be hand-soldered on the bottom side of the board.

## Troubleshooting the Final Device

- Make sure A+ is at the final's collector; if not, check for shorts and/or opens.
- Check the matching circuitry for shorts and/or opens. Also, check for faulty components.
- Measure the resistance from base to emitter; it should be less than 1 ohm. If not, check for proper soldering on L3852 and L3851; replace faulty component(s).
- Current drain on the final device should be >3.5 A. for $25-$ Watt operation. If low current, go on to the next step.
- Remove L3851 from the board and check the base-emitter and base-collector junction diode drops. Normal voltage drop should be between 0.4 and 1.0 V . If either junction is outside this range, replace the final device.

NOTE: When replacing either the driver or final device, apply thermal compound on the heatsink surface. Torque the screws to the correct value; see the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20).

## Testing the Antenna Switch and Harmonic Filter

Verify that most of this circuit is functioning properly by testing the receiver insertion loss as follows:

- Apply a low-level signal source at the antenna connector.
- Apply the conditions indicated in Table 4-10 for RX tests.
- Measure the power at the receive coax.
- If the difference between the input and output (insertion loss) is less than 1 dB , then the circuitry is functioning properly.
- Additional antenna switch tests are:
- Check CR3920, CR3921, and CR3922 with an ohmmeter for forward and reverse continuity.
- In the transmit mode, adjust control voltage for 28 Watts at the antenna connector. Check for less than 10 mW at the end of the receive input cable. If power exceeds 10 mW , then check CR3922 and associated circuitry. Receiver sensitivity can degrade if power at this port exceeds 10 mW .
- Check for proper DC current through the PIN diodes; correct current is indicated if approximately 1.5 V is present at the junction of C3900 and L3900 during transmit mode.


## DO NOT measure bias directly at the PIN diodes while in transmit mode unless TX injection is removed.

## WARNING

### 4.5.1.2.5 Power Control and Protection Circuitry

Localizing Problems to a Circuit
Power leveling and current limiting are set to values detailed in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20). These values will vary from unit to unit, depending on the unique variations of each unit. If symptoms indicate that either of these circuits have failed, verify that the radio has been properly aligned before investigating the circuitry.

Temperature sense and control voltage limit are fixed by design and are not influenced by the alignment of the radio. If symptoms indicate that these circuits have failed, then troubleshoot the circuit.

The tests that follow are intended to provide a convenient means of verifying that a particular circuit is functioning properly. These tests will isolate the failure to a minimum number of components. Refer to the Theory of Operation and the schematic for information needed to identify the failed component(s).

## Temperature Sense Circuit Test

Temporarily install a 6.8 k ohm resistor in parallel with RT3876. Key the transmitter and monitor the output power. The power meter should read approximately one-half the rated power (12 Watts).

## Control-Voltage-Limit Circuitry Test

Disconnect the transmitter injection from the internal transceiver chassis. This will require removal of the power amplifier assembly. With all other connections in normal condition, key the transmitter and monitor the control voltage. If the voltage exceeds 9.2 V , troubleshoot the control voltage limit circuitry.

## Current-Limiting Circuitry Test

Refer to Chapter 6 of the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20) for current limit setting instructions. When ready to adjust current limit, decrease the relative current limit value with the keyboard per instructions. After several decrements, the current limit should begin to reduce power. After this test, reset the current limit to its original value. If the circuitry does not perform as indicated, troubleshoot the current limit circuitry.

## Power-Leveling Circuitry Test

With the radio connected for power measurements, vary the line voltage from 12.5 to 16 V . The power should not vary more than 2 Watts . At a line voltage of 13.8 V , vary the frequency using the three test modes. If power varies more than 2 Watts, measure the detected voltage on P0853, pin 9. If this voltage varies more than 0.2 V over line and frequency variations, the power control circuitry (most of which is located on the command board) may be malfunctioning. If the detected voltage varies less than 0.2 V , the problem is likely in diode CR3900, the harmonic filter, the antenna switch, or the output coax. Check continuity through the 12-pin DC connector P0853 on the PA board; check digital/analog circuitry, and check 5-V regulator operation. See Table 4-12, DC Voltage Chart, for typical values.

With the radio connected for power measurements and a disconnected TX injection coax, the detected voltage at P0853, pin 9 , should measure approximately 1.3 V .
NOTE: If any part of the power leveling circuitry is replaced, perform the power set procedure. See the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20) for details.

### 4.5.1.3 50 Watt Power Amplifiers

This information will help you troubleshoot the ASTRO Spectra radio. Use this information, along with the Theory of Operation, to diagnose and isolate the cause of failures. The principle tools needed to troubleshoot a circuit to the component level are the schematic and the Theory of Operation.

In addition to the schematic and theory, this section includes troubleshooting information that will help you test and check the circuits to localize and isolate problems.
Prior to troubleshooting, it is important to review the Theory of Operation, including specific precautions and troubleshooting methods. Because much of the radio's circuitry operates at high frequency, measurements must be taken very carefully. Notes and cautions are added to the text to alert the reader to this need in areas of greatest sensitivity. However, the need for extreme care does exist in all measurements and tests at high frequency.

### 4.5.1.3.1 General Troubleshooting and Repair Notes

Most of the common transmitter symptoms are caused by either failure of the power amplifier or a failure in the control circuitry. The initial troubleshooting effort should be toward isolating the problem to one of those two areas. If either the control voltage or keyed 9.4 V are zero, then the problem is likely to be in the control circuit. If those voltages are present, then the problem is more likely in the power amplifier circuit.
If, for diagnostic reasons, a chip component needs to be removed to facilitate testing, such as a series capacitor removed to allow for signal insertion, then the components (s) returned to the circuit should be new parts. The application of a soldering iron to many chip components will tend to cause leaching which could lead to failure.

After a PA board is replaced, or if any power control circuitry components are replaced, readjust the power according to instructions in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20).

NOTE: Due to high operating frequencies, you must use specified Motorola parts when component replacement is necessary. Substitute components may not work. It is also critical that you use great care when replacing parts. Excessive solder or flux, longer than original leads on coax connectors, misorientation of parts, and other commonly benign imperfections may cause the radio's performance to degrade.

### 4.5.1.3.2 PA Functional Testing

To test the PA assembly for proper operation, perform the following steps:

1. Disassemble the PA assembly from the radio, leaving the power cable connected to the rear connector. Replace the PA shield and cover. Disconnect the coax connectors and the ribbon cable. Connect a power meter to the antenna port using minimum cable length.
a. When setting or measuring RF power, follow these guidelines to avoid measurement errors due to cable losses or non-50-ohm connector VSWR:

- All cables should be very short and have Teflon dielectric.
- Attenuators and 50 ohm loads should have at least 25 dB return loss.
- Mini UHF to 'N' adapter, P/N 58-803671321, should be used at the antenna connector. All other connectors should be ' N ' type. No other adapters, barrel connectors, etc. should be used.
b. Maximum input level to the PA is 20 mW . Too much input power could result in damage to the LLA stage.

2. Apply the input power and DC voltages indicated in Table 4-14 to the power amplifier assembly. To make the DC connections, use small spring clips or make a test adapter similar to that shown in Figure 4-6.

Table 4-14. DC Voltages and Input Power Chart

| Test | Keyed 9.4 V | CONTROL <br> VOLTAGE <br> DRIVE | POWER IN <br> (mW) | A+.V |
| :---: | :---: | :---: | :---: | :---: |
| Transmit | 9.4 | See note $^{\mathrm{a}}$ | 10 | 13.4 |
| Receive | 0 | 0 | 0 | 13.4 |

a. Set initially to zero. Increase value until power equals 28 wafts or 9.2 V maximum. Do NOT exceed 9.2 V.
3. Apply the required input power via an adapter cable. For this application, non 'N' type connectors are acceptable.
4. With the applied control voltage initially at 0 V , slowly increase the voltage until power out equals 55 Watts. Power should rise smoothly with control voltage once the turn-on threshold is reached. Control voltage should not exceed 8.0 V .
5. If 8.0 V does not produce 55 Watts , then a failure exists in the power amplifier circuit.
6. Refer to the voltage chart (see Table 4-15). Measure the indicated voltages. If they are not within the limits shown on chart, then a failure exists in the PA assembly.
7. If the voltages in the chart are correct, verify that the injection is at least 10 mW (see the VCO Troubleshooting Section).
8. If no failure is located from the previous checks, troubleshoot the power control circuitry.


Figure 4-6. PA Test Adapter, 50 Watt Power Amplifier

Table 4-15. Power Control DC Voltage Chart

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Low | TYP | HI | Low | TYP | HI |  |
| P0853 |  |  |  |  |  |  |  |
| 1 | - | - | - | - | - | - | Key (no pin or wire) |
| 2 |  | 0 |  | 0 | 2.0 | 3.2 | Control Voltage Limit |
| 3 |  | 0 |  | 2.0 | 7.0 | 9.2 | Control Drive Voltage |
| 4 | 10.8 | 13.8 | 16.6 | 10.4 | 13.4 | 16.2 | Current Sense + |
| 5 | 0 | 0 | 0 | 9.2 | 9.4 | 9.8 | Keyed 9.4 |
| 6 | 10.8 | 13.8 | 16.6 | 10.4 | 13.4 | 16.2 | A+ to Command Board |
| 7 |  | 0 |  |  | 1.2 |  | Temp Sense (cutback begins at 3.3 V ) |
| 8 | - | - | - | - | - | v | Key (no pin) |
| 9 |  | 0 |  | 1.5 | 3.5 | 5.0 | Forward Detect Volt |
| 10 | 10.8 | 13.8 | 16.6 | 10.4 | 13.4 | 16.2 | A+ to Command Board |
| 11 | 9.4 | 9.6 | 9.9 | 9.4 | 9.6 | 9.9 | 9.6-V Supply from Command Board |
| 12 | 10.8 |  |  | 9.8 | 13.1 | 15.9 | Current Sense - (voltage delta 150 mV ) |
| U0500 |  |  |  |  |  |  |  |
| 1 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 2 |  | 0 |  |  | 3.2 |  | Control AMP Input |

Table 4-15. Power Control DC Voltage Chart (Continued)

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Low | TYP | HI | Low | TYP | HI |  |
| 3 | 0 | 0 | 0 | 0 | 0 | 0 | Control AMP Input (not used) |
| 4 |  | 0 |  | 0 | 2 | 3.2 | Control Voltage Limit (cutback at 3.3 V ) |
| 5 |  | 0 |  |  | 0 |  | N.C. |
| 6 | 1.5 | 3.0 | 4.5 | 1.5 | 3.0 | 4.5 | Power Set from D-A (max power at 1.5 V ) |
| 7 |  | 0 | 0 | 1.5 | 3.0 | 4.5 | Power Set Buffer Out |
| 8 |  | 0 |  | 1.3 | 3.5 | 6.0 | Coupler Buffer Out |
| 9 |  | 0 |  | 1.3 | 3.5 | 6.0 | Forward Detect Voltage |
| 10 |  | 0 |  |  | 0 |  | Reflected Power Detect (not used) |
| 11 |  | 0 |  | 1.3 | 3.5 | 6.0 | Same as pin 8 (not used) |
| 12 |  | 0 |  | 0 | 1.2 | 6.0 | Thermister Buffer out (increases as PA gets hot) |
| 13 |  | 0 |  | 0 | 1.2 | 6.0 | Thermister Buffer in |
| 14 |  | 5.0 |  |  | 5.0 |  | $5-\mathrm{V}$ Sense Input (follows pin $20 \pm 0.1 \mathrm{~V}$ ) |
| 15 | 4.9 | 5.0 | 5.7 | 4.9 | 5.0 | 5.7 | 5-V Current Limit (limits at 5.7 V ) |
| 16 | 5.0 | 5.7 | 6.4 | 5.0 | 5.7 | 6.4 | 5-V Series Pass Drive (6.4 at max current) |
| 17 | 9.5 | 9.6 | 9.9 | 9.5 | 9.6 | 9.9 | 9.6-V Sense Input |
| 18 |  | 7 |  |  | 7 |  | 5-V Reg. Compensation Capacitor |
| 19 |  | 5.7 |  |  | 5.7 |  | N.C. |
| 20 | 4.9 | 5.0 | 5.1 | 4.9 | 5.0 | 5.1 | 5-V Reference Input (UNSW5-V) |
| 21 |  | 1.2 |  |  | 1.2 |  | 9.6-V Reg. Compensation Capacitor |
| 22 |  | 0 |  |  | 0 |  | N.C. |
| 23 |  | 0.9 | 9.6 |  | 1.2 | 9.6 | 9.6-V Series Pass Drive |
| 24 |  | 2.9 |  |  | 3.3 |  | Regulator Enable/Compensation |
| 25 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 26 |  | 0 |  |  | 0 |  | N.C. |
| 27 |  | 13.6 |  |  | 13.6 |  | N.C. |
| 28 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 29 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 30 | - | - | - | - | v | - | 9.6-V Programming (N.C.) |
| 31 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |

Table 4-15. Power Control DC Voltage Chart (Continued)

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :--- |
|  | LOW | TYP | HI | LOW | TYP | HI |  |
| 32 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | Decoupled A+ |
| 33 | 4.0 | 5.0 |  |  | 0 | 0.2 | TX PA Enable (from U520-25) |
| 34 |  | 0 |  |  | 1.3 |  | Control AMP one-shot |
| 35 |  | 0 |  |  | 0 |  | Lock (5-V of Synth Out of Lock) |
| 36 |  | 0 |  |  | 0.8 |  | Control AMP one-shot |
| 37 | 10.8 | 13.6 | 16.3 | 10.0 | 13.0 | 16.0 | A+ (Current Sense +) |
| 38 | 10.8 | 13.6 | 16.3 | 10.0 | 13.0 | 16.0 | Current Sense - Voltage Delta 150 mV (30 Watt <br> only) |
| 39 |  | 0 |  | 9.2 | 9.4 | 9.8 | Keyed 9.4-V in |
| 40 | 1.5 | 3.0 | 4.5 | 1.5 | 3.0 | 4.5 | Current Limit D-A (max current at 4.5 V) |
| 41 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 42 |  | 0 |  |  | 2.2 | 9.6 | Control AMP Output (Approx 1/2-V Control) |
| 43 |  | 1.3 |  |  | 7.0 |  | Loop Integrator Capacitor |
| 44 |  | 2.1 |  |  | 3.2 |  | Control AMP Reference |
| Q0500E |  | 13.0 |  |  | 13.0 |  | A+ - CR0500 Drop |
| Q0501C |  | 12.3 |  |  | 12.3 |  | VQ0500E - B/E Drop |
| Q0501E |  | 0.2 |  |  | 0.2 |  | V pin 23 - B/E Drop |
| Q0503E |  | 0 |  |  | 1.5 |  | V pin 42 - B/E Drop (TX) |
| Q0503C |  | 13.6 |  |  | 9.0 |  |  |
| Q0504B |  | 13.6 |  |  | 12.9 |  | A+ - B/E Drop (TX) |

### 4.5.1.3.3 Localizing Problems

Failure locations often can be determined by externally measured symptoms. Basic symptoms are noted below with probable failure locations.

1. Low Power and High Current

- Check for improper load conditions caused by high VSWR external to the radio.
- Check output coax and mini UHF connector.
- Check harmonic filter.
- Check output impedance-matching circuitry from the final device to the harmonic filter.

2. Low Power and Low Current

- If control voltage drive is equal to 8.0 V , then check per the above.
- It control voltage drive is less than 8.0 V , then check the control circuitry.

3. Power Intermittently Low (or Zero) and Current Less than 1 A. When Power Drops

- Check LLA stage.

4. Power Zero and Current Greater Than 5 A.

- Check harmonic filter, antenna switch, and matching circuits beyond final stage.

5. Power Zero and Current Between 2 and 5 A .

- Check driver and/or final stages.

6. Power Zero and Current Less Than 1 A.

- Check LLA/driver circuitry.


### 4.5.1.3.4 Isolating Failures

Methods of analyzing individual stages of the power amplifiers are detailed below. Most of the stages are Class $C$ and must be analyzed under relatively high RF power levels. Generators capable of such levels may not be available in all service shops, therefore the tests below are arranged in order of ascending power. This tends to allow the preceding stage to be the source of RF power for testing the next stage.

## Testing Low-Level Amplifier (LLA) Circuitry

The required DC and RF conditions are defined in Table 4-15. Measure LLA voltages according to Table 4-16.

If the above DC bias conditions are correct, check to see if the LLA is providing drive power to the driver Q3804. Do so by checking Q3804's collector current under normal drive conditions, as follows:

- Remove R3810 and L3806 (Be sure to reinstall after testing).
- Solder wires to the remaining pads.
- Place an ammeter in series with the collector of Q3804.
- Check for 0.1 to 0.5 A . depending on the control voltage.

NOTE: With no RF drive to the input of the PA, the collector current of Q3804 should be zero.
Table 4-16. LLA and Pre-Driver Typical Voltages

| CONTROL <br> VOLTAGE | RF DRIVE OFF |  | RF DRIVE ON |  |
| :--- | :---: | :---: | :---: | :---: |
|  | $\mathbf{9 . 2 ~ V}$ | $\mathbf{6 . 0} \mathbf{V}$ | $\mathbf{9 . 2 ~ V}$ | $\mathbf{6 . 0} \mathbf{V}$ |
| Q3801 | - | - | - | - |
| Base | - | 0.7 | 0.7 | 0.5 |
| Collector | 0.7 | 9.0 | 8.0 | 8.8 |
| Q3802 | 8.3 | - | - | - |
| Base | - | 8.4 | 7.5 | 8.2 |
| Collector | 7.7 | 1.4 | 2.3 | 1.2 |
| Emitter | 2.0 | 9.0 | 8.0 | 8.8 |
| Q3806 | - | - | - | - |
| Base | 5.1 | 4.1 | 5.1 | 4.1 |
| Collector | 7.7 | 8.4 | 7.5 | 8.2 |
| Emitter | 4.5 | 3.4 | 4.5 | 3.4 |
| Q3804 | - | - | - | - |
| Base | 0.5 | 0.5 | 0.0 | 0.2 |
| Collector | 13.8 | 13.8 | 13.3 | 13.4 |

If the above DC bias conditions are correct, check to see if the LLA is providing drive power to the pre-driver, Q3804. Do so by checking Q3804's collector current under normal drive conditions, as follows:

- Remove R3810 and L3806 (Be sure to reinstall after testing).
- Solder wires to the remaining pads.
- Place an ammeter in series with the collector of Q3804. Check for 0.1 to 0.5 A . depending the control voltage.

NOTE: With no RF drive to the input of the PA, Q3804's collector current should be zero.
If Q3804 draws no current under normal conditions, then check for a shorted or open input cable, or for defective parts in the input network or matching circuitry between Q3801 and Q3804. If all the above check out OK, then replace Q3801.

## Testing Pre-Driver Circuitry

The pre-driver is a typical Class C stage, except the base is biased with resistors R3809 and R3806. The necessary conditions for proper operation of this stage are input drive power, and bias conditions as shown in Table 4-16 above.

NOTE: If it is necessary to replace Q3804, use a hot-air blower to remove and replace the part. It is important that the replacement device's case be properly soldered to its heatsink. Do so by flowing a small bead of solder around the rim of the device while it is clamped in the hot-air soldering device. The base and collector leads must be hand-soldered on the bottom of the board.

## Troubleshooting the Driver Stage

- Make sure $A+$ is at the collector.
- Check for shorts and/or opens in the matching circuitry. Also look for faulty components.
- Measure the DC resistance from base to emitter. It should be less than 1 ohm. If not, check L3810 for proper soldering and replace if faulty.
- Check the current drain of the driver. It should be around 0.5 to 2.5 A . for $50-\mathrm{Watt}$ operation. If current drain is low, go to next step.
- Unsolder the base lead. Making sure the lead is not touching the PC board, check the base-emitter and base-collector junction diode drops. Normal voltage drop should be between 0.4 and 1.0 V . If either junction reads outside this range, replace the driver device.
- Unsolder either L3854, R3875, or L3851 to isolate the driver and final stages. Measure the collector emitter DC resistance. If the resistance is below 5 k ohms, then replace the driver device.


## Troubleshooting the Final Device

- Make sure $A+$ is at the final's collector; if not, check for shorts and/or opens.
- Check the matching circuitry for shorts and/or opens. Also, check for faulty components.
- Measure the resistance from base to emitter; it should be less than 1 ohm. If not, check for proper soldering on L3852 and L3853; replace faulty component (s).
- Current drain on the final device should be $>6 \mathrm{~A}$. for $50-$ Watt operation. If low current, go on to the next step.
- Remove L3853 from the board and check the base-emitter and base-collector junction diode drops. Normal voltage drop should be between 0.4 and 1.0 V . If either junction is outside the range, replace the final device.
- Unsolder either L3859, R3875, or L3851 to isolate the driver and final stages. Measure the collector emitter DC resistances. If the resistance is below 5 k ohms, then replace the driver device.

NOTE: The position of capacitors C3853 and C3854 is critical to the performance of the circuit. If they are removed for any reason, they must be re-installed as close to the cap of the final device as possible. When replacing either the driver or final device, apply thermal compound on the heatsink surface. Torque the screws to the correct value; see the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20).

## Testing the Antenna Switch and Harmonic Filter

Verify that most of this circuit is functioning properly by testing the receiver insertion loss as follows:

- Apply a low-level signal source at the antenna connector.
- Apply the conditions indicated in Table 4-14 for RX tests.
- If the difference between the input and output (insertion loss) is less than 1 dB , then the circuitry is functioning properly.
Additional antenna switch tests are:
- Check CR3920, CR3921, and CR3922 with an ohm meter for forward and reverse continuity.
- In the transmit mode, adjust the control voltage for 55 Watts at the antenna connector. Check for less than 10 mW at the end of the receive input cable. If power exceeds 10 mW , then check CR3922 and associated circuitry. Receiver sensitivity can degrade if power at this port exceeds 10 mW .
- Check for proper DC current through the PIN diodes; correct current is indicated if approximately 1.5 V is present at the junction of C3900 an L3900 during the transmit mode.



## DO NOT measure bias directly at the PIN diodes while in transmit mode unless TX injection is removed.

## WARNING

### 4.5.1.3.5 Power Control and Protection Circuitry

## Localizing Problems to a Circuit

Power leveling and current limiting are set to values detailed in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20). These values will vary from unit to unit, depending on the unique variations of each unit. If symptoms indicate that either of these circuits have failed, verify that the radio has been properly aligned before investigating the circuitry.

Temperature sense and control voltage limit are fixed by design and are not influenced by the alignment of the radio. If symptoms indicate that these circuits have failed, then troubleshoot the circuit.

The tests that follow are intended to provide a convenient means of verifying that a particular circuit is functioning properly. These tests will isolate the failure to a minimum number of components. Refer to the Theory of Operation and the schematic for information needed to identify the failed component(s).

## Temperature Sense Circuit Test

Temporarily place a leaded 6.8k ohm resistor in parallel with RT3875. Key the transmitter and monitor the output power. The power meter should read approximately $1 / 2$ the rated power (25 Watts).

## Control-Voltage-Limit Circuitry Test

Disconnect the transmitter injection from the internal transceiver chassis. This will require removal of the power amplifier assembly. With all other connections in normal condition, key the transmitter and monitor the control voltage. It the voltage exceeds 9.0 V , troubleshoot the control voltage limit circuitry.

## Current-Limiting Circuitry Test

When ready to adjust current limit, decrease the relative current limit value with the keyboard per instructions. After several decrements, the current limit should begin to reduce power. After this test, reset the current limit to its original value. If the circuitry does not perform as indicated, troubleshoot the current limit circuitry.

## Power-Leveling Circuitry Test

With the radio connected for power measurements, vary the line voltage from 12.5 to 16 V . The power should not vary more than 12.5 to 16 V . The power should not vary more than 2 Watts. At a line voltage of 13.6 V , vary the frequency using the three test modes.

If power varies more than 2 Watts, measure the detected voltage on P0853, pin 9. If this voltage varies more than 0.2 V over line and frequency variations, the power control circuitry (most of which is located on the command board) may be malfunctioning. If the detected voltage varies less than 0.2 V , the problem is probably in diode CR3900, the harmonic filter, the antenna switch, or the output coax. Check continuity through the 12-pin connector P0853 on the PA board; check digital/analog circuitry, and check 5-V regulator operation. See Table 4-15 for typical values.

With the radio connected for power measurements and a disconnected TX injection coax, the detected voltage at P0853, pin 9, should measure approximately 1.3 V .

NOTE: If any part of the power leveling circuitry is replaced, perform the power set procedure. See the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20) for details.

### 4.5.2 UHF Band

### 4.5.2.1 High-Power Amplifier

This information will help you troubleshoot the Spectra radio. Use this information, along with the Theory of Operation, to diagnose and isolate the cause of failures. This section includes troubleshooting information that will help you test and check the circuits to localize and isolate problems.

Prior to troubleshooting, it is important to review the Theory of Operation, including specific precautions and troubleshooting methods. Because much of the radio's circuitry operates at UHF frequencies, measurements must be taken very carefully. Notes and cautions are added to the text to alert the reader to this need in areas of greatest sensitivity. However, the need for extreme care does exist in all measurements and tests at UHF frequencies.

### 4.5.2.1.1 General Troubleshooting and Repair Notes

Most of the common transmitter symptoms are not necessarily caused by failure of circuits on the PA board. Failure of command board or synthesizer circuits can disable the transmitter. The initial troubleshooting effort should be toward isolating the problem to one of these areas. If either the control voltage or keyed 9.4 V are zero, then the problem is likely to be in the control circuit or synthesizer. If those voltages are present, then the problem is more likely in the power amplifier circuit.

If, for diagnostic reasons, a chip component needs to be removed to facilitate testing, such as a series capacitor removed to allow for signal insertion, then the component(s) returned to the circuit should be new parts. The application of a soldering iron to many chip components will tend to cause leaching which could lead to failure.

If the harmonic filter is damaged and needs to be replaced, then removal and replacement requires the use of a hot-air source capable of reflowing the solder beneath the filter hybrid. When replacing it, add small amounts of fresh solder paste to the silver regions beneath the ceramic to assure adequate electrical ground contact. Save the original input and output connectors (J-straps); these are not included with the replacement kit. No tuning is required. The harmonic filter may be ordered separately, but if the PA kit is ordered a filter kit comes with the PA kit.

After a PA board is replaced, or if any power control circuitry components are replaced, readjust the power according to instructions in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20).

NOTE: Due to high operating frequencies, you must use specified Motorola parts when component replacement is necessary. Substitute components may not work. It is also critical that you use great care when replacing parts. Excessive solder or flux, Longer than original leads on coax connectors, misorientation of parts, and other commonly benign imperfections, may cause the radio's performance to degrade.

Bench testing the high-power Spectra PA is most easily accomplished if a Spectra control head, control cable, and power cable are available on the test bench. This greatly simplifies the troubleshooting as several supply voltages are provided by the command board. Proper operation of the command board circuitry can be simultaneously verified.

Begin troubleshooting by connecting an RF power meter and appropriate power load to the antenna connector. Connect the control cable and the power cable. Make sure the ignition sense lead is also connected to the positive lead of the power supply. Note that a regulated DC power supply capable of at least 30 A . is necessary to power a high-power Spectra transmitter. Remove the radio bottom cover. Remove the PA shield by pulling straight up on the plastic handle. This must be done carefully, as the edge of the PA shield can damage components on the PA board if it is removed unevenly. Set the power supply to 13.4 V . The radio may now be turned on. All critical voltages may be measured at connector J 1 from the top side of the PA board. A diagram of the connector pin-out as viewed from the top side of the PA board is shown below.

Pin Configuration of J1
As Viewed From Top of PA Board


1 Control Voltage Limit
2 Control Voltage Drive
3 Current Sense +
4 Key 9.4V
5 Filtered A+
6 Temp-Sense
7 Not Connected
8 Forward Power Detect
9 9.6V
10 Current Sense -
11 Not Connected
Figure 4-7. Connector Pin-Out - High-Power Amplifier

Table 4-17. Power Control DC Voltage Chart

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Low | TYP | HI | Low | TYP | HI |  |
| J1 |  |  |  |  |  |  |  |
| 1 |  | 0 |  | 0 | 2.0 | 3.2 | Control Voltage Limit |
| 2 |  | 0 |  | 2.0 | 7.0 | 10.0 | Drive Voltage |
| 3 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | Current Sense + |
| 4 | 0 | 0 | 0 | 9.2 | 9.4 | 9.8 | Keyed 9.4 |
| 5 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | A+ to Command Board |
| 6 |  | 0 |  |  | 1.2 |  | Temp Sense (cutback begins at 3.3 V ) |
| 7 | - | - | - | - | - | - | Key (no pin) |
| 8 |  | 0 |  | 13.0 | 9.3 | 5.0 | Forward Detect Voltage |
| 9 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | A+ to Command Board |
| $\begin{aligned} & 10 \\ & 11 \end{aligned}$ | $\begin{gathered} \hline 9.4 \\ 10.8 \end{gathered}$ | $\begin{gathered} \hline 9.6 \\ 13.6 \end{gathered}$ | $\begin{gathered} \hline 9.9 \\ 16.5 \end{gathered}$ | $\begin{aligned} & 9.4 \\ & 9.8 \end{aligned}$ | $\begin{gathered} \hline 9.6 \\ 12.8 \end{gathered}$ | $\begin{gathered} \hline 9.9 \\ 15.8 \end{gathered}$ | 9.6-V Supply from Command Board Current Sense - (voltage delta 150 mV ) |
| 12 | - | - | - | - | - | - | Key (no pin or wire) |
| U0500 |  |  |  |  |  |  |  |
| 1 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 2 |  | 0 |  |  | 3.2 |  | Control AMP Input |
| 3 | 0 | 0 | 0 | 0 | 0 | 0 | Control AMP Input (not used) |
| 4 |  | 0 |  | 0 | 2 | 3.2 | Control Voltage Limit (cutback at 3.3 V ) |
| 5 |  | 9 |  |  | 0 |  | N.C. |
| 6 | 1.5 | 3.0 | 4.5 | 1.5 | 3.0 | 4.5 | Power Set from D-A (max power at 1.5 V ) |
| 7 |  | 0 |  | 1.5 | 3.0 | 4.5 | Power Set Buffer Out |
| 8 |  | 0 |  | 1.3 | 3.5 | 6.0 | Coupler Buffer Out |
| 9 |  | 0 |  | 1.3 | 3.5 | 6.0 | Forward Detect Volt |
| 10 |  | 0 |  |  | 0 |  | Reflected Power Detect (not used) |
| 11 |  | 0 |  | 1.3 | 3.5 | 6.0 | Same as pin 8 (not used) |
| 12 |  | 0 |  | 0 | 1.2 | 6.0 | Thermister Buffer out (increases as PA gets hot) |
| 13 |  | 0 |  | 0 | 1.2 | 6.0 | Thermister Buffer in |
| 14 |  | 5.0 |  |  | 5.0 |  | 5 -V Sense Input (follows pin $20 \pm 0.1 \mathrm{~V}$ ) |
| 15 | 4.9 | 5.0 | 5.7 | 4.9 | 5.0 | 5.7 | 5 -V Current Limit (limits at 5.7 V ) |

Table 4-17. Power Control DC Voltage Chart (Continued)

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Low | TYP | Hi | Low | TYP | Hi |  |
| J1 |  |  |  |  |  |  |  |
| 16 | 5.0 | 5.7 | 6.4 | 5.0 | 5.7 | 6.4 | 5-V Series Pass Drive (6.4 at max current) |
| 17 | 9.5 | 9.6 | 9.9 | 9.5 | 9.6 | 9.9 | 9.6-V Sense Input |
| 18 |  | 7 |  |  | 7 |  | 5-V Reg. Compensation Capacitor |
| 19 |  | 5.7 |  |  | 5.7 |  | N.C. |
| 20 | 4.9 | 5.0 | 5.1 | 4.9 | 5.0 | 5.1 | 5-V Reference Input (UNSW5-V) |
| 21 |  | 1.2 |  |  | 1.2 |  | 9.6-V Reg. Compensation Capacitor |
| 22 |  | 0 |  |  | 0 |  | N.C. |
| 23 |  | 0.9 | 9.6 |  | 1.2 | 9.6 | 9.6-V Series Pass Drive |
| 24 |  | 2.9 |  |  | 3.3 |  | Regulator Enable/Compensation |
| 25 | v | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 26 |  | 0 |  |  | 0 |  | N.C. |
| 27 |  | 13.6 |  |  | 13.6 |  | N.C. |
| 28 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 29 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 30 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 31 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 32 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | Decoupled A+ |
| 33 | 4.0 | 5.0 |  |  | 0 | 0.2 | $\overline{\text { TX PA Enable }}$ (from U520-25) |
| 34 |  | 0 |  |  | 1.3 |  | Control AMP one-shot |
| 35 |  | 0 |  |  | 0 |  | $\overline{\text { Lock ( }}$--V of Synth Out of Lock) |
| 36 |  | 0 |  |  | 0.8 |  | Control AMP one-shot |
| 37 | 10.8 | 13.6 | 16.3 | 10.0 | 13.0 | 16.0 | A+ (Current Sense + |
| 38 | 10.8 | 13.6 | 16.3 | 10.0 | 13.0 | 16.0 | Current Sense - Voltage Delta 150 mV |
| 39 |  | 0 |  | 9.2 | 9.4 | 9.8 | Keyed 9.4-V in |
| 40 | 1.5 | 3.0 | 4.5 | 1.5 | 3.0 | 4.5 | Current Limit D-A (max current at 4.5 V ) |
| 41 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 42 |  | 0 |  |  | 2.2 | 9.6 | Control AMP Output (Approx 1/2-V Control) |
| 43 |  | 1.3 |  |  | 7.0 |  | Loop Integrator Capacitor |

Table 4-17. Power Control DC Voltage Chart (Continued)

| LOCATION | RX MODE |  |  | TX MODE |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :--- |
|  | LOW | TYP | HI | LOW | TYP | HI |  |
|  | J1 |  |  |  |  |  |  |

Key the transmitter. The RF power meter should read at least 100 Watts if it is calibrated. Range 3 UHF radios will have power set to 78 Watts at modes above 470 MHz . R4 UHF radios will be set to 78 Watts on all modes. If power is low, the power set must be checked first before suspecting a defective PA or command board. This may be checked using a PC and RSS software. Alternatively, front panel programming may be used. Please refer to the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20) for programming instructions.

If correct power output can not be obtained by following the power set procedure outlined in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20), it is possible that current limit may be improperly set. This can not be adjusted using front panel programming. A PC with RSS must be used. A simple way to check for current limit engagement is to temporarily short out the current sense resistor R5875 with a piece of 12- or 14gauge wire. If full power is restored, then RSS must be used to properly set current limit.
If it is verified that both power set and current limit are not related to the power problem, then the synthesizer output must be checked. A milliwatt meter connected to the TX injection cable should indicate at least 30 mW of injection power during key-up. If this is not the case, refer to the RF Board and VCO troubleshooting procedures in this chapter.

If the command board and synthesizer are functioning properly, the PA must be defective. Details on troubleshooting each circuit of the PA follow.

### 4.5.2.1.2 PA Functional Testing

NOTE: When setting or measuring RF power at UHF, follow these guidelines to avoid measurement errors due to cable losses or non-50-ohm connector VSWR:

- All coaxial cables should be low loss and as short as possible.
- Attenuators and 50 -ohm loads should have at least 25 dB return loss.
- Mini UHF to 'N' adapter, P/N 58803671321, should be used at the antenna connector. All other connectors should be 'N' type. No other adapters, barrel connectors, etc. should be used.
Maximum input level to the PA is 50 mW . Too much input power could result in damage to the LLA stage.

Methods of analyzing individual stages of the power amplifiers are detailed below. Most of the stages are Class-C and must be analyzed under relatively high RF power levels. The following information should help in isolation and repair of the majority of transmitter failures.

## Testing Low-Level Amplifier (LLA) Circuitry

Proper operation of the LLA can be checked by monitoring the voltage across resistor R5805. The voltage should measure in the range of 0.4 to 1.2 V , depending on the value of control voltage. A 0.4V reading corresponds to a low control voltage ( 4 to 5 V ) and a 1.2-V reading corresponds to a high control voltage (up to control voltage limit).
Measure LLA voltages according to Table 4-18. If the DC bias conditions are correct, check to see if the LLA is providing drive power to Q5803. Do so by checking Q5803 collector current under normal drive conditions, as follows:

- Remove R5810 and L5806 (Be sure to reinstall after testing).
- Solder wires to the remaining pads.
- Place an ammeter in series with Q5803 collector.
- Check for 0.2 to 0.5 A . (depending on control voltage).

NOTE: With no RF drive to the input of the PA, Q5803 collector current should be zero.
Table 4-18. LLA and 2nd Stage Typical Voltages

| CONTROL VOLTAGE | RF DRIVE OFF |  | RF DRIVE ON |  |
| :---: | :---: | :---: | :---: | :---: |
|  | 10.0 V | 6.0 V | 10.0 V | 6.0 V |
| Q5801 | - | - | - | - |
| Base | 0.7 | 0.7 | 0.7 | 0.3 |
| Collector | 8.1 | 9.1 | 8.0 | 8.8 |
| Q5800 | - | - | - | - |
| Base | 7.6 | 8.5 | 7.4 | 8.3 |
| Collector | 2.3 | 1.4 | 2.8 | 1.1 |
| Emitter | 8.1 | 9.1 | 8.0 | 8.8 |
| Q5806 | - | - | - | - |
| Base | 6.4 | 3.8 | 6.4 | 3.9 |
| Collector | 7.6 | 8.5 | 7.4 | 8.3 |
| Emitter | 5.7 | 3.2 | 5.7 | 3.2 |
| Q5803 | - | - | - | - |
| Base | 0.6 | 0.6 | 0.0 | 0.3 |
| Collector | 9.6 | 9.6 | 9.5 | 9.5 |

NOTE: The LLA voltages change with different control voltages. An example of LLA voltages with control voltage equal to 10.0 V and 6 V is shown.

If Q5803 draws no current under normal conditions, then check for short or open input cable, or for defective parts in the transmit injection filter or matching circuitry between Q5801 and Q5803.

## Testing Second Stage Circuitry Q5803

The second stage is a typical class-C stage, except the base is biased with resistors R5809 and R5806. The necessary conditions for proper operation of this stage are input drive power, and bias conditions as shown in Table 4-18.

NOTE: If it is necessary to replace Q5803, use a hot-air blower to remove and replace the part. It is important that the replacement device's case be properly soldered to its heatsink. Do so by flowing a small bead of solder around the rim of the device while it is clamped in the hot-air soldering device. The base and collector leads must be hand-soldered on the bottom side of the board.

## Troubleshooting the Third Stage Q5850

- Make sure $A+$ is at the collector.
- Check for shorts and/or opens in the matching circuitry. Also look for faulty components (cracked parts or parts not properly soldered).
- Measure the DC resistance from base to emitter. It should be less than 1-ohm. If not, check L5851 and L5852 for proper soldering, and replace if faulty.
- Check the current drain of Q5850. Remove L5854 and R5850 and solder wires to the pads. With an ammeter connected to these wires, check the collector current drain during transmit. It should be around 1.5 to 2.0 A. If current drain is low, go to next step.
- Remove L5851 from the board and check the base-emitter and base-collector junction diode voltages using the diode check function of a multimeter. Normal voltage drop should be near 0.6 V. If either junction is open or short circuited replace the device.


## Troubleshooting the Driver Stage Q5851

- Make sure A+ is at the driver's collector. Check for shorts and or opens.
- Check the matching circuitry for shorts and/or opens. Also, check for faulty components. (Cracked parts or parts not properly soldered.)
- Measure the resistance from base to emitter; it should be less than 1 ohm. If not, check for proper soldering on L5855 and L5857. Replace faulty component(s).
- Current drain for this stage should be close to 5 A . If low current, go to the next step.
- Remove L5857 from the board and check the base-emitter and base-collector junction diode drops. Normal voltage drops should be near 0.6 V. If either junction is open or shorted, replace the device.

NOTE: The position of capacitors C5861, C5862, C5863 and C5864 is critical to the performance of the circuit. If they are removed for any reason, they must be re-installed in the exact same physical location from which they were removed.

## Analysis of the Final Amplifier Stage (Q5875 and Q5876)

Extreme care must be taken when troubleshooting the final amplifier due to the high RF currents and voltages present.

A visual inspection of the matching networks should be done first. Check for defective solder joints or burned components. Good soldering of the transistor device leads is essential. Make sure A+ voltage is reaching the collector of each final device.

Check the base-emitter and base-collector junctions of the final devices by removing L5877, L5876, and R5878. Using the diode check function of a multimeter, the junctions should have a forward voltage drop close to 0.6 V . Replace a final device if it has an open or shorted junction.

Capacitors C5885, C5886, C5887, and C5888 are placed on the bottom side of the PA board underneath the leads of the final devices. Extreme care should be used when replacing these parts. Exact positioning is critical. Inspect for solder shorts on these capacitors before installing the PA board in the radio chassis.

Installation of the PA board into the radio chassis must be done carefully. The PC board screws use a T-15 Torx bit and should be torqued to 6 to 8 inch-pounds. The device screws use a T-8 Torx bit and should be torqued to 6 to 8 inch-pounds. Always apply thermal compound to the area under the device flanges before installing the PA board.

Current drain of the final amplifier may be checked by measuring the voltage across R5875 during transmit. A voltage drop of 0.10 to 0.15 V indicates the finals are drawing 10 to 15 A ., which is within the acceptable range.

## Testing the Antenna Switch and Harmonic Filter

Use care when replacing the harmonic filter. Removal of the filter is best accomplished by heating the filter/PC board assembly with a heat gun or heat blower until the solder joint reflows.
Verify that the receive path of the antenna switch and the harmonic filter are functioning by testing the receiver insertion loss as follows:

- Apply a low-level signal source at the antenna connector.
- Verify the conditions indicated in Table 4-17 for RX tests.
- Measure the power at the receive coax.
- If the difference between the input and output (insertion loss) is less than 1 dB , then the circuitry is functioning properly. Additional antenna switch tests are:
- Check CR5900, CR5902, CR5904, and CR5905 using the diode-check function of a multimeter. Note that CR5904 and CR5905 are on the bottom side of the board. These two diodes affect the receive path only and are unrelated to transmitter problems.
- Check for proper DC current through the PIN diodes; correct current is indicated if approximately 1.5 V is present at the junction of R5900 and L5900 during transmit.


## DO NOT measure bias directly at the PIN diodes while in transmit mode unless TX injection is removed.

## WARNING

### 4.5.2.1.3 Power Control and Protection Circuitry

## Localizing Problems to a Circuit

Power leveling and current limiting are set to values detailed in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20). These values will vary from unit to unit, depending on the unique variations of each unit. If symptoms indicate that either of these circuits have failed, verify that the radio has been properly aligned before investigating the circuitry.
Temperature sense and control voltage limit are fixed by design and are not influenced by the alignment of the radio. If symptoms indicate that these circuits have failed, then troubleshoot the circuit.
The tests that follow are intended to provide a convenient means of verifying that a particular circuit is functioning properly. These tests will isolate the failure to a minimum number of components. Refer to the Theory of Operation and the schematic for information needed to identify the failed component(s).

## Temperature Sense Circuit Test

Temporarily install a 2.2 k ohm resistor in parallel with RT5875. Key the transmitter and monitor the output power. The power meter should read approximately one-half the rated power.

## Control-VoItage-Limit Circuitry Test

Disconnect J5901 (transmitter injection) from the PA input. With all other connections in normal condition, key the transmitter and monitor the control voltage at J1 pin 2. If the voltage exceeds 10.0 V, troubleshoot the control voltage limit circuitry.

## Current-Limiting Circuitry Test

When ready to adjust current limit, decrease the relative current limit value with the keyboard per instructions. After several decrements, the current limit should begin to reduce power in 0.5 to 1.0 Watt increments. After this test, reset the current limit to its original value. If the circuitry does not perform as indicated, troubleshoot the current limit circuitry.

## Directional Coupler and Power-Leveling Test

The directional coupler combined with the RPCIC form a closed-loop power leveling circuit. This circuit keeps forward power essentially constant under variations of line voltage, frequency, and VSWR.

The directional coupler samples a small amount of forward power during transmit. This power is rectified by a detector diode CR5906. This rectified DC voltage is fed back to the RPCIC where it is compared to a reference voltage. An error voltage is generated which is ultimately translated into the control voltage via RPCIC circuitry and amplifiers Q503 and Q504 on the command board. Control voltage is routed to the LLA stage, thereby completing the feedback loop. In operation, the control loop tends to maintain the forward detected voltage constant versus frequency and line voltage variations. Proper operation can be observed by monitoring the forward detected voltage while varying the supply voltage from 13.4 to 16.1 V . Forward-detected voltage should not change more than a few hundreths of a volt. Note that the forward power may not necessarily be level if one of the other protection circuits such as temp-sense or current limit are engaged.

## PA Voltage Protection Circuit

Some versions of the PA board may include a voltage protection circuit. This circuit is intended to prevent premature failure of a transmitter operated in extreme conditions. An example of an extreme condition would be operation at above normal battery voltages (greater than 15 V ) combined with high temperatures (greater than $500^{\circ} \mathrm{C}$ or $122^{\circ} \mathrm{F}$ ).

The circuit monitors the A+ voltage from the battery, and it is activated if the A+ voltage exceeds approximately 15 V . R5825 and R5823 form a voltage divider connected to A+. The divided A+ voltage is connected to the base of Q5805. The emitter of Q5805 is connected to Zener diode Z1. This 5-V Zener diode, combined with the voltage divider action of R5825 and R5823, sets the voltage "trip point" at which 05805 turns on (A+ near 15 V ). When Q5805 turns on, this provides a path for current to flow through the base-emitter junction of Q5802. Q5802 then acts as a switch to connect the K9.4 voltage supply to R5826 and the directional coupler circuit composed of C5924, R5916, R5905, and R5904. A fixed DC bias voltage is applied to the forward power detector.

This fixed DC bias voltage is summed with the rectified RF signal that is coupled from the output of the transmitter. Since the PA power control requires that the detected voltage is a constant value, the output power of the power amplifier must be reduced by an amount proportional to the applied DC bias. The values of R5916, R5905, and R5904 are chosen such that power is cut in half. The reduced output power decreases the current drain of the transmitter, and therefore reduces the internal temperature of the amplifier devices which increases their lifetime. The circuit disengages and full rated power is restored if the over-voltage condition is corrected.

## Low-Voltage Current Drain Cutback

An additional circuit associated with the over-voltage protection circuit is the low-voltage current drain circuit. This circuit acts to reduce the transmitter current drain under conditions of low supply voltage. This action extends the available transmit time when, for example, the transmitter in a vehicular installation must be used when the engine is not running. Operation of this circuit is similar to the over-voltage circuit. R5819 and R5820 form a voltage divider which is connected to the base of transistor Q5804. If the A+ voltage drops below approximately 12 V , Q5804 will begin to conduct. This turns on Q5802, which supplies a DC bias voltage to the forward power detector as explained in the Theory of Operation for the over-voltage protection circuit. The transmitter output power is reduced by the power control, which results in reduced current drain and extended battery life.

NOTE: If any part of the power leveling circuitry is replaced, perform the power set procedure. See the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20) for details.

## Miscellaneous Circuits and Notes

Diode CR5875 acts as a reverse-protect diode. This diode also protects from over-voltage conditions, as it has a Zener breakdown voltage of approximately 28 V . When replacing this diode, care must be taken to place the diode with the cathode marking ring down (towards the PC board).


Figure 4-8. Block Diagram for Spectra High-Power Power Amplifier

### 4.5.2.2 40 Watt Power Amplifiers

This information will help you troubleshoot the Spectra radio. Use this information, along with the Theory of Operation, to diagnose and isolate the cause of failures. The principle tools needed to troubleshoot a circuit to the component level are the schematic and the Theory of Operation.

In addition to the schematic and theory, this section includes troubleshooting information that will help you test and check the circuits to localize and isolate problems.

Prior to troubleshooting, it is important to review the Theory of Operation, including specific precautions and troubleshooting methods. Because much of the radio's circuitry operates at UHF frequencies, measurements must be taken very carefully. Notes and cautions are added to the text to alert the reader to this need in areas of greatest sensitivity. However, the need for extreme care does exist in all measurements and tests at UHF frequencies.

### 4.5.2.2.1 General Troubleshooting and Repair Notes

Most of the common transmitter symptoms are caused by either failure of the power amplifier or a failure in the control circuitry. The initial troubleshooting effort should be toward isolating the problem to one of those two areas. If either the control voltage or keyed 9.4 V are zero, then the problem is likely to be in the control circuit. If those voltages are present, then the problem is more likely in the power amplifier circuit.

If, for diagnostic reasons, a chip component needs to be removed to facilitate testing, such as a series capacitor removed to allow for signal insertion, then the component(s) returned to the circuit should be new parts. The application of a soldering iron to many chip components will tend to cause leaching which could lead to failure.

If the harmonic filter is damaged and needs to be replaced, then removal and replacement requires the use of a hot-air source capable of reflowing the solder beneath the filter hybrid. When replacing it, add small amounts of fresh solder paste to the silver regions beneath the ceramic to assure adequate electrical ground contact. Save the original input and output connectors (J-straps); these are not included with the replacement kit. No tuning is required. The harmonic filter may be ordered separately, but if the PA kit is ordered, a filter kit comes with the PA kit.

After a PA board is replaced, or if any power control circuitry components are replaced, readjust the power according to instructions in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20).

NOTE: Due to high operating frequencies, you must use specified Motorola parts when component replacement is necessary. Substitute components may not work. It is also critical that you use great care when replacing parts. Excessive solder or flux, longer than original leads on coax connectors, misorientation of parts, and other commonly benign imperfections may cause the radio's performance to degrade.

### 4.5.2.2.2 PA Functional Testing

Test the PA assembly for proper operation as follows:

1. Disassemble the PA assembly from the radio, leaving the power cable connected to the rear connector. Replace the PA shield and cover. Disconnect the coax connectors and the ribbon cable. Connect a power meter to the antenna port using minimum cable length.

- When setting or measuring RF power at UHF, follow these guidelines to avoid measurement errors due to cable losses or non-50-ohm connector VSWR:
- All cables should be very short and have Teflon dielectric.
- Attenuators and 50 -ohm loads should have at least 25 dB return loss.
- Mini UHF to 'N' adapter, P/N 5880367B21, should be used at the antenna connector. All other connectors should be ' N ' type. No other adapters, barrel connectors, etc. should be used.
- Maximum input level to the PA is 50 mW . Too much input power could result in damage to the LLA stage.

2. Apply the input power and DC voltages indicated in Table 4-19 to the power amplifier assembly. To make the DC connections, use small spring--clips or make a test adapter similar to that shown in "Figure 4-9. PA Test Adapter, 40 Watt Power Amplifier".
3. Apply the required input power via an adapter cable. For this application, non N-type connectors are acceptable.
4. With the applied control voltage initially at 0 V , slowly increase the voltage until power out equals 46 Watts. Power should rise smoothly with control voltage once the tum-on threshold is reached. Control voltage should not exceed 10.0 V .
5. If 10.0 V does not produce 46 Watts , then a failure exists in the power amplifier circuit.
6. Refer to the voltage chart (Table 4-20). Measure the indicated voltages. If they are not within the limits shown in the chart, then a failure exists in the PA assembly.
7. If the voltages in the chart are correct, verify that the injection is at least 30 mW . (See the VCO troubleshooting section.)
8. If no failure is located from the previous checks, troubleshoot the power control circuitry.

Table 4-19. DC Voltages and Input Power Chart

| Test | Keyed 9.4 V | $\mathbf{9 . 6} \mathbf{~ V}$ | CONTROL <br> VOLTAGE <br> DRIVE | POWER IN <br> $(\mathbf{m W})$ | A+.V |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Transmit | 9.4 | 9.6 | See note $^{\text {a }}$ | 30 | 13.0 |
| Receive | 0 | 9.6 | 0 | 0 | 13.0 |

a. Set initially to zero. Increase value until power equals 46 Wafts or 10.0 V maximum. Do NOT exceed 10.0 V .


Figure 4-9. PA Test Adapter, 40 Watt Power Amplifier

Table 4-20. Power Control DC Voltage Chart

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Low | TYP | HI | Low | TYP | Hi |  |
| P0853 |  |  |  |  |  |  |  |
| 1 | - | - | - | - | - | - | Key (no pin or wire) |
| 2 |  | 0 |  | 0 | 2.0 | 3.2 | Control Voltage Limit |
| 3 |  | 0 |  | 2.0 | 7.0 | 10.0 | Drive Voltage |
| 4 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | Current Sense + |
| 5 | 0 | 0 | 0 | 9.2 | 9.4 | 9.8 | Keyed 9.4 |
| 6 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | A+ to Command Board |
| 7 |  | 0 |  |  | 1.2 |  | Temp Sense (cutback begins at 3.3 V ) |
| 8 | - | - | - | - | - | - | Key (no pin) |
| 9 |  | 0 |  | 13.0 | 9.3 | 5.0 | Forward Detect Voltage |
| 10 | 10.8 | 13.8 | 16.6 | 10.4 | 13.4 | 16.2 | A+ to Command Board |
| 11 | 9.4 | 9.6 | 9.9 | 9.4 | 9.6 | 9.9 | 9.6-V Supply from Command Board |
| 12 | 10.8 | 13.6 | 16.5 | 9.8 | 12.8 | 15.8 | Current Sense - (voltage delta 150 mV ) |
| U0500 |  |  |  |  |  |  |  |
| 1 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 2 |  | 0 |  |  | 3.2 |  | Control AMP Input |
| 3 | 0 | 0 | 0 | 0 | 0 | 0 | Control AMP Input (not used) |
| 4 |  | 0 |  | 0 | 2 | 3.2 | Control Voltage Limit (cutback at 3.3 V ) |
| 5 |  | 0 |  |  | 0 |  | N.C. |
| 6 | 1.5 | 3.0 | 4.5 | 1.5 | 3.0 | 4.5 | Power Set from D-A (max power at 1.5 V ) |
| 7 |  | 0 |  | 1.5 | 3.0 | 4.5 | Power Set Buffer Out |
| 8 |  | 0 |  | 1.3 | 3.5 | 6.0 | Coupler Buffer Out |
| 9 |  | 0 |  | 1.3 | 3.5 | 6.0 | Forward Detect Voltage |
| 10 |  | 0 |  |  | 0 |  | Reflected Power Detect (not used) |
| 11 |  | 0 |  | 1.3 | 3.5 | 6.0 | Same as pin 8 (not used) |
| 12 |  | 0 |  | 0 | 1.2 | 6.0 | Thermister Buffer out (increases as PA gets hot) |
| 13 |  | 0 |  | 0 | 1.2 | 6.0 | Thermister Buffer in |
| 14 |  | 5.0 |  |  | 5.0 |  | $5-\mathrm{V}$ Sense Input (follows pin $20 \pm 0.1 \mathrm{~V}$ ) |

Table 4-20. Power Control DC Voltage Chart (Continued)

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Low | TYP | HI | Low | TYP | Hi |  |
| 15 | 4.9 | 5.0 | 5.7 | 4.9 | 5.0 | 5.7 | 5-V Current Limit (limits at 5.7 V ) |
| 16 | 5.0 | 5.7 | 6.4 | 5.0 | 5.7 | 6.4 | 5-V Series Pass Drive (6.4 at max current) |
| 17 | 9.5 | 9.6 | 9.9 | 9.5 | 9.6 | 9.9 | 9.6-V Sense Input |
| 18 |  | 7 |  |  | 7 |  | 5-V Reg. Compensation Capacitor |
| 19 |  | 5.7 |  |  | 5.7 |  | N.C. |
| 20 | 4.9 | 5.0 | 5.1 | 4.9 | 5.0 | 5.1 | 5-V Reference Input (UNSW5-V) |
| 21 |  | 1.2 |  |  | 1.2 |  | 9.6-V Reg. Compensation Capacitor |
| 22 |  | 0 |  |  | 0 |  | N.C. |
| 23 |  | 0.9 | 9.6 |  | 1.2 | 9.6 | 9.6-V Series Pass Drive |
| 24 |  | 2.9 |  |  | 3.3 |  | Regulator Enable/Compensation |
| 25 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 26 |  | 0 |  |  | 0 |  | N.C. |
| 27 |  | 13.6 |  |  | 13.6 |  | N.C. |
| 28 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 29 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 30 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 31 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 32 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | Decoupled A+ |
| 33 | 4.0 | 5.0 |  |  | 0 | 0.2 | TX PA Enable (from U520-25) |
| 34 |  | 0 |  |  | 1.3 |  | Control AMP one-shot |
| 35 |  | 0 |  |  | 0 |  | Lock (5-V of Synth Out of Lock) |
| 36 |  | 0 |  |  | 0.8 |  | Control AMP one-shot |
| 37 | 10.8 | 13.6 | 16.3 | 10.0 | 13.0 | 16.0 | A+ (Current Sense + ${ }^{\text {a }}$ |
| 38 | 10.8 | 13.6 | 16.3 | 10.0 | 13.0 | 16.0 | Current Sense - Voltage Delta 150 mV (30 Watt only) |
| 39 |  | 0 |  | 9.2 | 9.4 | 9.8 | Keyed 9.4-V in |
| 40 | 1.5 | 3.0 | 4.5 | 1.5 | 3.0 | 4.5 | Current Limit D-A (max current at 4.5 V ) |
| 41 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 42 |  | 0 |  |  | 2.2 | 9.6 | Control AMP Output (Approx 1/2-V Control) |

Table 4-20. Power Control DC Voltage Chart (Continued)

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :--- | :---: |
|  | LOW | TYP | HI | LOW | TYP | HI |  |  |  |
| 43 |  | 1.3 |  |  | 7.0 |  | Loop Integrator Capacitor |  |
| 44 |  | 2.1 |  |  | 3.2 |  | Control AMP Reference |  |
| Q0500E |  | 13.0 |  |  | 13.0 |  | A+ - CR0500 Drop |  |
| Q0501C |  | 12.3 |  |  | 12.3 |  | VQ0500E - B/E Drop |  |
| Q0501E |  | 0.2 |  |  | 0.2 |  | V pin 23 - B/E Drop |  |
| Q0503E |  | 0 |  |  | 1.5 |  | V pin 42 - B/E Drop (TX) |  |
| Q0503C |  | 13.6 |  |  | 9.0 |  |  |  |
| Q0504B |  | 13.6 |  |  | 12.9 |  | A+ - B/E Drop (TX) |  |

NOTE: For antenna switch transmit bias conditions, RF drive must be removed from PA.

Table 4-21. Antenna Switch DC Voltage Chart

| LOCATION |  | TYPICAL RX | TYPICAL TX <br> NO PRE- <br> DRIVE | COMMENTS |
| :--- | :---: | :---: | :---: | :---: |
| CR5920 | ANODE | 0 | 1.6 |  |
|  | CATHODE | 0 | 0.8 |  |
|  | ANODE | 0 | 0.8 |  |
|  | CATHODE | - | - |  |
| CR5922 | ANODE | 0 | $<0.8$ |  |
|  | CATHODE | - | - |  |

### 4.5.2.2.3 Localizing Problems

Failure locations often can be determined by externally measured symptoms. Basic symptoms are noted below with probable failure locations.

1. Low Power and High Current

- Check for improper load conditions caused by high VSWR external to the radio.
- Check output coax and mini-UHF connector.
- Check harmonic filter and J-straps for opens and/or shorts.
- Check output impedance-matching circuitry from the final device to the harmonic filter.

2. Low Power and Low Current

- If control voltage is equal to 10.0 V , then check per the above.
- If control voltage is less than 10.0 V , then check the control circuitry.

3. Power Intermittently Low (or Zero) and Current Less than 1 A. When Power Drops

- Check LLA stage.

4. Power Zero and Current Greater Than 2 A.

- Check harmonic filter, antenna switch, matching circuits between driver and final stages, and matching circuits beyond final stage.

5. Power Zero and Current Less Than 1 A.

- Check LLA/pre-driver circuitry.


### 4.5.2.2.4 Isolating Failures

Methods of analyzing individual stages of the power amplifiers are detailed below. Most of the stages are Class $C$ and must be analyzed under relatively high RF power levels. Generators capable of such levels may not be available in all service shops, therefore the tests below are arranged in order of ascending power. This tends to allow the preceding stage to be the source of RF power for testing the next stage.

1. Testing Low-Level Amplifier (LLA) Circuitry

The required DC and RF conditions are defined in Table 4-19. Measure LLA voltages according to Table 4-22.
If the above DC bias conditions are correct, check to see if the LLA is providing drive power to the pre-driver, Q5803. Do so by checking Q5803 collector current under normal drive conditions, as follows:

- Remove R5810 and L5806 (Be sure to reinstall after testing.)
- Solder wires to the remaining pads.
- Place an ammeter in series with Q5803 collector.
- Check for 0.2 to 0.5 A. (depending on control voltage).

NOTE: With no RFdrive to the input of the PA, Q5803 collector current should be zero.
Table 4-22. LLA and Pre-Driver Typical Voltages

| CONTROL <br> VOLTAGE | RF DRIVE OFF |  | RF DRIVE ON |  |
| :--- | :---: | :---: | :---: | :---: |
|  | $\mathbf{1 0 . 0} \mathbf{V}$ | $\mathbf{6 . 0} \mathbf{V}$ | $\mathbf{1 0 . 0} \mathbf{V}$ | $\mathbf{6 . 0} \mathbf{V}$ |
| Q5801 | - | - | - | - |
| Base | 0.7 | 0.7 | 0.7 | 0.3 |
| Collector | 8.1 | 9.1 | 8.0 | 8.8 |
| Q5800 | - | - | - | - |
| Base | 7.6 | 8.5 | 7.4 | 8.3 |
| Collector | 2.3 | 1.4 | 2.8 | 1.1 |
| Emitter | 8.1 | 9.1 | 8.0 | 8.8 |
| Q5806 | - | - | - | - |
| Base | 6.4 | 3.8 | 6.4 | 3.9 |
| Collector | 7.6 | 8.5 | 7.4 | 8.3 |
| Emitter | 5.7 | 3.2 | 5.7 | 3.2 |
| Q5803 | - | - | - | - |
| Base | 0.6 | 0.6 | 0.0 | 0.3 |
| Collector | 9.6 | 9.6 | 9.5 | 9.5 |

NOTE: The LLA voltages change with different control voltages. An example of LLA voltages with control voltage equal to 10.0 V and 6 V is shown.

If Q5803 draws no current under normal conditions, then check for short or open input cable, or for defective parts in the transmit injection filter or matching circuitry between Q5801 and Q5803. If all of the above check out OK, then replace Q5803.
2. Testing Pre-Driver Circuitry.

The pre-driver is a typical class-C stage, except the base is biased with resistors R5809 and R5806. The necessary conditions for proper operation of this stage are input drive power, and bias conditions as shown in Table 4-22, above.

NOTE: If it is necessary to replace Q5803, use a hot-air blower to remove and replace the part. It is important that the replacement device's case be properly soldered to its heatsink. Do so by flowing a small bead of solder around the rim of the device while it is clamped in the hot-air soldering device. The base and collector leads must be hand-soldered on the bottom side of the board.
3. Troubleshooting the Driver Stage

- Make sure A+ is at the collector.
- Check for shorts and/or opens in the matching circuitry. Also look for faulty components. (Cracked parts or parts not properly soldered).
- Measure the DC resistance from base to emitter. It should be less than 1 -ohm. If not, check L5851 and L5852 for proper soldering, and replace if faulty.
- Check the current drain of the driver. It should be around 1.5 to 2.0 A . for 40-Watt operation. If current drain is low, go to next step.
- Remove L5851 from the board and check the base-emitter and base-collector junction diode drops. Normal voltage drop should be between 0.4 and 1.0 V . If either junction reads outside this range, replace the driver device.

4. Troubleshooting the Final Device

- Make sure A+ is at the final's collector; if not, check for shorts and/or opens. If $A+$ is shorted, check C5877 and C5878 first for shorts, by lifting L5878 and measuring the resistance from collector to ground.
- Check the matching circuitry for shorts and/or opens. Also, check for faulty components. (Cracked parts or parts not properly soldered.)
- Measure the resistance from base to emitter; it should be less than 1 ohm. If not, check for proper soldering on L5875, L5876, and L5883; replace faulty component(s).
- Current drain on the final device should be $>5 \mathrm{~A}$. for $40-$ Watt operation. If low current, go on to the next step.
- Remove L5875 from the board and check the base-emitter and base-collector junction diode drops. Normal voltage drop should be between 0.4 and 1.0 V . If either junction is outside this range, replace the final device.
NOTE: The position of capacitors C5875, C5876, C5877, and C5878 is critical to the performance of the circuit. If they are removed for any reason, they must be re-installed as close to the cap of the final device as possible.
When replacing either the driver or final device, apply thermal compound on the heatsink surface. Torque the screws to the correct value; see the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20).

5. Testing the Antenna Switch and Harmonic Filter

Verify that most of this circuit is functioning properly by testing the receiver insertion loss as follows:

- Apply a low-level signal source at the antenna connector.
- Apply the conditions indicated in Table 4-19 for RX tests.
- Measure the power at the receive coax.
- If the difference between the input and output (insertion loss) is less than 1 dB , then the circuitry is functioning properly.

Additional antenna switch tests are:

- Check CR5920, CR5921, and CR5922 with an ohmmeter for forward and reverse continuity.
- In the transmit mode, adjust control voltage for 44 Watts at the antenna connector. Check for less than 10 mW at the end of the receive input cable. If power exceeds 10 mW , then check CR5922 and associated circuitry. Receiver sensitivity can degrade if power at this port exceeds 10 mW .
- Check for proper DC current through the PIN diodes; correct current is indicated if approximately 1.5 V is present at the junction of C5920 and L5920 during transmit mode.


## DO NOT measure bias directly at the PIN diodes while in transmit mode unless

 TX injection is removed.
## WARNING

### 4.5.2.2.5 Power Control and Protection Circuitry

1. Localizing Problems to a Circuit

Power leveling and current limiting are set to values detailed in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20). These values will vary from unit to unit, depending on the unique variations of each unit. If symptoms indicate that either of these circuits have failed, verify that the radio has been properly aligned before investigating the circuitry.
Temperature sense and control voltage limit are fixed by design and are not influenced by the alignment of the radio. If symptoms indicate that these circuits have failed, then troubleshoot the circuit.

The tests that follow are intended to provide a convenient means of verifying that a particular circuit is functioning properly. These tests will isolate the failure to a minimum number of components. Refer to the Theory of Operation and the schematic for information needed to identify the failed component(s).
2. Temperature Sense Circuit Test

Temporarily install a 6.8 k ohm resistor in parallel with RT5875. Key the transmitter and monitor the output power. The power meter should read approximately one-half the rated power ( 25 Watts).
3. Control-Voltage-Limit Circuitry Test

Disconnect J5901 (transmitter injection) from the internal transceiver chassis. This will require removal of the power amplifier assembly. With all other connections in normal condition, key the transmitter and monitor the control voltage at the node of R5811, C5814,L5808, and R5808. If the voltage exceeds 10.0 V , troubleshoot the control voltage limit circuitry.
4. Current-Limiting Circuitry Test

When ready to adjust current limit, decrease the relative current limit value with the keyboard per instructions. After several decrements, the current limit should begin to reduce power in 0 . 1 - to 0.5 -Watt increments. After this test, reset the current limit to its original value. If the circuitry does not perform as indicated, troubleshoot the current limit circuitry.
5. Power-Leveling Circuitry Test

With the radio connected for power measurements, vary the line voltage from 12.5 to 16 V . The power should not vary more than 2 Watts . At a line voltage of 13.6 V , vary the frequency using the three test modes. If power varies more than 2 Watts , measure the detected voltage on P0853, pin 9.1 this voltage varies more than 0.2 V over line and frequency variations, the power control circuitry (most of which is located on the command board) may be malfunctioning. If the detected voltage varies less than 0.2 V , the problem is likely in diode CR5900, the harmonic filter, the antenna switch, or the output coax. Check continuity through the 12-pin DC connector P0853 on the PA board; check digital/analog circuitry, and check $5-\mathrm{V}$ regulator operation. See Table 4-20 for typical values.
With the radio connected for power measurements and, disconnected TX injection coax, the detected voltage a P0853, pin 9 , should measure approximately 1.3 V .

NOTE: If any part of the power leveling circuitry is replaced, perform the power set procedure. See the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20) for details.

### 4.5.3 800 MHz Band

### 4.5.3.1 15 Watt and 35 Watt Power Amplifiers

This information will help you troubleshoot the Spectra radio. Use this information, along with the Theory of Operation, to diagnose and isolate the cause of failures. The principle tools needed to troubleshoot a circuit to the component level are the schematic and the Theory of Operation.

In addition to the schematic and theory, this section includes troubleshooting information that will help you test and check the circuits to localize and isolate problems.

Prior to troubleshooting, it is important to review the Theory of Operation, including specific precautions and troubleshooting methods. Because much of the radio's circuitry operates at 800 MHz , measurements must be taken very carefully. Notes and cautions are added to the text to alert the reader to this need in areas of greatest sensitivity. However the need to extreme care does exist in all measurements and tests at 800 MHz .

### 4.5.3.1.1 General Troubleshooting and Repair Notes

Most of the common transmitter symptoms are caused by either failure of the power amplifier or a failure in the control circuitry. The initial troubleshooting effort should be toward isolating the problem to one of those two areas. If either the control voltage or keyed 9.4 V are zero, then the problem is likely to be in the control circuit. If those voltages are present, then the problem is more likely in the power amplifier circuit.
If for diagnostic reasons, a chip component needs to be removed to facilitate testing, such as a series capacitor removed to allow for signal insertion, then the component(s) returned to the circuit should be new parts. The application of a soldering iron to many chip components will tend to cause leaching which could lead to failure.

If the harmonic filter is damaged and needs to be replaced, then removal and replacement requires the use of a hot air source capable of reflowing the solder beneath the filter hybrid. When replacing it, add small amounts of fresh solder paste to the silver regions beneath the ceramic to assure adequate electrical ground contact. Save the original input and output connectors ('J' straps); these are not included with the replacement kit. No turning is required. The harmonic filter may be ordered separately, but if the PA kit is ordered, a filter kit comes with the PA kit.
The pass device may be ordered separately or may be received as part of the hardware kit-it is not part of the PA kit. The PA kit comes with all surface-mount components, including the harmonic filter hybrid, but the harmonic filter cover is not included. Neither does the PA kit include the Power Module, nor, on 35-Watt models, the final device and associated matching capacitors.
After a PA board is replaced, or if any power control circuitry components are replaced, readjust the power according to instructions in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20).

NOTE: Due to the high frequency of operation, it is imperative that you use specified Motorola parts when component replacement is necessary. At these frequencies, second and third order properties of the components are very important and are part of the circuit's design. Substitute components may not work. It is also critical that you use great care when replacing parts. Excessive solder or flux, longer than original leads on coax connectors, misorientation of parts, and other commonly benign imperfections may cause the radio's performance to degrade.

### 4.5.3.1.2 PA Functional Testing

To test the PA assembly for proper operation, perform the following steps:
NOTE: The following instructions pertain to both the 15 Watt and 35 Watt power amplifiers. A distinction between the two PA's is given only where necessary.

1. Disassemble the PA assembly from the radio, leaving the power cable connected to the rear connector. Replace the $15-$ Watt PA shield (or the $35-$ Watt PA shield and cover). Disconnect the coax connectors and the ribbon cable. Connect a power meter to the antenna port using minimum cable length.
When setting or measuring RF power at 800 MHz , follow these guidelines to avoid measurement errors due to cable losses or non-50-ohm connector VSWR:

- All cables should be very short and have Teflon dielectric.
- Attenuators and 50 -ohm loads should have at least 25 dB return loss.
- Mini UHF to 'N' adapter P/N 5880367B21, should be used at the antenna connector. All other connectors should be ' N ' type. No other adapters, barrel connectors, etc. should be used.

Maximum input level to the PA is 200 mW . Over driving the buffer could result in damage to the PA buffer stage.
2. Apply the input power and DC voltages indicated in Table 4-23 to the power amplifier assembly. To make the DC connections, use small spring-clips or make a test adapter similar to that shown in Figure 4-10.


Figure 4-10. PA Test Adapter, 15 and 35 Watt Power Amplifier

Table 4-23. DC Voltages and Input Power Chart

| Test | Keyed 9.4 V | $\mathbf{9 . 6} \mathbf{~ V}$ | CONTROL <br> VOLTAGE <br> DRIVE | POWER IN <br> $(\mathbf{m W})$ | A+.V |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Transmit | 9.4 | 9.6 | See note $^{\mathrm{a}}$ | 0.1 | 13.0 |
| Receive | 0 | 9.6 | 0 | 0 | 13.0 |

a. Set initially to zero. Increase value until power equals 17 wafts(15-Watt radio) or 38 Watts (35-Watt radio) or 11.0 V maximum.
3. Apply the required input power via adapter cable 30-80373B27 or equivalent. For this application, non N -type connectors are acceptable.
4. With the applied control voltage initially at 0 V slowly increase the voltage until power out equals 17 Watts ( 15 -Watt radio) or 38 Watts (38-Watt radio) Power should rise smoothly with control voltage once the turn-on threshold is reached. Control voltage should no exceed 11.0 V .
5. If 11.0 V does not produce 17 (or 38 ) Watts, then a failure exists in the power amplifier circuit.
6. Refer to the voltage chart (see Table 4-24). Measure the indicated voltages. If they are not within the limits shown in the chart, then a failure exists in the PA assembly.
7. If the voltages in the chart are correct, verify that the injection is at least 75 mW . (See the VCO troubleshooting section.)
8. If no failure is located from the previous checks troubleshoot the power control circuitry.

Table 4-24. Power Control DC Voltage Chart

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :--- |
|  | LOW | TYP | HI | LOW | TYP | HI |  |
| P0853 |  |  |  |  |  |  |  |
| 1 | - | - | - | - | - | - | Key (no pin or wire) |
| 2 |  | 0 |  | 0 | 2.0 | 3.2 | Control Voltage Limit |
| 3 |  | 0 |  | 2.0 | 7.0 | 13.0 | Drive Voltage |
| 4 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | Current Sense + |
| 5 | 0 | 0 | 0 | 9.2 | 9.4 | 9.8 | Keyed 9.4 |
| 6 | 10.8 | 13.6 | 16.5 | 10.8 | 13.6 | 16.5 | A+ to Command Board |
| 7 |  | 0 |  |  | 1.2 |  | Temp Sense (cutback begins at 3.3 V) |
| 8 | - | - | - | - | - | - | Key (no pin) |
| 9 |  | 0 |  | 1.3 | 3.5 | 6.0 | Forward Detect Voltage |
| 10 | 10.8 | 13.8 | 16.5 | 10.8 | 13.6 | 16.5 | A+ to Command Board |
| 11 | 9.4 | 9.6 | 9.9 | 9.4 | 9.6 | 9.9 | $9.6-V$ Supply from Command Board |
| 12 | 20.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | Current Sense - (voltage delta 150 mV) |

Table 4-24. Power Control DC Voltage Chart (Continued)

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | LOW | TYP | HI | Low | TYP | HI |  |
| U0500 |  |  |  |  |  |  |  |
| 1 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 2 |  | 0 |  |  | 3.2 |  | Control AMP Input |
| 3 | 0 | 0 | 0 | 0 | 0 | 0 | Control AMP Input (not used) |
| 4 |  | 0 |  | 0 | 2 | 3.2 | Control Voltage Limit (cutback at 3.3 V ) |
| 5 |  | 0 |  |  | 0 |  | N.C. |
| 6 | 1.5 | 3.0 | 4.5 | 1.5 | 3.0 | 4.5 | Power Set from D-A (max power at 1.5 V ) |
| 7 |  | 0 | 0 | 1.5 | 3.0 | 4.5 | Power Set Buffer Out |
| 8 |  | 0 |  | 1.3 | 3.5 | 6.0 | Coupler Buffer Out |
| 9 |  | 0 |  | 1.3 | 3.5 | 6.0 | Forward Detect Voltage |
| 10 |  | 0 |  |  | 0 |  | Reflected Power Detect (not used) |
| 11 |  | 0 |  | 1.3 | 3.5 | 6.0 | Same as pin 8 (not used) |
| 12 |  | 0 |  | 0 | 1.2 | 6.0 | Thermister Buffer out (increases as PA gets hot) |
| 13 |  | 0 |  | 0 | 1.2 | 6.0 | Thermister Buffer in |
| 14 |  | 5.0 |  |  | 5.0 |  | $5-\mathrm{V}$ Sense Input (follows pin $20 \pm 0.1 \mathrm{~V}$ ) |
| 15 | 4.9 | 5.0 | 5.7 | 4.9 | 5.0 | 5.7 | $5-\mathrm{V}$ Current Limit (limits at 5.7 V ) |
| 16 | 5.0 | 5.7 | 6.4 | 5.0 | 5.7 | 6.4 | 5-V Series Pass Drive (6.4 at max current) |
| 17 | 9.5 | 9.6 | 9.9 | 9.5 | 9.6 | 9.9 | 9.6-V Sense Input |
| 18 |  | 7 |  |  | 7 |  | 5-V Reg. Compensation Capacitor |
| 19 |  | 5.7 |  |  | 5.7 |  | N.C. |
| 20 | 4.9 | 5.0 | 5.1 | 4.9 | 5.0 | 5.1 | 5-V Reference Input (UNSW5-V) |
| 21 |  | 1.2 |  |  | 1.2 |  | 9.6-V Reg. Compensation Capacitor |
| 22 |  | 0 |  |  | 0 |  | N.C. |
| 23 |  | 0.9 | 9.6 |  | 1.2 | 9.6 | 9.6V Series Pass Drive |
| 24 |  | 2.9 |  |  | 3.3 |  | Regulator Enable/Compensation |
| 25 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 26 |  | 0 |  |  | 0 |  | N.C. |
| 27 |  | 13.6 |  |  | 13.6 |  | N.C. |
| 28 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |

Table 4-24. Power Control DC Voltage Chart (Continued)

| LOCATION | RX MODE |  |  | TX MODE |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Low | TYP | HI | Low | TYP | HI |  |
| 29 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 30 | - | - | - | - | - | - | 9.6-V Programming (N.C.) |
| 31 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 32 | 10.8 | 13.6 | 16.5 | 10.0 | 13.0 | 16.0 | Decoupled A+ |
| 33 | 4.0 | 5.0 |  |  | 0 | 0.2 | TX PA Enable (from U520-25) |
| 34 |  | 0 |  |  | 1.3 |  | Control AMP one-shot |
| 35 |  | 0 |  |  | 0 |  | Lock (5-V of Synth Out of Lock) |
| 36 |  | 0 |  |  | 0.8 |  | Control AMP one-shot |
| 37 | 10.8 | 13.6 | 16.3 | 10.0 | 13.0 | 16.0 | A+ (Current Sense + ${ }^{\text {( }}$ |
| 38 | 10.8 | 13.6 | 16.3 | 10.0 | 13.0 | 16.0 | Current Sense - Voltage Delta 150 mV (35 Watt only) |
| 39 |  | 0 |  | 9.2 | 9.4 | 9.8 | Keyed 9.4-V in |
| 40 | 1.5 | 3.0 | 4.5 | 1.5 | 3.0 | 4.5 | Current Limit D-A (max current at 4.5 V ) |
| 41 | 0 | 0 | 0 | 0 | 0 | 0 | Ground |
| 42 |  | 0 |  |  | 2.2 | 9.6 | Control AMP Output (Approx 1/2-V Control) |
| 43 |  | 1.3 |  |  | 7.0 |  | Loop Integrator Capacitor |
| 44 |  | 2.1 |  |  | 3.2 |  | Control AMP Reference |
| Q0500E |  | 13.0 |  |  | 13.0 |  | A+ - CR0500 Drop |
| Q0501C |  | 12.3 |  |  | 12.3 |  | VQ0500E - B/E Drop |
| Q0501E |  | 0.2 |  |  | 0.2 |  | $V$ pin $23-\mathrm{B} / \mathrm{E}$ Drop |
| Q0503E |  | 0 |  |  | 1.5 |  | $V$ pin $42-\mathrm{B} / \mathrm{E}$ Drop (TX) |
| Q0503C |  | 13.6 |  |  | 9.0 |  |  |
| Q0504B |  | 13.6 |  |  | 12.9 |  | A + - B/E Drop (TX) |

NOTE: For antenna switch transmit bias conditions, RF drive must be removed from PA.

Table 4-25. Antenna Switch DC Voltage Chart

| LOCATION | TYPICAL RX | TYPICAL TX <br> NO PRE- <br> DRIVE | COMMENTS |  |
| :--- | :---: | :---: | :---: | :--- |
|  | ANODE | 0 | 1.6 | TX Series P.I.N. diode |
|  | CATHODE | 0 | 0.8 | (on in TX mode) |
| CR9921 | ANODE | 0 | 0.8 | TX Shunt P.I.N. diode |
|  | CATHODE | - | - | (on in TX mode) |
| CR9922 | ANODE | 5.15 V | $<0.2$ | RX Series P.I.N. diode |
|  | CATHODE | 4.45 V | 8.7 | (off in TX mode) |
| Q9920 | COLLEC | 5.15 V | $<0.2$ |  |

### 4.5.3.1.3 Localizing Problems

Failure locations often can be determined by externally measured symptoms. Basic symptoms are noted below with probable failure locations.

1. Low Power and High Current

- Check for improper load conditions caused by high VSWR external to the radio.
- Check output coax and mini-UHF connector.
- Check harmonic filter and J-straps.
- Check output impedance-matching circuitry from the final device to the harmonic filter.

2. Low Power and Low Current

- If control voltage is greater than 10 V , then check per the above.
- If control voltage is less than 10 V , then check the control circuitry.

3. Power Intermittently Low (or zero) and Current less than 1 A. when Power Drops

- Check Buffer Stage.

4. Power Zero and Current greater than 5 A.

- Check harmonic filter, antenna switch, and matching circuits beyond final stage.

5. Power Zero and Current between 2 and 5 A .

- Check Power Module.

6. Power Zero and Current less than 1 A.

- Check input coax.
- Check Buffer Stage.


### 4.5.3.1.4 Isolating Failures

Methods of analyzing individual stages of the Power Amplifiers are detailed below. Most of the stages are Class $C$ and must he analyzed under relatively high RF power levels. Generators capable of such levels may not be available in all service shops, therefore the tests below are arranged in order of increasing power. This tends to allow the preceding stage to be the source of RF power for testing the next stage. If adequate power sources are available, then any stage may be tested with external signal injection.

1. Testing Buffer Circuitry

The required DC and RF conditions are defined in Table 4-23. With no RF input applied, the collector voltage of Q9800 should be 9.4 V . If not, check L9805, L9801, and the feed runners. The base voltage should be $0.6-\mathrm{V}$ ( $0.7-\mathrm{V}$ without RF). If not, check R9801, CR9800, and related adaptive bias circuitry.

To check for power out, remove R9805 and lift the output end of C9807. Solder the center conductor of a small-diameter 50-ohm, coax to the vacated pad on the buffer side. Solder the coax's shield to ground. Under the conditions specified in Table 4-23, the measured power should be at least 350 mW . After output power has been tested, replace the resistor and capacitor with new parts.
An alternate method of testing the buffer's power out is to carefully lift the input lead of the power module ( pin 1) from the circuit board and replace it with the center conductor of a small-diameter coax. Solder the shield of the coax. Solder the shield of the coax to the adjacent ground pad.

To test the input VSWR of the circuit, apply 70 mW to the input. Using a directional coupler, verify that the reflected power is less than 20 mW .
2. Testing the Power Module (U9850)

The power module is a packaged gain block with 50-ohm input and output impedances. It has three gain stages, the first two of which have controlled voltage applied (for regulating power) and the final stage has A+ applied.

If the buffer stage has not been confirmed in "working order," an external 400 mW must be injected. Do this by carefully lifting pin 1 of the power module and soldering the center conductor of a small diameter coax to the pin. Solder the shield to the ground pad adjacent to pin 1. To this cable, inject 400 mW . (This application is not so critical to require an ' N ' connector on the loose end of the coax.)

If the buffer stage is confirmed in "working order," then provide 100 mW drive to the buffer (K9.4-V must be applied) to drive the module.
To measure the output power from the module, remove the series DC blocking capacitor C9879 (15W) or C9856 (35W), then connect a 39 pF blocking capacitor from the center conductor of a small diameter coax to the vacated pad, and finally, ground the shield of the output coax. Use this coax to measure output power.

Control voltage (Pins 2 and 3 ) should be 10 V ; $\mathrm{A}+($ pin 4 ) should be 13.0 V . Apply voltages through the DC connector on the PA board.

With either 100 mW applied to the buffer or 400 mW applied to the module input, the output power should be at least 15 Watts . If power out is less than 15 Watts , the module is defective and must be replaced.

NOTE: When replacing the module, apply thermal compound on the heatsink surface. Torque the screws to the correct value; see the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20).

When testing is complete, replace any capacitors or resistors that were removed for testing with new parts.
3. Testing the Final Stage (35-Watt Models Only)

The final stage is capable of producing over 50 Watts. Be sure to protect power measuring equipment with series attenuation. 30 dB is usually adequate.

15 Watts are needed to drive the final stage. Because this may exceed the power available at 800 MHz in many repair facilities, these tests consider the module stage as the drive source for the final stage. Therefore, check out the module first to ensure that it operates properly.

In the course of testing the final stage with the module as the power source, begin with control voltage at zero and increase control voltage smoothly until output of the final stage reaches 40 Watts. If control voltage reaches 10 V , but the power out does not reach 40 Watts, the final stage is defective. Under normal conditions, the protection circuitry limits the power to the final stage to approximately 17 Watts maximum, protecting it from overdrive and damage. Under test conditions, however, the protection circuitry is disabled. Observe the above caution; the power module can produce in excess of 25 Watts.
Measure the output power by lifting the output side of C9856 and connecting to the center conductor of a small-diameter coax which has its shield grounded. If the output stage does not produce 40 Watts (at 10-V control voltage), then remove the RF drive and perform the following tests:

- Check continuity from the collector lead to the A+ connector on the back of the radio.
- Examine the solder connections on all leads of the device (Q9880) and the clamped mica capacitors.

NOTE: The position of the clamped capacitors adjacent to the device is critical to the performance of the circuit. If they are removed for any reason, they must be re-installed with their leads approximately 70 mil s ( 0.070 inches) from the final device cap.
4. Testing the Antenna Switch and Harmonic Filter

Verify that most of this circuit is functioning properly by testing the receiver insertion loss as follows:

- Apply a low-level signal source at the antenna connector.
- Apply the conditions indicated in Table 4-23 for RX tests.
- Measure the power at the receive coax.
- If the difference between the input and output (insertion loss) is less than 2 dB , then the circuitry is functioning properly.

Additional antenna switch tests are:

- Check CR9922 with an ohmmeter for forward and reverse continuity.
- In the transmit mode, adjust control voltage for 38 Watts at the antenna connector. Check for less than 10 mW at the end of the receive input cable. If power exceeds 10 mW , then check CR9922 and associated circuitry. Receiver sensitivity can degrade if power at this port exceeds 10 mW .
- Check for proper DC current through the PIN diodes; correct current is indicated if approximately 1.5 V is present at the junction of C9920 and L9920 during transmit mode.


## DO NOT measure bias directly at the PIN diodes while in transmit mode unless TX injection is removed.

## WARNING

### 4.5.3.1.5 Power Control and Protection Circuitry

1. Localizing Problems to a Circuit

Power leveling and current limiting (35-Watt models only) are set to values detailed in the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20). These values will vary from unit to unit, depending on the unique variations of each unit. If symptoms indicate that either of these circuits have failed, verify that the radio has been properly aligned before investigating the circuitry.
Temperature sense, voltage control limit, and interstage drive limit (on $35-$ Watt models only) are fixed by design and are not influenced by the alignment of the radio. If symptoms indicate that these circuits have failed, then troubleshoot the circuit.

The tests that follow are intended to provide a convenient means of verifying that a particular circuit is functioning properly. These tests will isolate the failure to a minimum number of components. Refer to the Theory of Operation and the schematic for information needed to identify the failed component(s).
2. Temperature Sense Circuit Test

Temporarily install a 6.8 k ohm resistor in parallel with RT9650. Key the transmitter and monitor the output power. The power meter should read approximately $1 / 2$ the rated power (7.5 Watts or 17.5 Watts).
3. Control-Voltage-Limit Circuitry Test

Disconnect P9641 (Transmitter injection) from the internal transceiver chassis. This will require removal of the power amplifier assembly. With all other connections in normal condition, key the transmitter and monitor the control voltage on pin 2 of the power module. If the voltage exceeds 12.5 V , troubleshoot the control voltage limit circuitry.
4. Interstage Drive Limiter Circuitry Test ( $35-$ Watt models)

Check this circuit only when the final device Q9880) has failed. With the radio off, check CR9930 and associated components.
5. Current-Limiting Circuitry Test (35-Watt models)

When ready to adjust current limit, decrease the relative current limit value with the keyboard per instructions. After several decrements, the current limit should reduce power from 0.1 Watt to 0.5 Watt. After this test, reset the current limit. If the circuitry does not perform as indicated, troubleshoot the current limit circuitry.
6. Power-Leveling Circuitry Test

With the radio connected for power measurements, vary the line voltage from 12.5 to 16 V . The power should not vary more than 3 Watts. At a line voltage of 13.6 V , vary the frequency using the three test modes. If power varies more than 3 Watts, measure the detected voltage on P0853, pin 9 . If this voltage varies more than 0.2 V over line and frequency variations, the power control circuitry (most of which is located on the command board) may be malfunctioning. If the detected voltage varies less than 0.2 V , the problem is likely in CR9900, the harmonic filter, the antenna switch, or the output coax. Check continuity through 12 pin DC connector P0853 on the PA board; check digital/analog circuitry, and check 5-V regulator operation. See Table 4-24 for typical values.

NOTE: If any part of the power leveling circuitry is replaced, perform the power set procedure. See the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20) for details.

## This Page Intentionally Left Blank

## Chapter 5 Troubleshooting Charts

### 5.1 Introduction

This chapter contains detailed troubleshooting flowcharts. These charts should be used as a guide in determining the problem areas. They are not a substitute for knowledge of circuit operation and astute troubleshooting techniques. It is advisable to refer to the related detailed circuit descriptions in the theory section prior to troubleshooting a radio.

### 5.2 List of Troubleshooting Charts

Most troubleshooting charts (see Table 5-1) end up by pointing to an IC to replace. It is not always noted, but is good practice, to verify supplies and grounds to the affected IC, and trace continuity to the malfunctioning signal and related circuitry before replacing any IC. For instance, if a clock signal is not available at a destination IC, continuity from the source IC should be checked before replacing the source IC.

Table 5-1. List of Troubleshooting Charts

| Chart <br> Number |  | Page <br> Number |
| :--- | :--- | :--- |
| Chart C.1 | RF Board Back-End | $5-3$ |
| Chart C.2 | Command Board | $5-4$ |
| Chart C.3 | Radio Power-Up Fail | $5-5$ |
| Chart C.4 | Bootstrap Fail | $5-6$ |
| Chart C.5 | $01 / 90$, General Hardware Failure | $5-7$ |
| Chart C.6 | $01 / 81$, Host ROM Checksum Failure | $5-7$ |
| Chart C.7 | $01 / 82$, or 002, External EEPROM Checksum Failure | $5-8$ |
| Chart C.8 | $01 / 84$, SLIC Initialization Failure | $5-8$ |
| Chart C.9 | $01 / 88$, MCU (Host $\mu$ C) External SRAM Failure | $5-9$ |
| Chart C.10 | $01 / 92$, Internal EEPROM Checksum Failure | $5-9$ |
| Chart C.11 | $02 / A 0$, ADSIC Checksum Failure | $5-10$ |
| Chart C.12 | $02 / 81$, DSP ROM Checksum Failure | $5-10$ |
| Chart C.13 | $02 / 88$, DSP External SRAM Failure U414 | $5-11$ |
| Chart C.14 | $02 / 84$, DSP External SRAM Failure U403 | $5-11$ |
| Chart C.15 | $02 / 82$, DSP External SRAM Failure U402 | $5-12$ |
| Chart C.16 | $02 / 90$, General DSP Hardware Failure | $5-12$ |
| Chart C.17 | $09 / 10$, Secure Hardware Failure | 5 |
| Chart C.18 | $09 / 90$, Secure Hardware Failure | 5 |

Table 5-1. List of Troubleshooting Charts (Continued)

| Chart <br> Number | Description | Page <br> Number |
| :--- | :--- | :--- |
| Chart C.19 | No RX Audio | $5-14$ |
| Chart C.20 | No TX Modulation | $5-15$ |
| Chart C.21 | Key Load Fail | $5-16$ |
| Chart C.22 | 800 MHz Receiver Front-End Hybrid | $5-17$ |
| Chart C.23 | UHF Receiver Front-End Hybrid | $5-17$ |
| Chart C.24 | VHF Receiver Front-End Hybrid | $5-18$ |
| Chart C.25 | ASTRO Spectra Plus VOCON Power-Up Failure | $5-19$ |
| Chart C.26 | ASTRO Spectra Plus VOCON DC Supply Failure | $5-20$ |
| Chart C.27 | ASTRO Spectra Plus VOCON TX Modulation Failure Sheet 1 of 4 | $5-21$ |
| Chart C.28 | ASTRO Spectra Plus VOCON TX Modulation Failure Sheet 2 of 4 | $5-22$ |
| Chart C.29 | ASTRO Spectra Plus VOCON TX Modulation Failure Sheet 3 of 4 | $5-23$ |
| Chart C.30 | ASTRO Spectra Plus VOCON TX Modulation Failure Sheet 4 of 4 | $5-24$ |
| Chart C.31 | ASTRO Spectra Plus VOCON RX Audio Failure | $5-24$ |
| Chart C.32 | ASTRO Spectra Plus VOCON Secure Hardware Failure | $5-25$ |
| Chart C.33 | ASTRO Spectra Plus VOCON Key Load Fail | $5-26$ |

NOTE: The term $\mu \mathrm{C}$ is used in several of the following troubleshooting charts; $\mu \mathrm{C}=\mathrm{MCU}$.


Chart C. 1 RF Board Back-End




Chart C. 4 Bootstrap Fail


Chart C. 6 01/81, Host ROM Checksum Failure


Chart C. 7 01/82 or 002, External EEPROM Checksum Failure



Chart C. 9 01/88, MCU (Host mC) External SRAM Failure


Chart C. 11 02/AO, ADSIC Checksum Failure


Chart C. 13 02/88, DSP External SRAM Failure U414


Chart C. 15 02/82, DSP External SRAM Failure U402


Chart C. 17 09/10, Secure Hardware Failure
 his failure relates only to
secure equipped radios and secure equipped radios and
indicates a power up self-test failure for the secure module. More specifically this failure indicates a failure in
communications between the
DSP and secure module. The DSP and secure module. The
secure module is not considere secure module is no
field repairable so
troubleshooting is limited to verifiying a problem with the
module and replacing. Thpical module and replacing. Typ
failure modes would be: falure modes would be:

1) Open between secure module
and vocon board at $J 801$ and vocon board at J 001 . 2) The DSP communicates with
the secure module via the SCI/SSI bus (Reeer to Fig. S1). A failure of this bus. 3) Failure to get proper supplies and grounds to J801.


Chart C. 18 09/90, Secure Hardware Failure


Chart C. 19 No RX Audio



Chart C. 21 Key Load Fail


Chart C. 22800 MHz Receiver Front-End Hybrid


Chart C. 24 VHF Receiver Front-End Hybrid


Chart C. 25 ASTRO Spectra Plus VOCON Power-Up Failure


Chart C. 26 ASTRO Spectra Plus VOCON DC Supply Failure



Chart C. 28 ASTRO Spectra Plus VOCON TX Modulation Failure Sheet 2 of 4


Chart C. 29 ASTRO Spectra Plus VOCON TX Modulation Failure Sheet 3 of 4


Chart C. 30 ASTRO Spectra Plus VOCON TX Modulation Failure Sheet 4 of 4



Chart C. 32 ASTRO Spectra Plus VOCON Secure Hardware Failure


## Chapter 6 Troubleshooting Waveforms

### 6.1 Introduction

This chapter contains images of waveforms that might be useful in verifying operation of certain parts of the circuitry. These waveforms are for reference only; the actual data depicted will vary depending upon the operating conditions.

### 6.2 ASTRO Spectra Waveforms

Waveform W1: Power-On Reset Timing


MAEPF-25187-O

## Waveform W2: DSP SSI Port RX Mode



Note 1: Typically SCKR is a 2.4 MHz clock. In low power modes, as shown here, SCKR is 600 KHz .

## Waveform W3: DSP SSI Port TX Mode CSQ



## Waveform W4: ABACUS Programming at Mode Change



## Waveform W5: ABACUS/ADSIC Interface



Note 2: Since these signals are a differential current loop these voltages are very low.

## Waveform W6: SPI Bus Programming ADSIC



W6: SPI Bus Programming ADSIC. MAEPF-24381-O
Trace 1 - ADSIC SEL*
Trace 2 -SPI_SCK
Trace 3 -MOSI
Note: These waveforms are typical to any device on the SPI bus.

## Waveform W7: Receive Audio



Note 3: Actual level is dependent upon volume setting.

## Waveform W8: Transmit Audio



## Waveform W9: Power-Down Reset



Waveform W10: ADSIC 2.4 MHz Reference


W10 ADSIC 2.4 MHz Reference
Trace 1-IDC @ U406
MAEPF-24385-O

### 6.3 ASTRO Spectra Digital Plus VOCON Board Waveforms

This section contains images of waveforms specific to the ASTRO Spectra Digital Plus VOCON board. These waveforms might be useful in verifying operation of certain parts of the circuitry. These waveforms are for reference only; the actual data depicted will vary depending upon the operating conditions.

## 32 kHz Clock Waveform



Trace 1 - R428 - 32 kHz Clock

### 16.8 MHz Clock Waveform



Trace 1 - TP401 - 16.8 MHz Clock

## TX Modulation Out Waveform



Transmitting
1 kHz tone at 85 mVrms into microphone
Trace 1 - U201 - 9

## Differential ADDAG Output Waveform



Transmitting
1 kHz tone at 85 mV rms into microphone
Trace 1 - U201 - 4
Trace 2 - U201 - 5

## TX SSI Waveform



Transmitting
1 kHz tone at 85 mVrms into microphone
Trace 1 - U201 - 33 - Data
Trace 2 - U201 - 35 - Frame Sync
Trace 3-U201 - 34 - Clock

## SPI Bus Waveform



Radio Power Up
Trace 1 - U201 - 41 - Data
Trace 2 - U201 - 43 - Chip Select
Trace 3 - U201 - 42 - Clock

## TX 1 kHz Tone Waveform



Transmitting
1 kHz tone at 85 mVrms into microphone
Trace 1 - U402-17

## Serial Audio Port Waveform



Transmitting
1 kHz tone at 85 mV rms into microphone
Trace 1 - U402-7-Frame Sync
Trace 2 - U402 - 11 - Clock
Trace 3-U402-13 - Data

## RX Audio Waveform



Receiving
1 kHz tone at 3 kHz Dev, -47 dBm
Trace 1 - U402-2

## RX BBP Waveform



Receiving
1 kHz tone at 3 kHz Dev, -47 dBm
Trace 1 - TP221 - Frame Sync
Trace 2 - TP223 - Data
Trace 3 - TP219 - Clock

## Secure Interface Waveform



Receiving
1 kHz tone at 3 kHz Dev, -47 dBm Secure Mode
Trace 1 -P1- 8 - Data
Trace 2 - P1 - 10 - SS
Trace 3 - P1-9-Clock

## 8 kHz Frame Sync for Security Circuitry Waveform



Receiving
1 kHz tone at 3 kHz Dev, -47dBm Secure Mode
Trace 1 - U601 - 5

## This Page Intentionally Left Blank

## Chapter 7 Schematics, Component Location Diagrams, and Parts Lists

| The following table lists the schematics and diagrams contained in this chapter. |  | Figure Title <br> HLN6458E VOCON Board Schematic (Sheet 2 of 2) | Page | Figure Title <br> HLD4342B and HLD4343B VHF VCO Carrier Schematic Diagram | Page | Figure Title | Page |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Figure Title | Page |  | 7-33 | HLD4342B and HLD4343B VHF VCO Carrier Schematic Diagram | 7-64 | HLD6022C VHF 50 Watt PA Schematic <br> HLD6022C VHF 50-Watt PA Component Location Diagram, Side 1 | 7-97 |
| ASTRO Spectra Radio Interconnection | Page | HLN6458E VOCON Board Component Location Diagrams (Sheet 1 of 2) | 7-34 | HLD4342D and HLD4343D VHF VCO Carrier Schematic Diagram | 7-65 |  | 7-98 |
| HRN4009B/HRN6014A VHF RF Board; HRN4010B/ HRN6020A UHF RF Board; and HRN6019A 800 MHz RF Board Schematic | 7-5 | HLN6458E VOCON Board Component Location Diagrams (Sheet 2 of 2) | 7-35 | HLD4342B/HLD4343B VHF VCO Carrier Component Location Diagram | 7-66 | HLD6022C VHF 50-Watt PA Component Location Diagram, Side 2 | 7-99 |
|  |  | HLN6458F/G VOCON Board Schematic (Sheet 1 of 2) | 7-38 |  |  | HLD6064C VHF 100-Watt PA Schematic | 7-101 |
| HRN4009B/HRN6014A VHF RF Board, HRN4010B/ HRN6020A UHF RF Board, and HRN6019A 800 | 7-6 |  |  | HLD4342D/HLD4343D VHF VCO Carrier Component Location Diagram | 7-67 | HLD6064C VHF 100-Watt PA Component Location Diagram, Side 1 | 7-102 |
| MHz RF Board Component Location Diagrams |  | HLN6458F/G VOCON Board Schematic (Sheet 2 of 2) | 7-39 | UHF VCO Ranges 1, 2, 3, and 4 Hybrid Schematic | 7-70 | HLD6064C VHF 100-Watt PA Component Location Diagram, Side 2 | 7-103 |
| HRN4009C/HRN6014C VHF RF Board Schematic Diagram | 7-9 | HLN6458F/G VOCON Board Component Location Diagrams (Sheet 1 of 2) | 7-40 | HLE6101A UHF VCO Range 1 Hybrid and HLE6102A Range 2 Hybrid Component Location Diagram | 7-71 |  |  |
| HRN4009C/HRN6014C VHF RF Board Component | 7-10 | HLN6458F/G VOCON Board Component Location Diagrams (Sheet 2 of 2) | 7-41 |  |  | , | -105 |
| Location Diagrams |  |  |  | HLE6103B UHF VCO Range 3 Hybrid and HLE6104B Range 4 Hybrid Component Location Diagram | 7-73 | HLD6032B/HLD6066B VHF 25-Watt PA Component Location Diagram, Side 1 | 7-106 |
| HRN4009E and HRN6014D VHF RF Board; HRN4010D and HRN6020C UHF RF Board; and | 7-12 | HLN6458H VOCON Board Schematic (Sheet 1 of 2) | 7-44 |  |  | HLD6032B/HLD6066B VHF 25-Watt PA Component Location Diagram, Side 2 | 7-107 |
| HRN6019C 800 MHz RF Board Schematic Diagra |  | HLN6458H VOCON Board Schematic (Sheet 2 of 2) | 7-45 | UHF VCO Ranges 1, 2, 3, and 4 Schematic Diagram | 7-75 |  |  |
| (Sheet 1 of 2) | 7-13 | HLN6458H VOCON Board Component Location Diagrams | 7-46 | HLE6045B Range 1 and HLE6046B Range 2 UHF VCO Component Location Diagram | 7-76 | HLE6062B and HLE6071B UHF 25-Watt PA Schematic | 7-110 |
| HRN4010D and HRN6020C UHF RF Board; and HRN6019C 800 MHz RF Board Schematic Diagram (Sheet 2 of 2) |  | ASTRO Spectra Plus Top Level Schematic (Sheet 1 of 2) | 7-49 | HLE6000D Range 3 and HLE6041D Range 4 UHF VCO Component Location Diagrams | 7-77 | HLE6062B UHF 25-Watt PA Component Location Diagram, Side 1 | 7-111 |
| HRN4009E and HRN6014D VHF RF Board; | 7-14 | ASTRO Spectra Plus Top Level Schematic (Sheet 2 of 2) | 7-50 | HLF6080B $800 \mathrm{MHz} \mathrm{VCO} \mathrm{Schematic} \mathrm{Diagram} \quad 7-79$ |  | HLE6062B UHF 25-Watt PA Component Location Diagram, Side 2 | 7-112 |
| HRN4010D and HRN6019C 800 MHz RF Component Location Diagram |  | ASTRO Spectra Plus RF Interface Schematic (Sheet 1 of 2) | 7-51 | HLF6080B 800 MHz VCO Component Location Diagram | 7-80 | HLE6043C, HLE6044C, and HLE6049B UHF 40Watt PA Schematic | 7-114 |
| HLN5558E/F/G, HLN6529C/D/E/F/G, HLN6560C/D/ E/F/G/H and HLN6562C/D/E/F/G/H Command Board Schematic Diagram | 7-17 | ASTRO Spectra Plus RF Interface Schematic (Sheet 2 of 2) | 7-52 | HRD6001E/6002E/6011E/6012E VHF Receiver Front-End Schematic | 7-82 | HLE6043C, HLE6044C, and HLE6049B UHF 40Watt PA Component Location Diagram, Side 1 | 7-115 |
|  |  | ASTRO Spectra Plus Digital/USB Schematic (Sheet 1 of 2) | 7-53 | HRD6001E/6002E/6011E/6012E VHF Component Location Diagram | 7-83 | HLE6043C, HLE6044C, and HLE6049B UHF 40Watt PA Component Location Diagram, Side 2 | 7-116 |
| G/H, and HLN6562C/D/E/F/G/H Command Board Component Location Diagrams |  | ASTRO Spectra Plus Digital/USB Schematic (Sheet 2 of 2) | 7-54 | HRD6001G/6002G/6011G/6012G VHF Receiver Front-End Schematic | 7-87 | HLE6039C, HLE6040C, and HLE6051C UHF 100Watt PA Schematic | 7-120 |
| HLN5558H/J, HLN6529H, HLN6560J and HLN6562J Command Board Schematic Diagram | 7-21 | ASTRO Spectra Plus Audio/DC Schematic | 7-55 | HRD6001G/6002G/6011G/6012G VHF Receiver Front-End Component Location Diagram | 7-88 | HLE6039C, HLE6040C, and HLE6051C UHF 100Watt PA Component Location Diagram, Side 1 | 7-121 |
| HLN5558H/J, HLN6529H, HLN6560J and HLN6562J Component Location Diagram | 7-22 | ASTRO Spectra Plus Voltage Conversion Schematic ASTRO Spectra Plus Secure Interface Schematic | 7-56 | HRE6001B/6002C/6003B/6004B/6011B/6012B/ 6014B UHF Receiver Front-End Preamp and Standard Schematics | 7-90 | HLE6039C, HLE6040C, and HLE6051C UHF 100Watt PA Component Location Diagram, Side 2 | 7-122 |
| HLN6458D VOCON Board Schematic (Sheet 1 of 2) | 7-26 | ASTRO Spectra Plus VOCON Component Location Diagram, Top View | 7-58 | HRE6001B/6002C/6003B/6004B/6011B/6012B/ 6014B UHF Receiver Front-End Hybrid Component Location Diagram | 7-91 | HLF6078B 800 MHz 15-Watt PA Schematic | 7-127 |
| HLN6458D VOCON Board Schematic (Sheet 2 of 2) | 7-27 | ASTRO Spectra Plus VOCON Component Location Diagram, Bottom View | 7-59 |  |  | HLF6078B 800 MHz 15-Watt PA Component Location Diagram, Side 1 | 7-128 |
| Diagrams (Sheet 1 of 2) | 7-29 | HLD6061D and HLD6062D VHF VCO Hybrid Schematic <br> HLD6061D and HLD6062D VHF VCO Hybrid Component Location Diagram | 7-62 | HRF6004B/C 800 MHz Receiver Front-End Schematic Diagram | 7-94 | HLF6078B 800 MHz 15-Watt PA Component Location Diagram, Side 2 | 7-129 |
| Diagrams (Sheet 2 of 2) |  |  | 7-63 | HRF6004B/C 800 MHz Receiver Front-End Component Location Diagram | 7-95 | HLF6077D 800 MHz 35-Watt PA Schematic | 7-131 |
| HLN6458E VOCON Board Schematic (Sheet 1 of 2) | 7-32 |  |  |  |  |  |  |


| Figure Title | Page |
| :--- | :---: |
| HLF6077D 800 MHz 35-Watt PA Component <br> Location Diagram, Side 1 | $7-132$ |
| HLF6077D 800 MHz 35-Watt PA Component <br> Location Diagram, Side 2 | $7-133$ |

### 7.1 RF Section

Table 7-1: J500 Command Board to RF Board

| J500 <br> Pin\# | Description | To <br> RF Board |
| :--- | :--- | :--- |
| 1 | SW +5V | J500B-1 |
| 2 | REF TUNE | J500B-2 |
| 3 | DOUT* | J500B-3 |
| 4 | DOUT | J500B-4 |
| 5 | SBI | J500B-5 |
| 6 | N/C | J500B-6 |
| 7 | ODC | J500B-7 |
| 8 | GND | J500B-8 |
| 9 | GND | J500B-9 |
| 10 | SW +9.6V | J500B-10 |
| 11 | MOD OUT | J500B-11 |
| 12 | A+ | J500B-12 |
| 13 | GND | J500B-13 |
| 14 | LOCK DET* | J500B-14 |
| 15 | KEYED +9.4V | J500B-15 |
| 16 | ROSC/PSC_CE* | J500B-16 |
| 17 | REF MOD | J500B-17 |
| 18 | MOSI | J500B-18 |
| 19 | SCLK | J500B-19 |
| 20 | SYN SEL* | J500B-20 |
| 21 | N/C | J500B-21 |
| 22 | GND | J500B-22 |
| 23 | GND | J500B-23 |
| 24 | GND | J500B-24 |
|  |  |  |

Table 7-2: P502 Command Board to Control Head Interconnect Board

| P502 <br> Pin \# | Description | To <br> Intcon Board |
| :--- | :--- | :--- |
| 1 | SW +9.6 V | $\mathrm{~N} / \mathrm{C}$ |

Table 7-2: P502 Command Board to Control Head Interconnect Board

| P502 Pin \# | Description | To Intcon Board |
| :---: | :---: | :---: |
| 2 | PTT*/P RESET | N/C |
| 3 | EMERGENCY | N/C |
| 4 | SPARE 2 | N/C |
| 5 | DISC AUDIO 2 | N/C |
| 6 | N/C | J103-27 |
| 7 | GND | J103-3 |
| 8 | VRS RX AUDIO | N/C |
| 9 | BUSY | J103-25 |
| 10 | GND | N/C |
| 11 | LH RESET | J103-23 |
| 12 | VRS TX AUDIO | N/C |
| 13 | DISC AUDIO | N/C |
| 14 | VIP OUT 1 | J103-8 |
| 15 | P RS DATA | J103-21 |
| 16 | BOOT/VPP | J103-20 |
| 17 | Vo 2 | J103-19 |
| 18 | VO 1 | J103-18 |
| 19 | RX/RS232 | N/C |
| 20 | TX/RS232 | N/C |
| 21 | IGN SW B+ | J103-11 |
| 22 | BUS- | J103-10 |
| 23 | KEYFAIL*TX | J103-9 |
| 24 | PTTB/VIP IN 1 | N/C |
| 25 | BUS+ | J103-22 |
| 26 | CTS/RS232 | N/C |
| 27 | VIP OUT 2 | J103-7 |
| 28 | GND | J103-6 |
| 29 | HUB/BUSY | N/C |
| 30 | A+ | J103-5 |
| 31 | SW B+ | J103-4 |
| 32 | KEYED SLOT | - |
| 33 | RTS/RS232 | N/C |
| 34 | SPEAKER LOW | N/C |
| 35 | SPEAKER HIGH | N/C |
| 36 | UNSW +5V | N/C |
| 37 | SW +5V | N/C |

Table 7-2: P502 Command Board to Control Head Interconnect Board

| P502 <br> Pin \# | Description | To <br> Intcon Board |
| :--- | :--- | :--- |
| 38 | VIP IN 2 | N/C |

Table 7-3: P503 Command Board to RF Power Amplifier Board

| P503 <br> Pin \# | Description | To <br> RF PA <br> Board |
| :--- | :--- | :--- |
| 1 | CONT VOLT LIMIT | P853-2 |
| 2 | CONT VOLT DRIVE | P853-3 |
| 3 | CURRENT SENSE + | P853-4 |
| 4 | KEYED +9.4V | P853-5 |
| 5 | A+ | P853-6 |
| 6 | TEMP SENSE | P853-7 |
| 7 | KEYED SLOT | P853-8 |
| 8 | FWD DET VOLT | P853-9 |
| 9 | A+ | P853-10 |
| 10 | SW +9.6V | P853-11 |
| 11 | CURRENT SENSE- | P853-12 |
| 12 | GND | J2-8 |
| 13 | MIC HI/RESET/ <br> RTS RS232 | J2-15 |
| 14 | SPEAKER LOW | J2-7 |
| 15 | BUS+ | J2-14 |
| 16 | SPEAKER HIGH | J2-6 |
| 17 | PTT*/TX RS232 | J2-13 |
| 18 | IGN/SW B+ | J2-5 |
| 19 | V_OUT 1 | J2-12 |
| 20 | SW B+ | J2-4 |
| 21 | DISC AUD/RX RS232 | J2-11 |
| 22 | V_OUT 2 | J2-3 |
| 23 | BUS- | J2-10 |
| 24 | EMERGENCY | J2-2 |
| 25 | BUSY | J2-9 |
|  |  |  |

Table 7-4: Configuring Command Board Jumpers for Non RS232 Operation

| Jumper | Function |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :---: |
|  | MIC <br> HI | PTT | DISC <br> AUD | VRS <br> TX |  |
| JU518 |  | OUT |  |  |  |
| JU519 |  |  | OUT |  |  |
| JU520 |  |  |  | OUT |  |
| JU521 | OUT |  |  |  |  |
| JU513 | IN |  |  |  |  |
| JU500 |  | IN |  |  |  |
| JU528 |  |  | IN |  |  |
| JU515 |  |  |  | IN |  |

Table 7-5: J501 Command Board to VOCON Board

| $\begin{array}{\|c\|c\|} \hline J 501 \\ \text { Pin } \end{array}$ | Description | Command Board | VOCON Board |
| :---: | :---: | :---: | :---: |
| 1 | DOUT* | J500-3 | U406-H3 |
| 2 | DOUT | J500-4 | U406-K3 |
| 3 | GROUND | P503-12 | GROUND |
| 4 | GROUND | P503-12 | GROUND |
| 5 | CTSOUT* | U526-13 | CTSOUT* |
| 6 | SBI | J500-5 | U406-J3 |
| 7 | DOC | J500-7 | U406-F3 |
| 8 | SPI SCK | J500-19 | U204-G5 |
| 9 | MOSI | J500-18 | U204-J6 |
| 10 | LOCK DET* | J500-14 | U206-K2 |
| 11 | SYN SEL* | J500-20 | U204-H7 |
| 12 | ROSC/PSC CE* | J500-16 | U204-F6 |
| 13 | DA SEL* | U502-25 | U204-H5 |
| 14 | TXPA EN* | U500-33 | U206-G9 |
| 15 | 5 V EN* | CR401 | U206-F8 |
| 16 | EMERG | Q507 | U204-A3 |
| 17 | BOOT DATA IN | U525-14 | $\begin{aligned} & \hline \text { U206-J21 } \\ & \text { U204-J7 } \end{aligned}$ |
| 18 | BOOT DATA OUT | U522-19 | U204-G6 |
| 19 | BUSY OUT | U522-13 | U206-K8 |
| 20 | SB9600 BUSY | U522-11 | U204-J3 |
| 21 | KEYFAIL* | U525-1 | $\begin{aligned} & \text { U206-J8/J801- } \\ & 15 \end{aligned}$ |
| 22 | VIP OUT1 | P502-14 | U206-E7 |
| 23 | VIP OUT2 | P502-27 | U206-J3 |
| 24 | VIP IN1 | Q512 | U206-A7 |
| 25 | VIP IN2 | A513 | U206-D5 |
| 26 | MOB IRQ* | U522-24 | U206-B4 |
| 27 | LHRST0* | U522-8 | U206-B7 |
| 28 | LHRST1 | Q511 | U206-A8 |
| 29 | POR* | U522-40 | $\begin{aligned} & \text { U206-C9/ } \\ & \text { U409-2 } \end{aligned}$ |
| 30 | INT PTT* | U522-38 | U206-H2 |
| 31 | $\begin{aligned} & \text { OPTB+/BOOT SEL/ } \\ & \text { VPP } \end{aligned}$ | P502-16 | CR201 |
| 32 | TAMPER | GND | J801-6 |
| 33 | +5V | Q502 | +5V |

Table 7-5: J501 Command Board to VOCON Board

| J501 <br> Pin <br> $\#$ | Description | Command <br> Board | vocon <br> Board |
| :--- | :--- | :--- | :--- |
| 34 | +5V | Q502 | +5V |
| 35 | B+ SENSE | P502-31 | B+ SENSE |
| 36 | KG BACKUP | JU527 | J801-23 |
| 37 | +5VA | U500-14 | +5VA |
| 38 | UNSW B+ | Q508 | UNSW B+ |
| 39 | MAI | U524 | U406-C1 |
| 40 | SDO | U524-1 | U406-B5 |
| 41 | LATCH SEL* | U530-12 | U206-G8 |
| 42 | RTSIN* | U526-14 | U206-J8 |
| 43 | RS232 DATA OUT | U526-15 | U206-A5 |
| 44 | SPKREN | Q401 | U206-G4 |
| 45 | MICEN | Q404 | U206-G7 |
| 46 | GROUND | P503-12 | GROUND |
| 47 | GROUND DIGITAL | P503-12 | GRD DIGITAL |
| 48 | REFMOD | U401-5 | U406-B1 |
| 49 | MODIN | U401-3/ | U406-B2 |
| 50 | RS232 DATA IN | U5530 |  |
|  |  |  | U206-B2 |

Table 7-6: J801 VOCON Board to Encryption Board

| J801 <br> Pin \# | Description | To Encryption <br> Board |
| :--- | :--- | :--- |
| 1 | SW B+ | J401-17 |
| 2 | SW B+ | J401-17 |
| 3 | EMC RXO | U405-B7 |
| 4 | EMC TXO | U405-A7 |
| 5 | N/C |  |
| 6 | GROUND |  |
| 7 | MISO | U204-H6 |
| 8 | MOSI | U204-J6 <br> J601-1 |
| 9 | SPI SCK | U204-G5 J601-2 |
| 10 | EMC EN | U206-D6 |
| 11 | EMC REQ* | U206-H3 |
| 12 | EMC MAKEUP* | U206-K7 |
| 13 | N/C |  |

Table 7-6: J801 VOCON Board to Encryption Board

| J801 <br> Pin \# | Description | To Encryption <br> Board |
| :--- | :--- | :--- |
| 14 | N/C |  |
| 15 | KEYFALL*RTSIN |  |
| 16 | N/C | U206-J8 |
| 17 | N/C |  |
| 18 | N/C |  |
| 19 | N/C |  |
| 20 | UNSW B + | J401-19 |
| 21 | GROUND |  |
| 22 | GROUND |  |
| 23 | N/C |  |
| 24 | N/C |  |
| 25 | N/C |  |

Table 7-7: J601 RF Board to VCO Board

| J601 <br> Pin \# | Description | To <br> vco Board |
| :--- | :--- | :--- |
| 1 | SYNTH-FDBK | P601-1 |
| 2 | +9.6V | P601-2 |
| 3 | POS-S.L. | P601-3 |
| 4 | NEG-S.L. | P601-4 |
| 5 | KEYED-9.4V | P601-5 |
| 6 | DATA | P601-6 |
| 7 | CLOCK | P601-7 |
| 8 | SEL | P601-8 |
| 9 | AUX-2 | P601-9 |
| 10 | VCO-MOD | P601-10 |
| 11 | AUX-1 | P601-11 |
| 12 | S.F.8.6V | P601-12 |
|  |  |  |




HRN4009B/HRN6014A VHF RF Board; HRN4010B/HRN6020A UHF RF Board; and HRN6019A 800 MHz RF Board Schematic


HRN4009B/HRN6014A VHF RF Board, HRN4010B/HRN6020A UHF RF Board, and HRN6019A 800 MHz RF Board Component Location Diagrams

## HRN4009B, HRN6014A VHF RF Board HRN4010B, HRN6020A UHF RF Boar HRN6019A 800 MHz RF Board

## Electrical Parts Lis

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
|  |  | CAPACITOR, Fixed: unless otherwise stated |
| C300 | 2113740865 | 470pF |
| C301, 302 | 2113741N45 | . 014 F |
| С303 |  | Not Placed |
| C304 | 2311049J26 | 10pF |
| С305 | 2113741841 | 6800pF |
| С306 | 2113740869 | 680pF |
| C307 | 2113741N45 | . 01 pF |
| C308 | 2311049 J 26 | 10pF |
| С309 | 2113741N57 | .033 F |
| C310 | 2113741N69 | 0.14F |
| C311 | 2311049,26 | 10pF |
| C312, 313 |  | Not Placed |
| C314 | 2113741N69 | 0.14F |
| C315 | 2113740873 | 1000pF |
| C316 | 2113743 G 21 | 1.04F (VHF Bd) |
|  | or 2311049A37 | 1.04F (UHF and 800MHz Bds) |
| C317 | 2311049J26 | 10pF |
| C318 | 2113740831 | 18pF |
| C319 | 2113741N45 | . 014 F |
| C320 | 2113741N61 | .047 F |
| C321 | 2113741N21 | 1000pF |
| C322 | 2113741N69 | 0.14F (Placed on UHF Bd Only) |
| C323 | ---- | Not Placed |
| C324 | 2113741N69 | 0.14F |
| C325, 326 | 2113741N45 | . 01 uF |
| C327 | 2311049J26 | 10pF |
| С328 | ---- | Not Placed |
| C329 | 2113741N69 | 0.14F |
| С330 | 2113740849 | 100pF |
| C332 thru 334 | 2113740849 | 100pF |
| С336, 337 | 2113740849 | 100pF |
| Сз39 | 2113740849 | 100pF |
| C341 | 2113740849 | 100pF (Placed on VHF Bd Only) |
| C343 | 2113740827 | 12pF |
| C345 | 2113741N69 | 0.14F |
| C350 | 2113741 N 45 | .014F |
| C351 | 2113741N69 | 0.14F (Placed on UHF Bd Only) |
| C352 | 2113741N45 | . 01 HF |
| C353 | $2113740 \mathrm{B15}$ | 3.9pF (VHF Bd) |
|  | or 2113740B13 | 3.3pF (UHF Bd) |
|  | or 2113740B19 | 5.6pF ( 800 MHz Bd ) |
| C354 | 2113741N69 | 0.14F |
| C355 | 2113741N69 | 0.1 1 F (VHF and 800MHz Bds) |
|  | or 2311049, 04 | 2.2 2 F (UHF Bd) |
| C357 | 2113740817 | 4.7pF (VHF Bd)) |


| ITEM | $\begin{array}{c}\text { MOTOROLA } \\ \text { PART }\end{array}$ | DESCRIIPTIOR |
| :--- | :---: | :--- |$]$


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R316 | 0611077A94 | 6.8k |
| R317 | 0611077A43 | 51 |
| R318 | 0611077A01 | 0 |
| R319 | ----- | Not Placed |
| R320 | 0611077A26 | 10 |
| R325 | ----- | Not Placed |
| R350 | 0611077A58 | 220 (VHF and 800MHz Bds) |
|  | or 0611077A58 | 120 (UHF Bd) |
| R351 | 0611077A26 | 10 (VHF and 800MHz Bds) |
|  | or 0611077A36 | 27 (UHF Bd) |
| R353 | 0611077A78 | 1.5k |
| R354 | 0611077A28 | 12 |
| R355 | 0611077A01 | 0 (VHF and 800MHz Bds) |
|  | or 0611077A98 | 10k (UHF Bd) |
| R356 | 0611077A38 | 33 (VHF and $800 \mathrm{MHz} \mathrm{Bds)}$ |
|  | or 0611077A52 | 120 (UHF Bd) |
| R357 | 0611077A98 | 10k |
| R358 | 0611077A10 | 2.2 (Placed on VHF and 800 MHz Bds Only) |
| R602 | 0611077A43 | 51 |
| R607 | 0611077A74 | 1k |
| R608 | 0611077A98 | 10k |
| R609 | 0611077A62 | 330 |
| R610 | 0611077A43 | 51 |
| R611 | 0611077B31 | 220k |
| R612 | 0611077 B 31 | 220 k (Placed on UHF Bd only) |
| R613 | $0611077 B 11$ | 33k |
| R614 | 0611077A90 | 4.7k |
| R615 | $0611077 B 47$ | 1M |
| R616 | 0611077 B07 | 22k (VHF Bd) |
|  | or $0611077 \mathrm{B0} 1$ | 12k (UHF Bd) |
|  | or 0611077 A98 | 10 k (800MHz Bd) |
| R617 | 0611077B11 | 33k |
| R618 | 0611077A28 | 12 |
| R620 | 0611077A74 | 1k |
| R622 | 0611077A74 | 1k |
| R623 | 0611077A26 | 10 |
| R624 | 0611077A98 | 10k (VHF Bd) |
|  | or 0611077A90 | 4.7k (UHF and $800 \mathrm{MHz} \mathrm{Bds)}$ |
| R625 | 0611077 B07 | 22 k (VHF Bd) |
|  | or 0611077A96 | 8.2k (UHF Bd) |
|  | or 0611077A78 | 1.5 k (800MHz Bd) |
| R626 | 0611077B23 | 100k |
| R627 | 0611077A90 | 4.7 k (VHF Bd) |
|  | or 0611077A82 | 2.2 k (UHF Bd) |
|  | or 0611077A80 | 1.8k (800MHz Bd) |
| R628 thru 630 | 0611077A78 | 1.5 k |
| R631 | 0611077B47 | 1M |
| R633 | 0611077B21 | 82k |
| R634 | 1880273N01 | Potentiometer |
| R635 | $0611077 B 23$ | 100k |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R637 | 0611077831 | 220k (VHF and UHF Bds) |
|  | or 0611077847 | 19 (800MHz Bd) |
| R638, 639 | 0611077A98 | 10k |
| R640 | 0611077 B 31 | 220k (VHF and UHF Bds) |
|  | or $0611077 \mathrm{B47}$ | 1M (800MHz Bd) |
| R641 | 0611077A43 | 51 |
| R642 | 0611077A98 | 10k |
| R645 | $0611077 B 47$ | 1M |
| R646, 647 | 0611077A98 | 10k |
| R648 | ----- | Not Placed |
| R653 | $0611077 B 11$ | 33k |
|  |  | INTEGRATED CIRCUIT MODULE: See Note 1. |
| U301 | 5105835490 | Hybrid, ABACUS ( 800 MHz Bd ) |
| U302 | 5105469E90 | 5 V Regulator |
| U600 | 5180291809 | Reference Oscillator; 16.8 MHz (VHF and UHF Bds) |
|  | or 5180291811 | Reference Oscillator; 16.8 MHz (800MHz Bd) |
| U601 | 5180057S09 | Prescaler |
| $\mathrm{U602}^{\text {Y350 }}$ | 5180057 S10 | Synthesizer |
|  |  | CRYSTAL: See Note 1. |
|  | 9180084M01 | 109.65 MHz |
| Y350 |  | miscellaneous: |
|  | 2605256x01 | Shield, Filter |
|  | 7580094M06 | Pad, VF Shock |
|  | 8405884V02 | Circuit Board |

Notes:
For optimum performance, order replaceement diodes, transistors, and circuit

3. Part value notations:
$\mathrm{p}=10^{-12}$
$\mathrm{n}=10^{-9}$
$\mathrm{n}=10^{-9}$
$\mu=10^{-6}$
$\mathrm{H}=10^{-6}$
$\mathrm{~m}=10^{-3}$
$\mathrm{m}=10^{3}$
$\mathrm{k}=10^{3}$
$\mathrm{n}=10^{6}$
4. TTEM refers to the component reference designator.
5.
The RF Board $k$ kits use a

-     - layer printed circuit board.


- mwer Lavers



HRN4009C/HRN6014C VHF RF Board Component Location Diagrams

| ITEM | MOTOROLA PARTNUMBER | description |
| :---: | :---: | :---: |
|  |  | CAPACITOR，Fixed： unless otherwise stated |
| C300 | 2113740B65 | 470pF |
| C301， 302 | 2113741N45 | ．014F |
| С303 | －－－－－ | Not Placed |
| C304 | 2311049J26 | 10pF |
| C305 | 2113741841 | 6800pF |
| C306 | 2113740869 | 680pF |
| C307 | 2113741 N45 | ． $01 \mu \mathrm{~F}$ |
| C308 | 2311049J26 | 10pF |
| C309 | 2113741N57 | ．033 F |
| C310 | 2113741N69 | 0．1 1 F |
| C311 | 2311049， 26 | 10pF |
| C312 |  | Not Placed |
| C313 | 2113740865 | 470pF |
| C314 | 2113741N69 | 0．1 1 F |
| C315 | 2113740873 | 1000pF |
| C316 | 2113743 G 21 | 1．0ヶF |
| C317 | 2311049J26 | 10pF |
| C318 | 2113740 B 31 | 18pF |
| C319 | 2113741N45 | ． 01 ¢ F |
| C320 | 2113744N61 | ．047 F |
| C321 | 2113741N21 | 1000pF |
| C322， 323 | －－－－－ | Not Placed |
| C324 | 2113741N69 | 0．1年 |
| C325， 326 | 2113741N45 | ． $01 \mu \mathrm{~F}$ |
| C327 | 2311049J26 | 10pF |
| C329 | 2113741N69 | 0．1年 |
| С330 | $2113740 B 49$ | 100pF |
| C332 thru 334 | 2113740849 | 100pF |
| C336， 337 | 2113740849 | 100pF |
| С339 | 2113740849 | 100pF |
| C341 | 2113740849 | 100pF |
| C343 | 2113740827 | 12pF |
| C345 | 2113741N69 | 0．1 1 F |
| C350 | 2113741N45 | ．014F |
| C351 | －－．－－ | Not Placed |
| C352 | 2113741N45 | ．014F |
| C353 | 2113740815 | 3．9pF |
| C354， 355 | 2113741N69 | 0．1瑗 |
| C357 | 2113740817 | 4．7pF |
| C358 | 2113741N69 | $0.1 \mu \mathrm{~F}$ |
| C600 | $2113740 B 49$ | 100pF |
| C601 | 2113740B34 | 24 pF |
| ${ }^{6} 604$ | 2113744 N 21 | 1000pF |
| C605 | 2380090M06 | 10pF |
| C606， 607 | 2311049， 26 | 10pF |
| C608 | 2380090M06 | 10pF |
| C609 | 2113741N69 |  |
| C610 | 2113741N45 | ． $01 \mu \mathrm{~F}$ |
| C611 | －－－－－ | Not Placed |
| C612 | 2113741N69 | $0.1 \mu \mathrm{~F}$ |
| C613 | 2113741N45 | ． 01 HF |
| C614 | 2113740 B31 | 18pF |
| C615 | 2113740B39 | 39pF |
| C616 | 2113741N45 | ． 01 ¢ F |
| C617 thru 619 | 2113741N69 | 0．1年 |
| C620 | 2113741N45 | ． $01 \mu \mathrm{~F}$ |
| C621 | 2113740825 | 10pF |
| C622 | 2113741N21 | 1000pF |


| ITEM | MOTOROLA | DESCRIPTION |
| :---: | :---: | :---: |
| C623 | 0880027809 | 100pF |
| C624 | 2113741N45 | ． 01 ¢F |
| C625 | 2113741 28 | 0．22 $\mu \mathrm{F}$ |
| C626 | $2113740 \mathrm{B76}$ | 1500pF |
| C627 thru 629 | 2113741 N45 | ．01 ${ }^{\text {F }}$ |
| C630 | 211374 0B73 | 1000pF |
| C631 | 2311049，07 | 3．3pF |
| C632 | 2380090906 | 10pF |
| C633 | 2311049J26 | 10pF |
| C634 | 2113740876 | 1500pF |
| C635 | 2380090M06 | 10pF |
| C636 | 2380090M07 | 47pF |
| C637 | 2113741N69 | 0．1年 |
| C638 | 2113741N45 | ．01 HF |
| C639 | 2113741N69 | 0．1年 |
| C641 | 2113740801 | 1．0pF |
| C642 | 2113741N69 | 0．1瑗 |
| C644 | 2113741N69 | 0．14F |
| C645 | 2113740849 | 100pF |
| C646 | 2113743G21 | 1．0pF |
| C647 | 2113741N69 | $0.1 \mu \mathrm{~F}$ |
| C648 thru 650 |  | Not Placed |
| C651 | 2113740849 | 100pF |
| C652 | －－－－－ | Not Placed |
| C653 | 2113740849 | 100pF |
| C654 | 2113740876 | 1500pF |
| C656 | 2113741N69 | 0．1出 |
| C657 | 2113740827 | 12pF |
| C658 | 2113741N45 | ． 01 ¢F |
| C659 | 2113740827 | 12 pF |
| C660 | 2113741N69 | 0．14F |
| C664 | 2113741N45 | ． 014 F |
| C665 | 2113740827 | 12pF |
| C670 | 2113740879 | 2000pF <br> DIODE：See Note 1. |
| CR301 | 4880006E09 | Varactor |
| CR350 | －－－－ | Not Placed |
| CR351 | 4882958R39 | 3.3 V |
| CR352 | 4880154K03 | Dual；Shotky |
| CR601 | 4880006E09 | Varactor |
| CR602， 603 | 4811058 B 11 |  |
| CR605 | －－ | Not Placed |
| CR606 | 4813830A28 | CORE： |
| E301 | 2680003м03 | Ferrite Bead |
| E302 | 2680004M03 | Ferrite Bead |
| E602 | 2680006M02 | Ferrite Bead |
| FL300， 301 | 9185128 U 02 | FILTER：See Note 2. LC； 450 kHz |
|  |  | JACK： |
| J350 | 0980110M01 | Receptacle，Coaxial |
| J500 | 0980104M02 | Receptacle，Bottom－Entry |
| J601 | 0980103N02 | Sockets，12－Position |
| J602 |  | Not Placed JUMPER： |
| JU300， 301 | 0611077 A01 | OW |
| JU351 | 0611077A01 | ow |
| JU352 |  | Not Placed |
| JU601 | 0611077A01 | ow |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| JJ604 thru 606 |  | Not Placed |
| JU607 | 0611077A01 | ow |
| JU608 |  | Not Placed |
| JU609 | 0611077A01 | ow |
| JU611 |  | Not Placed |
| JU612 | 0611077 A01 | ow |
| JU613 thru 615 | －－－－－ | Not Placed |
| JU617 | 0611077A01 |  |
| JU618 thru 620 |  | Not Placed |
|  |  | COIL，RF： <br> unless otherwise stated |
| L302 |  | Not Placed |
| L303 | 2411087A32 | 3.3 HH |
| L304 | 2480140 E 16 | 10 HH |
| L351 | 2480140 E 11 | 360 nH |
| L352， 353 | 2405452 C 78 | 295nH |
| L354 | 2480140 E16 | 10 HH |
| L600 | 2480140E06 | 130 nH |
| L603 |  | Not Placed |
| L604 | 2480140 E01 | $1.2 \mu \mathrm{H}$ |
| L605 |  | Not Placed |
| L608 | 2480140 E16 | 10 HH |
| L610， 611 | 2480140E01 | $1.2 \mu \mathrm{H}$ <br> TRANSISTOR：See Note 1. |
| Q350 | 4802000P02 | NPN |
| Q351 thru 353 | － | Not Placed |
| Q602， 603 | 4805128M27 | SOT89 |
| Q606 | 4884235R02 | SOT |
| Q607 | 4880182D48 | PNP |
| Q608 | 4880141 L 01 | PNP；SOT23 |
| Q609， 610 | 4880141L02 | NPN；SOT23 |
| Q670 | 4805218N11 | SOT |
|  |  | RESISTOR：Ohms $\pm 5 \%$ ； 1／8W unless otherwise stated |
| R300 | 0611077A98 | 10k |
| R301 | 0611077A19 | 5.1 |
| R302 | 0611077A36 | 27 |
| R303 | 0611077 B33 | 270k |
| R307， 308 | 0611077A46 | 68 |
| R311，R312 | 0611077A01 |  |
| R313 | 0611077A38 | ${ }^{33}$ |
| R314 | 0611077B09 | 27 k |
| R315 | 0611077A98 | 10k |
| R316 | 0611077A94 | 6．8k |
| R317 | 0611077A43 | 51 |
| R318 | 0611077A01 | 0 |
| R319 | －－．．－ | Not Placed |
| R320 | 0611077A26 | 10 |
| R325 | －－－－－ | Not Placed |
| R350 | 0611077A52 | 120 |
| R351 | 0611077A26 | 10 |
| R353 | 0611077A78 | 1．5k |
| R354 | 0611077A28 | 12 |
| R355 | 0611077A01 |  |
| R356 | 0611077A38 | 33 |
| R357 | 0611077A98 | 10k |
| R358 | 0611077A10 | 2.2 |
| R602 | 0611077A43 | 51 |
| R607 | 0611077A74 | 1 k |
| R608 | 0611077A98 | 10k |
| R609 | 0611077A62 0611077A43 | 330 51 |


| ITEM | MOTOROLA PARTNUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R611 | 0611077831 | 220k |
| R612 |  | Not Placed |
| R613 | 0611077811 | 33k |
| R614 | 0611077A90 | 4．7k |
| R615 | 0611077847 | 1M |
| R616 | 0611077807 | 22k |
| R617 | 0611077811 | 33k |
| R618 | 0611077A28 | 12 |
| R620 | 0611077 A74 | 1 k |
| R622 | 0611077 A74 | 1k |
| R623 | 0611077A26 | 10 |
| R624 | 0611077A98 | 10k |
| R625 | $0611077 B 07$ | 22k |
| R626 | $0611077 B 23$ | 100k |
| R627 | 0611077A90 | 4．7k |
| R628 thru 630 | 0611077A78 | 1．5k |
| R631 | 0611077847 | 1M |
| R633 | $0611077 \mathrm{B21}$ | 82k |
| R634 | 1880273N01 | Potentiometer |
| R635 | $0611077 B 23$ | 100k |
| R637 | 0611077831 | 220k |
| R638， 639 | 0611077A98 | 10k |
| R640 | 0611077 B 11 | 220k |
| R641 | 0611077A43 | 51 |
| R642 | 0611077A98 | 10k |
| R645 | $0611077 B 47$ | 1M |
| R646， 647 | 0611077A98 | 10k |
| R653 | $0611077 \mathrm{B11}$ | 33k |
| R670 | 0611077A90 | 4．7k <br> INTEGRATED CIRCUIT MODULE：See Note 1 |
| U301 | 5105835 G 9 | Hybrid，ABACUS |
| U302 | 5105469 E 90 | 5 V Regulator |
| U600 | 5180291809 | Reference Oscillator |
| U601 | 5180057S09 | Prescaler |
| U602 | 5180057 S10 | Synthesizer |
| Y350 | 9180084M01 | CRYSTAL：See Note 2 109．65MHz |
|  |  | MISCELLANEOUS： |
|  | 2605256X01 | Shield，Filter |
|  | 7580094M06 8485865B01 | Pad，VF Shock Circuit Board |

2． $\begin{aligned} & \text { For optimum performance，order replacement diodes，transistors，and circuir } \\ & \text { When orderinala part }\end{aligned}$
2．When ordering crystas，specity carrier
3．type number，and Motorola part number．
4． $\begin{aligned} & \mathrm{n}=10^{-9} \\ & \mathrm{H}=10^{-6} \\ & \mathrm{ITEM}\end{aligned}$
$\mathrm{m}=10^{-3}$
$\mathrm{k}=10^{3}$
$\mathrm{M}=10^{6}$
$\begin{array}{ll}\text { 4．} & \mathrm{M}=10^{6} \\ \text { 5．} & \text { The refers to the component reference designator．} \\ \text { Roard Kits use } 6 \text {－} 6 \text {－ayer printed dircuit board }\end{array}$



HRN4009E and HRN6014D VHF RF Board; HRN4010D and HRN6020C UHF RF Board; and HRN6019C 800 MHz RF Board Schematic Diagram (Sheet 1 of 2)


HRN4009E and HRN6014D VHF RF Board; HRN4010D and HRN6020C UHF RF Board; and HRN6019C 800 MHz RF Board Schematic Diagram (Sheet 2 of 2)




## HRN4009E and HRN6014D VHF RF Board, HRN6010D and HRN6020C UHF RF Board, and HRN6019C 800 MHz RF Board Parts Lists

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
|  |  | CAPACITOR, Fixed: unless otherwise stated |
| C300 | 2113740B65 | 470pF |
| C301, 302 | 2113741N45 | . 01 MF |
| C303 |  | Not Placed |
| C304 | 2311049J26 | 10pF |
| С305 | 2113741841 | 6800pF |
| C306 | 2113740B69 | 680pF |
| C307 | 2113741N45 | . 014 F |
| C308 | 2311049J26 | 10pF |
| C309 | 2113741N57 | .033 F |
| C310 | 2113741N69 | 0.14F |
| C311 | 2311049J26 | 10pF |
| C312, 313 |  | Not Placed |
| C314 | 2113741N69 | 0.14F |
| C315 | 2113740873 | 1000pF |
| C316 | 2311049A37 | 1.04F |
| C317 | 2311049J26 | 10pF |
| C318 | 2113740 B 31 | 18pF |
| C319 | 2113741 N45 | . 014 F |
| C320 | 2113741N61 | .047 ${ }^{\text {F }}$ |
| C321 | 2113741 N 21 | 1000pF |
| С322, 323 | 2113741 N69 | 0.14F (Placed on UHF Bd Only) |
| C324 | 2113741 N69 | 0.14 F |
| С325, 326 | 2113741 N45 | . 014 F |
| C327 | 2311049J26 | 10pF |
| С329 | 2113741 N69 | 0.14F |
| С330 | $2113740 B 49$ | 100pF |
| Сз32, 333 | 2113740849 | 100pF |
| C334 | 2113740849 | 100pF/Not Placed (see Note 6) |
| Сз36, 337 | 2113740B49 | 100pF |
| С339 | 2113740849 | 100pF |
| C341 | 2113740849 | 100pF (Placed on VHF Bd Only) |
| C343 | $2113740 \mathrm{B27}$ | 12 pF |
| C345 | 2113741 N69 | 0.14F |
| C350 | $2113740 \mathrm{B09}$ | 2.2pF |
| C351 | 2113741 N69 | 0.14F (Placed on UHF Bd Only) |
| C352 | 2113741N69 | 0.14F |
| C353 | 2113740 B 13 | 3.3pF |
| C354, 355 | 2113741 N69 | 0.14F |
| C357 | 213740813 | 3.3pF |
| C358 | 2113741 N69 | 0.14 F |
| C359 | 2113740B19 | 5.6pF |
| C360 | 2113741N69 | 0.14F |
| C361, 362 |  | Not Placed |
| С363 | 2113740807 | 1.8pF (Placed on VHF and 800MHz Bds Only) |
| C364 | 2113740811 | 2.7pF (VHF Bd) |
|  | or $2113740 \mathrm{B19}$ | 5.6pF (UHF and 800MHz Bds) |
| C365, 366 | 2113741 N 69 | 0.14 F |
| C600 | 2113740849 | 100pF |
| C601 | 2113740B34 | 24pF |
| C604 | 2113741N21 | 1000pF |
| C605 | 2380090м06 | 10pF |
| C606, 607 | 2311049J26 | 10pF |
| C608 | 2380090м06 | 10pF |
| C609 | 2113741N69 | 0.14F |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| E303 | 2605261 V 01 | Ferrite Bead (Placed on 800MHz Bd Only) |
| E602 | 2680006M02 | Ferrite Bead |
|  |  | FILTER: See Note 2. |
| FL300, 301 | 9185128 U 02 | LC; 450kHz |
| FL350, 351 | 4885230C01 | 109.65 MHz (VHF Bd) |
|  | or 4805736Y03 | Crystal; 109.65MHz (UHF and 800MHz Bds) |
|  |  | JACK: |
| J350 | 0980110M01 | Receptacle, Coaxial |
| J500 | 0980104M02 | Receptacle, Bottom-Entry |
| J601 | 0980103N02 | Sockets, 12-Position |
| J602 |  | Not Placed |
|  |  | JUMPER: |
| JU300, 301 | 0611077A01 | $0 \Omega$ |
| JU351 | 0611077 A01 | $0 \Omega$ (Placed on VHF and 800 MHz Bds Only) |
| JU352 | 0611077 A01 | $0 \Omega$ (Placed on UHF Bd Only) |
| JU601 | 0611077 A01 | $0 \Omega$ |
| JU604 |  | Not Placed |
| JU605 | 0611077 A01 | $0 \Omega$ (Placed on UHF and 800 MHz Bds Only) |
| JU606 | 0611077A01 | $0 \Omega$ (Placed on UHF Bd Only) |
| JU607 | 0611077 A01 | $0 \Omega$ (Placed on VHF and 800 MHz Bds Only) |
| JU608 | 0611077 A01 | $0 \Omega$ (Placed on UHF Bd Only) |
| JU609 | 0611077 A01 | $0 \Omega$ (Placed on VHF and 800 MHz Bds Only) |
| JU611 |  | Not Placed |
| JU612 | 0611077 A01 | $0 \Omega$ (Placed on VHF Bd Only) |
| JU613 | 0611077 A01 | $0 \Omega$ (Placed on UHF Bd Only) |
| JU614 | 0611077 A01 | $0 \Omega$ (Placed on $800 \mathrm{MHz} \mathrm{Bd} \mathrm{Only)}$ |
| JU615 | ----- | Not Placed |
| JU617 | 0611077 A01 | $0 \Omega$ (Placed on VHF Bd Only) |
| JU618 | 0611077 A01 | $0 \Omega$ (Placed on UHF Bd Only) |
| JU619 | 0611077A01 | $0 \Omega$ (Placed on 800 MHz Bd Only) |
| JU620 | ----- | Not Placed |
|  |  | COIL, RF: unless otherwise stated |
| L302 | --..- | Not Placed |
| L303 | 2411087A32 | $3.3 \mu \mathrm{H}$ |
| L304 | 2480140E16 | 10 HH |
| L350 | 2405423A12 | 470nH |
| L351 | 2405423 A 11 | 390 nH (VHF and UHF Bds) |
| L352 | Or 2405452C90 | 4000H (800MHz Bd) |
| L353 | 2405423A11 | 390 nH (VHF Bd) |
|  | or 2405423A12 | 470nH (UHF and 800MHz Bds) |
| L354 | 2480140 E 16 | 10 HH |
| L600 | 2480140E06 | 130 nH |
| L603 | ----- | Not Placed |
| L604 | 2480140E01 | 1.2 2 H |
| L605 |  | Not Placed |
| L608 | 2480140E16 | 10山H |
| L610, 611 | 2480140E01 | $1.2 \mu \mathrm{H}$ |
| Q350 | 4802000P02 | TRANSISTOR: See Note 1. NPN |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| Q351 | 4880052M01 | NPN; Darlington (Placed on UHF Bd Only) |
| Q352 | 4884235R02 | SOT (Placed on UHF Bd Only) |
| Q353 | 4880048M01 | NPN (Placed on UHF Bd Only) |
| Q354 | 4802000P02 | NPN |
| Q602, 603 | 4805128M27 | SOT89 |
| Q606 | 4884235R02 | SOT |
| Q607 | 4880182D48 | PNP |
| Q608 | 4880141L01 | PNP; SOT23 |
| Q609, 610 | 4880141L02 | NPN; SOT23 |
| Q670 | 4805218N11 | SOT (Placed on VHF Bd Only) |
|  |  | RESISTOR: Ohms $\pm 5 \%$; 1/8W unless otherwise stated |
| R300 | 0611077A98 | 10k |
| R301 | 0611077A19 | 5.1 |
| R302 | 0611077A36 | 27 |
| R303 | 0611077 B 33 | 270k |
| R307, 308 | 0611077A46 | 68 |
| R311 | 0611077 A01 | 0 |
| R312 | 0611077A01 | 0 (Placed on UHF and 800 MHz Bds Only) |
| R313 | 0611077A38 | 33 (VHF Bd) |
|  | or 0611077A74 | 1 k (UHF and $800 \mathrm{MHz} \mathrm{Bds)}$ |
| R314 | $0611077 \mathrm{B09}$ | 27k |
| R315 | 0611077 A98 | 10k |
| R316 | 0611077 A94 | 6.8 k |
| R317 | 0611077A43 | 51 |
| R318 | 0611077A01 | 0 |
| R319 |  | Not Placed |
| R320 | 0611077A26 | 10 |
| R325 | --.-- | Not Placed |
| R350 | 0611077A58 | 220 |
| R351 | 0611077A36 | 27 |
| R353 | 0611077A78 | 1.5k |
| R354 | 0611077A28 | 12 |
| R355 | 0611077 A 01 | 0 (VHF and 800MHz Bds) |
|  | or 0611077A98 | 10k (UHF Bd) |
| R356 | 0611077A58 | 220 |
| R357 | 0611077A98 | 10 k (Placed on VHF Bd Only) |
| R358 | 0611077A10 | 2.2 (Placed on VHF and 800 MHz Bds Only) |
| R360 | 0611077A58 | 220 (VHF Bd) |
|  | or 0611077A52 | 120 (UHF and 800MHz Bds) |
| R361 | 0611077 A32 | 18 (VHF Bd) |
|  | or 0611077A36 | 27 (UHF and 800MHz Bds) |
| R363 | 0611077 A78 | 1.5 k |
| R364 | 0611077A28 | 12 |
| R366 | 0611077A36 | 27 |
| R602 | 0611077A43 | 51 |
| R607 | 0611077 A74 | 1k |
| R608 | 0611077A98 | 10k |
| R609 | 0611077A62 | 330 |
| R610 | 0611077A43 | 51 |
| R611 | 0611077 B 31 | 220k |
| R612 | $0611077 \mathrm{B31}$ | 220k (Placed on UHF Bd only) |
| R613 | 0611077811 | 33k |
| R614 | 0611077A90 | 4.7k |
| R615 | 0611077847 | 1M |
| R616 | 0611077807 | 22 k (VHF Bd) |
| R617 | or 0611077A98 0611077B11 | 10 k (UHF and 800 MHz Bds) 33k |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R618 | 0611077A28 | 12 |
| R620 | 0611077 A74 | 1k |
| R622 | 0611077 A74 | 1k |
| R623 | 0611077 A26 | 10 |
| R624 | 0611077 A98 | 10k (VHF Bd) |
|  | or 0611077A90 | 4.7k (UHF and 800MHz Bds) |
| R625 | $0611077 \mathrm{B01}$ | 12k (VHF Bd) |
|  | or 0611077A78 | 1.5k (UHF and 800MHz Bds) |
| R626 | 0611077 B 23 | 100k |
| R627 | 0611077 A94 | 6.8 k (VHF Bd) |
|  | or 0611077A80 | 1.8 (UHF and 800MHz Bds |
| R628 thru 630 | 0611077 A78 | 1.5k |
| R631 | 0611077847 | 1 M |
| R633 | $0611077 \mathrm{B21}$ | 82k |
| R634 | 1880273N01 | Potentiometer |
| R635 | 0611077823 | 100k |
| R637 | 0611077847 | 1M |
| R638, 639 | 0611077 A98 | 10k |
| R640 | $0611077 \mathrm{B47}$ | 1M (Placed on VHF and 800 MHz Bds Only) |
| R641 | 0611077 A43 | 51 |
| R642 | 0611077 A98 | 10k |
| R645 | 0611077 B47 | 1M |
| R646, 647 | 0611077 A98 | 10k |
| R653 | 0611077811 | 33 k |
| R670 | 0611077A90 | 4.7k (Placed on VHF Bd Only) |
|  |  | : |
|  | 0680149M02 | THERMISTOR: |
| RT612 |  | 100k (Placed on UHF and 800MHz Bds Only) |
|  |  | INTEGRATED CIRCUIT MODULE: See Note 1. |
| U301 | 5105835 Y 90 | Hybrid, ABACUS (VHF and 800MHz Bds) |
| U302 | 5105469 E90 | 5 V Regulator |
| U600 | 5180291811 | Reference Oscillator; 16.8MHz |
| U601 | 5180057509 | Prescaler |
| U602 | 5180057S10 | Synthesizer (Placed on VHF and 800MHz Bds Only) |
|  |  | diode: |
|  | 4882958R39 | Zener, 3.3 V |
| Y350 |  | CRYSTAL: See Note 2. |
|  | 9180084M01 | 109.65 MHz |
|  |  | miscellaneous: |
|  | 2605256X01 | Shield, Filter |
|  | 7580094M06 | Pad, VF Shock |
|  | 8485219C02 | Circuit Board |

Notes:
1.
modules by Motorola part number only.
3. Pype number, and Motorola part numbe
$\mathrm{p}=10^{-12}$ $\mathrm{m}=10^{-3}$
$\mathrm{k}=10^{3}$
$\mathrm{p}=10^{-9}$
$=10.6$ $\mathrm{k}=10^{3}$
$\mathrm{M}=10^{6}$
4. ITEM refers to the component reference designator.
5.



6. C334 is Not Place | capacitor. |
| :--- |
| HRN 1009 .G |



HRNGO20D-F
HRN6019D-F.

### 7.2 Command Board Section



HLN5558E/F/G, HLN6529C/D/E/F/G, HLN6560C/D/E/F/G/H and HLN6562C/D/E/F/G/H Command Board Schematic Diagram


HLN5558E/F/G, HLN6529C/D/E, HLN6560C/D/E/F/G/H, and HLN6562C/D/E/F/G/H Command Board Component Location Diagrams

Command Boards Reference Designator and Part
Number Comparison Char
The following table lists those reference designators
common to the various command board kits and the part
numbers assigned to those designators for each kit and
version.

| Ref Des | HLN5558G <br> Part Number | HLN6529G <br> Part Number | HLN6560H <br> Part Number | HLN6562H <br> Part Number |
| :--- | :--- | :--- | :--- | :--- |
| C512 | $2311049 A 40$ | $2311049 A 40$ | $2311049 A 54$ | 2311049 A40 |
| C562 | 2113740 B65 | $2113740 B 65$ | NOTPLACED | NOTPLACED |
| C571 | $2105285 \times 01$ | NOTPLACED | $2105285 \times 01$ | $2105285 \times 01$ |
| JU500 | NOTPLACED | NOTPLACED | 0611077A01 | 0611077A01 |
| JU513 | NOTPLACED | NOTPLACED | 0611077A01 | 0611077A01 |
| JU518 | 0611077A01 | 0611077A01 | NOTPLACED | NOTPLACED |
| JU519 | 0611077A01 | 0611077A01 | NOTPLACED | 0611077A01 |
| JU521 | 0611077A01 | 0611077A01 | NOTPLACED | NOTPLACED |
| JU528 | NOTPLACED | NOTPLACED | 0611077A01 | NOTPLACED |
| JU532 | NOTPLACED | NOTPLACED | NOTPLACED | 0611077A01 |
| R561 | 0611077B29 | NOTPLACED | 0611077B29 | NOTPLACED |

## HLN5558E/F/G, HLN6529C/D/E, HLN6560C/D/E/F/G/H

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
|  |  | CAPACITOR, Fixed: unless otherwise stated |
| C401, 402 | 2113741N69 | 0.14F |
| C404 | 2113741N45 | . 014 F |
| C405 | 2113743G21 | 14F |
| C406 | 2113743G24 | 2.24F |
| C408 | 2113741N45 | .014F |
| C412 | 2113741N69 | 0.14F |
| C413 | $2113740 B 57$ | 220pF |
| C416 | 2113743G24 | 2.24F |
| C418 | 2113741N69 | 0.14F |
| C420, 421 | 2113743G24 | 2.24F |
| C434 | 2113741N69 | 0.14F |
| C436 | 2311049,09 | 3.34F |
| C437 | 2311049J26 | 10MF |
| C438 | 2311049J09 | 3.34F |
| C451, 452 | 2113743 G 21 | $1 \mu \mathrm{~F}$ |
| C453, 454 | 2113741N69 | 0.14F |
| C455 | $2113740 B 57$ | 220pF |
| C456 | 2380090M24 | 10MF |
| C457, 458 | 2113741N69 | 0.14F |
| C459 thru 461 | 2113743G24 | 2.24 F |
| C462 | 2113741851 | .018 HF (HLN5558F/G and HLN6529D/E) |
|  | or 2113741N45 | .01 F (HLN5558E and HLN6529C) |
| C463 | 2113740B49 | 100pF (HLN5558E and HLN6529C) |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
|  | Or2113740859 | 270pF(HLN5558F/G and HLN6529D/E |
| C464 | 2113741N45 | . 014 F |
| C465 | 2113741861 | .047 $\mu \mathrm{F}$ (not placed on <br> HLN5558F/G and HLN6529D/E) |
| C466 | $2113743 F 14$ | 0.47 F |
| C467 | 2113741B61 | .047 $\mu \mathrm{F}$ (Not placed on HLN5558F/G and HLN6529D/E) |
| C468 | $2113743 F 08$ | $0.22 \mu \mathrm{~F}$ |
| C476 | 2113740B65 | 470pF |
| C478 | 2113740B65 | 470pF |
| C480 | 2113740B65 | 470pF |
| C481 | 2113741N21 | 1000pF |
| C482 | 2311049C07 | 100 F |
| C500, 501 | 2113743 G 24 | 2.24 F |
| C502 | 2113740B36 | 30pF |
| C503 | 2113743 G 21 | $1 \mu \mathrm{~F}$ (Not placed on HLN5558F/G and HLN6529E) |
| C504 | 2113741N21 | 1000pF |
| C505 | 2311049A40 | 3.34F |
| C506, 507 | 2113743 G 24 | 2.2 2 F |
| C508 | 2113743 G 21 | $1 \mu \mathrm{~F}$ |
| C509 | 2113743 G 24 | 2.24F |
| C510 | 2113741N69 | 0.14F |
| C511 | 2113743 F 16 | $1 \mu \mathrm{~F}$ |
| C512 | 2311049A40 | 3.34F |
| C513 | 2311049A03 | 0.22 F |
| C524 | 2113741 N45 | . 01 MF |
| C527 | 2113741 N69 | 0.14F |
| C535 | 2113741 N 45 | . 014 F |
| C548 | 2113743 G 21 | $1 \mu \mathrm{~F}$ |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C549 | 2113741N69 | 0.14 F |
| C554 | 2113740865 | 470pF |
| C560 | 2311049A40 | 2.24F |
| C561 thru 565 | 2113740865 | 470pF |
| C569, 570 | 2113740865 | 470pF |
| C571 | 2105285×01 | 0.47 F (Not placed on HLN6529C/D/E) |
| C572 | 2113740865 | 470pF |
| C575 thru 577 | 2113740865 | 470pF |
| C580 | 2113741N69 | 0.14F |
| C581 thru 583 | 2113740865 | 470pF |
| C584 |  | Not Placed |
| C585 | 2113740865 | 470pF |
| C586 thru 589 | 2113743G24 | 2.24F |
| C591 | 2113740B36 | 30pF |
| C592 | 2113740B65 | 470pF |
| C801 | 2113741N69 | 0.14F |
| C802 thru 804 | 2113740865 | 470pF |
| C805 | 2113741N69 | 0.14F |
| C806 | 2113740865 | 470pF |
| C807 | 2113741N69 | 0.14F |
| C808, 809 | 2113740865 | 470pF |
| C811 | 2113740865 | 470pF |
| C813 | 2113741N69 | 0.14F |
| C814 | 2113740865 | 470pF |
| C815 | 2113741N69 | 0.14F |
| C816, 817 | 2113740B65 | 470pF |
| C818 | 2113741N69 | 0.14F |
| C819 | 2113743 G 21 | 14 F |
| C820 thru 822 | 2113740865 | 470pF |
| C860 thru 877 | 2113740865 | 470pF |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
|  |  | DIODE: See Note 1. |
| CR400 | 4813833C10 |  |
| CR401 | 4813833C03 | Dual |
| CR402 |  | Not Placed |
| CR403 | 4813833C10 |  |
| CR500, 501 | 4813833 C 10 |  |
| CR502 |  | Not Placed |
| CR503 CR504 | 4813833C10 | Not Placed |
|  |  |  |
| F500 | 6505663R06 | rus |
|  |  | JACK: |
| J500 | 0980104M02 | Receptacle, Bottom Entry |
|  |  | JUMPER: |
| JU500, 501 | -------- | Not Placed |
| JU505 | - $-\cdots \cdot \cdots$ - | Not Placed |
| JU512 | 0611077A01 | Not Placed 0 |
| JU513 |  | Not Placed |
| JU514 | 0611077A01 | $0 \Omega$ |
| JU515 thru 517 |  | Not Placed |
| JU518 thru 521 | 0611077A01 | $0 \Omega$ |
| JU523 thru 527 | 0611077A01 | $0 \Omega$ |
| JU528 ${ }^{\text {JU529 thru } 531}$ |  | Not Placed |
| JU529 thru 531 JU532 | 0611077A01 | $0 \Omega$ <br> Not Placed |
| JU533 | 0611077A01 | $0 \Omega$ |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| J0534,535 | - | Not Placed |
| JU536 | 0611077A01 | $0 \Omega$ |
|  |  | COIL, RF: unless otherwise stated |
| L500 | 2480067M01 | Bead |
| L506 | 2480067M01 | Bead |
| L511 | 2413924A13 | 10 H |
| L528 | 2480067M01 | Bead |
|  |  | PLUG: |
| P501 | 2805515W01 | Connector |
| P502 | 0980103 M 02 | Receptacle, Right Angle |
| P503 | 2880267M01 | Header, Right Angle |
| Q400 | 4880141L01 | TRANSISTOR: See Note 1. PNP |
| Q401 | 4880048M01 | NPN |
| Q402 | 4880141L01 | PNP |
| Q403, 404 | 4880048M01 | NPN |
| Q500 | 4813822 D 39 | PNP |
| Q501 | 4880053M02 | PNP |
| Q502 | 4880182 D 46 | NPN |
| Q503 | 4880141L02 | NPN |
| Q504 | 4805128M27 | SOT89 |
| Q506 | 4805128M27 | SOT89 |
| Q507 | 4880048M01 | NPN |
| Q508, 509 | 4805128M12 | SOT |
| Q510 | 4880048M01 | NPN |
| Q511 | 4880141L01 | NPN |
| Q512 |  | Not Placed |
| Q513 thru 515 | 4880048M01 | NPN |
| Q516, 517 |  | Not Placed |
| Q520 | 4880048M01 | NPN |
| Q521 | 4880182D46 | NPN |
| Q538 | 4880048M01 | NPN |
| Q540 thru 543 | 4880048M01 | NPN |
| Q554 | 4880048M01 | NPN |
| Q555 | 4805218N11 | SOT |
| R400 | 0611077 003 | RESISTOR, Fixed: $\Omega \neq 5 \%$ |
| R401, 402 | $0611077 \mathrm{B25}$ | 120k |
| R403, 404 | 0611077 B15 | 47k |
| R405, 406 | 0611077A82 | 2.2k |
| R407 | 0611077 A86 | 3.3k (HLN5558E and HLN6529C) |
|  | or $0611077 \mathrm{B15}$ | 47k (HLN5558F/G and HLN6529D/E) |
| R408 | $0611077 B 15$ | 47k (HLN5558F/G and HLN6529D/E) |
|  | or 0611077B29 | 180k (HLN5558E and HLN6529C) |
| R409, 410 | 0611077811 | 33k |
| R411, 412 | 0611077 B 15 | 47k |
| R413 | $0611077 \mathrm{B23}$ | 100k |
| R414 | 0611077 A74 | 1k |
| R415 | 0611077 A68 | 560 |
| R416 | 0611077 B15 | 47k |
| R417, 418 | 0611077A98 | 10k |
| R419 | 0611077 B23 | 100k |
| R420 | 0611077 B03 | 15k |
| R421 | 0611077 B07 | 22k |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R422 | 0611077 B20 | 75 k |
| R423 | 0611077A68 | 560 |
| R424, 425 | 0611077A74 | 1k |
| R426, 427 | 0611077A98 | 10k |
| R428, 429 | 0611077 A74 | 1 k |
| R437 | $0611077 \mathrm{B15}$ | 47k |
| R438, 439 | 0611077B03 | 15 k |
| R440 | $0611077 \mathrm{B23}$ | 100k |
| R441 | 0611077A90 | 4.7k (HLN5558F/G and HLN6529D/E) |
|  | or 0611077B12 | 36k (HLN5558E and HLN6529C) |
| R442 | 0611077 A95 | 7.5 k (HLN5558E and HLN6529C) |
|  | or 0611077A98 | 10k (HLN5558F/G and HLN6529D/E) |
| R443 | 0611077A95 | 7.5k (HLN5558F/G and HLN6529D/E) |
|  | or 0611077B01 | 12k (HLN5558E and |
| R454 | 0611077B23 | 100k |
| R455 | 0611077A98 | 10k |
| R456 | 0611077 B11 | 33k |
| R457, 458 | 0611077A10 | 2.2 |
| R459 |  | Not Placed |
| R500 | 0611077A46 | 68 |
| R501 | 0611077A58 | 220 |
| R502 | 0611077A10 | 2.2 |
| R503 | 0611077 A98 | 10k |
| R504 | 0611077A74 | 1 k |
| R505, 506 | 0611077A58 | 220 |
| R507 | $0611077 B 15$ | 47k |
| R508, 509 | 0611077819 | 68k |
| R510 | 0611077A98 | 10k |
| R511 | 0611077A74 | 1 k |
| R512 | 0611077A58 | 220 |
| R513 | 0611077A50 | 100 |
| R514 | 0611077A68 | 560 |
| R515 | 0611077 A74 | 1 k |
| R516 | 0611077 B 23 | 100k |
| R517 | 0611077A10 | 2.2 |
| R518 | 0611077 A74 | 1k |
| R519 | $0611077 B 15$ | 47k |
| R520 | 0611077474 | 1 l (HLN5558G and HLN6529E) |
|  | or 0611077A98 | 10k (HLN5558E/F and HLN6529C/D) |
| R521 | 0611077 A74 | 1k |
| R522 | 0611077A78 | 1.5k |
| R523 | 0611077A82 | 2.2k |
| R524 | 0611077A98 | 10k |
| R525 | 0611077A50 | 100 |
| R526 | 0611077 B07 | 22k |
| R527 thru 531 | $0611077 B 15$ | 47k |
| R532, 533 | 0611077A62 | 330 |
| R534 |  | Not Placed |
| R535 | 0611077A98 | 10k |
| R536 |  | Not Placed |
| R537 | 0611077A82 | 2.2k |
| R543 | 0611077 A74 | 1 k |
| R544 | 0611077A98 | 10k |
| R545 | 0611077A74 | 1 k |
| R546, 547 | 0611077A98 | 10 k |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R550, 551 | 0611077 A98 | 10k |
| R552 | 0611077815 | 47k |
| R553 | 0611077A90 | 4.7k |
| R554 | 0611077A80 | 1.8k |
| R555 | 0611077A98 | 10k |
| R557 | 0611077A98 | 10k |
| R560 | 0611077 A74 | 1k |
| R561 | 0611077B29 | 180k (Not placed on HLN6529C/D/E) |
| R562 | 0611077A50 | 100 |
| R565 | 0611077A50 | 100 |
| R571 thru 573 | 0611077A74 | 1k |
| R575, 576 | 0611077A50 | 100 |
| R577 | 0611077A98 | 10k |
| R578, 579 | 0611077A74 | 1k |
| R584, 585 | 0611077A50 | 100 |
| R590 | 0611077A01 | 0 |
| R591 | 0611077 A74 | 1k |
| R592 | 0611077A90 | 4.7k |
| R745 | 0611077A98 | 10k |
| U401, 402 | 5183222M49 | INTEGRATED CIRCUIT MODULE: See Note 1. |
| U450 | 5180065C19 | Audio Amplifier |
| U500 | 5180057S01 | Regulator/Power Control |
| U501 | 5113815G02 | 555 Timer |
| U502 | 5180057S02 | D/A Converter |
| U503 | 5180056m02 | Voltage Regulator |
| U522 | 5180057S04 | Serial I/O |
| U523 | 5113806 A20 | MUXIDEMUX, Triple 2-Ch |
| U524 | 5182400711 | Analog Switch, Quad |
| U525 | 5113806 A20 | MUX/DEMUX, Triple 2-Ch |
| U526 | 5113811 A11 | RS232-C Driver |
| U530 | $5105492 \times 76$ | 8-Bit Shift Register |
| VR400 | 4813830A28 | DIODE: See Note 1. |
| VR401 | 4813830A24 | Zener; 11 V |
| VR402 | 4813830A28 | Zener 15V |
| VR403 | 4813830A22 | Zener; 9.1V (HLN5558E/F and HLN6529C/D) |
| VR404 thru 406 VR407 | or 4813830A26 | Zener; 13V (HLN5558G and HLN6529E) |
|  | 4813830A28 | Zener 15V |
|  | 4805129M27 |  |
|  | 8405885 V 05 | MISCELLANEOUS: <br> Command Board |

Notes:
For optimum performance, order replacement diodes, transistors, and circuit 2. Modulus by Motororman part

Part value notations:
$\mathrm{p}=10^{-12}$
$\begin{array}{ll}\mathrm{p}=10^{-12} & \mathrm{~m}=10^{-3} \\ \mathrm{n}=10^{-9} & \mathrm{k}=10^{3} \\ \mu=0^{-6} & \mathrm{M}=10^{6}\end{array}$
3. ITEM refers to the component refererced designator. SIDE refers to the loca 4. tion of the component on the board; S1=Side 1 , S2=Sid

6-LAYER CIRCUIT BOARD DETAL VIEWING
COPPER STEPS IN PROPER LAYER SEQUENCE
SIDE 1



HLN5558H/J, HLN6529H, HLN6560J and HLN6562J Command Board Schematic Diagram

side 1

side 2

Command Boards Reference Designator and Part
Number Comparison Char
The following table lists those reference designators
common to the various command board kits and the part numbers assigned to those designators for each kit and version.

| Ref Des | HLN5558J <br> Part Number | HLN6529H <br> Part Number | HLN6560J <br> Part Number | HLN6562J <br> Part Number |
| :--- | :--- | :--- | :--- | :--- |
| C571 | 2105285X01 | NOTPLACED | 2105285X01 | 2105285X01 |
| JU500 | NOTPLACED | NOTPLACED | 0611077A01 | 0611077 A01 |
| JU513 | NOTPLACED | NOTPLACED | 0611077A01 | 0611077A01 |
| JU518 | 0611077A01 | 0611077A01 | NOTPLACED | NOTPLACED |
| JU519 | $06611077 A 01$ | 0611077A01 | NOTPLACED | 0611077A01 |
| JU521 | 0611077A01 | 0611077A01 | NOTPLACED | NOTPLACED |
| JU528 | NOTPLACED | NOTPLACED | 0611077A01 | NOTPLACED |
| JU532 | NOTPLACED | NOTPLACED | NOTPLACED | 0611077A01 |

## HLN5558H/J, HLN6529H, HLN6560J and HLN6562

 Parts List| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| BT001 | -------- | NOTPLACED |
| C401 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C402 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R } 10 \% \\ & 100000 \\ & \hline \end{aligned}$ |
| C404 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C405 | 2113743G21 | CER CHIP CAP 1.0 UF |
| C406 | 2113743G24 | $\begin{aligned} & \text { CAP CHIP } 2.2 \text { UF 16V +80- } \\ & 20 \% \end{aligned}$ |
| C408 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C412 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C413 | 2113740B57 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 220 \end{aligned}$ |
| C416 | 2113743G24 | CAP CHIP 2.2 UF 16V +80- $20 \%$ CAP |
| C418 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C420 | 2113743G24 | $\begin{aligned} & \text { CAP CHIP } 2.2 \text { UF } 16 \mathrm{~V}+80- \\ & 20 \% \end{aligned}$ |
| C421 | 2113743G24 | CAP CHIP 2.2 UF 16V +80- $20 \%$ |
| C434 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C436 | 2311049J09 | CAP TANT CHIP 3.31035 |
| C437 | 2311049 J 26 | CAP TANT CHIP 102016 |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C438 | 2311049J09 | CAP TANT CHIP 3.31035 |
| C451 \& C452 | 2113743 G 21 | CER CHIP CAP 1.0 UF |
| C453 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C454 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C455 | $2113740 B 57$ | CAP CHIP REEL CL1 +/-30 220 |
| C456 | 2380090M24 | CAP ALU 1020 50V SURF MT |
| C458 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| $\begin{aligned} & \text { C459 thru } \\ & \text { C461 } \\ & \hline \end{aligned}$ | 2113743 G 24 | $\begin{aligned} & \text { CAP CHIP 2.2 UF 16V +80- } \\ & 20 \% \\ & \hline \end{aligned}$ |
| C462 | 2113741B51 | CAP CHIP CL2 X7R REEL 18000 |
| C463 | 2113740B59 | CAP CHIP REEL CL1 +/-30 270 |
| C464 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C465 | -------- | NOTPLACED |
| C466 | 2113743F14 | $\begin{aligned} & \text { CAP CHIP .470UF +80-- } \\ & 20 \% \text { Y5V } \end{aligned}$ |
| C467 | - | NOTPLACED |
| C468 | 2113743F08 | CER CHIP CAP .220UF |
| $\begin{aligned} & \text { C476, C478 } \\ & \text { \& C480 } \\ & \hline \end{aligned}$ | 2113740B65 | CAP CHIP REEL CL1 $+/-30$ 470 |
| C481 | 2113741N21 | CAP CHIP CL2 X7R 10\% 1000 |
| C482 | 2311049A28 | CAP TANT CHIP 100UF 6 V |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C500 \& C501 | 2113743G24 | $\begin{aligned} & \text { CAP CHIP 2.2 UF 16V +80- } \\ & 20 \% \end{aligned}$ |
| C502 | 2113740 B 36 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 30 \end{aligned}$ |
| C503 | --------- | NOTPLACED |
| C504 | 2113741N21 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 1000 \end{aligned}$ |
| C505 | 2311049A40 | CAP TANT CHIP 2.21010 |
| C506 \& C507 | 2113743G24 | CAP CHIP 2.2 UF $16 \mathrm{~V}+80-$ $20 \%$ |
| C508 | 2113743G21 | CER CHIP CAP 1.0 UF |
| C509 | 2113743G24 | $\begin{aligned} & \text { CAP CHIP } 2.2 \text { UF } 16 \mathrm{~V}+80- \\ & 20 \% \end{aligned}$ |
| C510 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C511 | $2113743 F 16$ | CAP CHIP 1.0 UF $16 \mathrm{~V}+80-1$ $20 \%$ |
| C512 | 2311049A40 | CAP TANT CHIP 2.21010 |
| C513 | 2311049A03 | CAP TANT CHIP A/P . 2210 $35$ |
| C524 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C527 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C535 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C548 | 2113743G21 | CER CHIP CAP 1.0 UF |
| C549 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R } 10 \% \\ & 100000 \end{aligned}$ |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C554 | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C560 | 2311049A40 | CAP TANT CHIP 2.21010 |
| C561 | 2113740B65 | CAP CHIP REEL CL1 $+/-30$ 470 |
| C562 | $2113740 B 65$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 }+/-30 \\ & 470^{1} \text { and } 5 \\ & \text { NOTPLACED }{ }^{2,3} \text {, and } 4 \end{aligned}$ |
| C563 | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C564 | $2113740 B 65$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C565 | 2113740865 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C569 | $2113740 B 65$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C570 | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C571 | 2105285X01 | $\begin{aligned} & \text { CAP GOLD F SERIES ( } 47 \\ & \text { FARAD) } 1,2,4 \text { and } 5 \\ & \text { NOT PLACED }{ }^{3} \end{aligned}$ |
| C572 | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C575 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C576 | $2113740 B 65$ | CAP CHIP REEL CL1 $+/-30$ 470 |
| C577 | $2113740 B 65$ | CAP CHIP REEL CL1 $+/-30$ 470 |
| C580 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C581 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C582 | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C583 | 2113740B65 | CAP CHIP REEL CL1 $+/-30$ 470 |
| C584 | -- | NOTPLACED |
| C585 | 2113740B65 | $\underset{470}{\text { CAP CHIP REEL CL1 }+/-30}$ |
| $\begin{aligned} & \text { C586 thru } \\ & \text { C589 } \\ & \hline \end{aligned}$ | 2113743G24 | $\begin{aligned} & \hline \text { CAP CHIP } 2.2 \text { UF } 16 \mathrm{~V}+80- \\ & 20 \% \\ & \hline \end{aligned}$ |
| C591 | 2113740B36 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 30 \end{aligned}$ |
| C592 | 2113740B65 | CAP CHIP REEL CL1 $+/-30$ 470 |
| C601 | 2311049J26 | CAP TANT CHIP 102016 <br> 2, 3,4 , and 5 |
| C602 | 2113740B65 | CAP CHIP REEL CL1 $+/-30$ 470 |
| C604 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C605 | 2311049J26 | $\left.\right\|_{2,3,4, \text { and } 5} ^{\text {CAP CHIP } 102016}$ |
| C801 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| $\begin{array}{\|l} \hline \text { C802 thru } \\ \text { C804 } \\ \hline \end{array}$ | 2113740B65 | CAP CHIP REEL CL1 $+/-30$ 470 |
| C805 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C806 | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C807 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| $\begin{aligned} & \text { C808, C809 } \\ & \& \text { C811 } \end{aligned}$ | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C813 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C814 | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C815 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C816 \& C817 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C818 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C819 | 2113743 G 21 | CER CHIP CAP 1.0 UF |
| $\begin{array}{\|l} \hline \text { C820 thru } \\ \text { C822 } \\ \hline \end{array}$ | 2113740B65 | CAP CHIP REEL CL1 $+/-30$ 470 |
| $\begin{aligned} & \text { C860 thru } \\ & \text { C877 } \\ & \hline \end{aligned}$ | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| CR400 | 4813833C10 | DIODE GEN PUR 70V MMBD6050 |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| CR401 | 4813833C03 | DIODE DUAL 70V 'A1X' BAW56LT1 |
| CR402 | -------- | NOTPLACED |
| CR403, CR500 \& CR501 | 4813833C10 | DIODE GEN PUR 70V MMBD6050 |
| CR502 | -------- | NOTPLACED |
| CR503 | 4813833C10 | DIODE GEN PUR 70V MMBD6050 |
| CR504 | -------- | NOTPLACED |
| CR600 | 4813833 C 10 | DIODE GEN PUR 70V MMBD6050 |
| F500 | $6505663 R 06$ | FUSE 7 AMP |
| F501 | 6505663R03 | FUSE 5 AMP |
| J500 | 0980104M02 | RECEPTACLE BOTTOM ENTRY |
| JU500 | 0611077A01 | NOTPLACED ${ }^{1,2, \text { and } 3}$ RES CHIP JUMPER ${ }^{4}$ and 5 |
| JU501 | -------- | NOTPLACED |
| JU502 | 0611077A01 | RES CHIP JUMPER |
| JU505 | -------- | NOTPLACED |
| JU507 | 0611077A01 | RES CHIP JUMPER |
| $\begin{array}{\|l} \hline \text { JU508 thru } \\ \text { JU511 } \end{array}$ | -------- | NOTPLACED |
| JU512 | 0611077 A01 | RES CHIP JUMPER |
| JU513 | 0611077A01 | NOTPLACED ${ }^{1,2, \text { and } 3}$ RES CHIP JUMPER ${ }^{4}$ and 5 |
| JU514 | 0611077A01 | RES CHIP JUMPER |
| $\begin{array}{\|l} \hline \text { JU515 thru } \\ \text { JU517 } \end{array}$ | -------- | NOTPLACED |
| JU518 | 0611077A01 | RES CHIP JUMPER ${ }^{1,2,}$ and 3 <br> NOTPLACED ${ }^{4}$ and 5 |
| JU519 | 0611077 A01 | $\begin{aligned} & \text { RES CHIP JUMPER } \\ & \text { and } 5 \text {, 2,3, } \\ & \text { NOTPLACED }{ }^{4} \end{aligned}$ |
| JU520 | 0611077 A 01 | RES CHIP JUMPER |
| JU521 | 0611077A01 | RES CHIP JUMPER ${ }^{1,2,}$ and 3 <br> NOTPLACED 4 and 5 |
| $\begin{array}{\|l} \hline \text { JU523 thru } \\ \text { JU527 } \end{array}$ | 0611077A01 | RES CHIP JUMPER |
| JU528 | 0611077A01 | NOTPLACED ${ }^{1,2,3 \text {, and } 5}$ RES CHIP JUMPER ${ }^{4}$ |
| $\begin{array}{\|l\|} \hline \text { JU529 thru } \\ \text { JU531 } \end{array}$ | 0611077A01 | RES CHIP JUMPER |
| JU532 | --------- | NOTPLACED |
| JU533 | 0611077A01 | RES CHIP JUMPER |
| $\begin{array}{\|l\|} \hline \text { JU534, } \\ \text { JU535 } \end{array}$ | -------- | NOTPLACED |
| JU536 | 0611077A01 | RES CHIP JUMPER |
| JU537 | -------- | NOTPLACED |
| JU538 | 0611077A01 | RES CHIP JUMPER |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| JU601 |  | NOTPLACED |
| L500 \& L506 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L511 | 2413924A13 | IND CHIP 10.0 UH 10\% |
| L528 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| P501 | 2805515W01 | CONN PLUG |
| P502 | 0980103M02 | RECEPTACLE RIGHT ANGLE |
| P503 | 2880267M01 | PLUG RIGHT ANGLE HDR 26 POS |
| Q400 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q401 | 4880048M01 | TSTR NPN DIG 47K/47K |
| Q402 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| $\begin{aligned} & \text { Q403 \& } \\ & \text { Q404 } \end{aligned}$ | 4880048M01 | TSTR NPN DIG 47K/47K |
| Q500 | 4813822D39 | TSTR PNP 60V 7A 2N5194 |
| Q501 | 4880053M02 | TSTR NPN MXT2222A |
| Q502 | 4880182 D 46 | TSTR NPN 2N5190 |
| Q503 | 4880141L02 | TSTR NPN SOT23 LO PROFILE TAPE |
| $\begin{aligned} & \text { Q504 \& } \\ & \text { Q506 } \\ & \hline \end{aligned}$ | 4805128M27 | TSTR SOT89 BSR33 LH |
| Q507 | 4880048M01 | TSTR NPN DIG 47K/47K |
| Q508 | 4805218N50 | TSTR SOT BCW 60B (RH) |
| Q509 | 4805128M12 | TSTR SOT BCW 60B (RH) |
| Q510 | 4880048M01 | TSTR NPN DIG 47K/47K |
| Q511 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q512 |  | NOTPLACED |
| $\begin{aligned} & \text { Q513 thru } \\ & \text { Q515 } \end{aligned}$ | 4880048M01 | TSTR NPN DIG 47K/47K |
| $\begin{aligned} & \text { Q516 \& } \\ & \text { Q517 } \end{aligned}$ |  | NOTPLACED |
| Q520 | 4880048M01 | TSTR NPN DIG 47K/47K |
| Q521 | 4880182 D 46 | TSTR NPN 2N5190 |
| Q538 | 4880048M01 | TSTR NPN DIG 47K/47K |
| $\begin{aligned} & \text { Q540 thru } \\ & \text { Q543 } \\ & \hline \end{aligned}$ | 4880048M01 | TSTR NPN DIG $47 \mathrm{~K} / 47 \mathrm{~K}$ |
| Q554 | 4880048M01 | TSTR NPN DIG 47K/47K |
| Q555 | 4805218 N 11 | XISTOR SOT RH BST82 |
| $\begin{aligned} & \text { Q601 \& } \\ & \text { Q602 } \end{aligned}$ | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q603 | 4805128M12 | TSTR SOT BCW 60B (RH) <br> 2, 3, 4, and 5 |
| R400 | $0611077 B 03$ | RES CHIP 15K 5 1/8W |
| R401 \& R402 | 0611077 B 25 | RES CHIP 120K $51 / 8 \mathrm{~W}$ |
| R403 \& R404 | 0611077 B 15 | RES CHIP $47 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R405 \& R406 | 0611077 A82 | RES CHIP $220051 / 8$ |
| R407 \& R408 | 0611077 B 15 | RES CHIP 47K 5 1/8W |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R409 \& R410 | $0611077 \mathrm{B11}$ | RES CHIP 33K 5 1/8W |
| R411 \& R412 | $0611077 \mathrm{B15}$ | RES CHIP 47K 5 1/8W |
| R413 | 0611077 B 23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R414 | 0611077A74 | RES CHIP 10005 1/8 |
| R415 | 0611077 A68 | RES CHIP 5605 1/8W |
| R416 | 0611077 B 15 | RES CHIP 47K $51 / 8 \mathrm{~W}$ |
| R417 \& R418 | 0611077 A98 | RES CHIP 10K 5 1/8W |
| R419 | 0611077 B 23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R420 | 0611077 B03 | RES CHIP 15K 5 1/8W |
| R421 | 0611077 B07 | RES CHIP 22K 5 1/8W |
| R422 | 0611077B20 | RES CHIP 75K 5 1/8W |
| R423 | 0611077 A68 | RES CHIP 5605 1/8W |
| R424 \& R425 | 0611077A74 | RES CHIP 10005 1/8 |
| R426 \& R427 | 0611077A98 | RES CHIP 10K 5 1/8W |
| R428 \& R429 | 0611077A74 | RES CHIP $100051 / 8$ |
| R437 | 0611077 B 15 | RES CHIP 47K 5 1/8W |
| R438 \& R439 | 0611077 B03 | RES CHIP 15K 5 1/8W |
| R440 | 0611077 B 23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R441 | 0611077A90 | RES CHIP $470051 / 8$ |
| R442 | 0611077 A98 | RES CHIP 10K 5 1/8W |
| R443 | 0611077 A95 | RES CHIP 75005 1/8 |
| R454 | 0611077 B 23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R455 | 0611077A98 | RES CHIP 10K 5 1/8W |
| R456 | 0611077 B 11 | RES CHIP $33 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R457 \& R458 | 0611077A10 | RES CHIP 2.25 1/8W |
| R459 |  | NOTPLACED |
| R500 | 0611077A46 | RES CHIP 685 1/8W |
| R501 | 0611077 A58 | RES CHIP 2205 1/8W |
| R502 | 0611077A10 | RES CHIP 2.25 1/8W |
| R503 | 0611077 A98 | RES CHIP 10K 5 1/8W |
| R504 | 0611077A74 | RES CHIP 10005 1/8 |
| R505 \& R506 | 0611077 A58 | RES CHIP 2205 1/8W |
| R507 | 0611077 B 15 | RES CHIP 47K 5 1/8W |
| R508 \& R509 | 0611077 B 19 | RES CHIP $68 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R510 | 0611077 A98 | RES CHIP 10K 5 1/8W |
| R511 | 0611077A74 | RES CHIP $100051 / 8$ |
| R512 | 0611077A58 | RES CHIP 2205 1/8W |
| R513 | 0611077A50 | RES CHIP 1005 1/8W |
| R514 | 0611077 A68 | RES CHIP 5605 1/8W |
| R515 | 0611077 A74 | RES CHIP 10005 1/8 |
| R516 | 0611077 B 23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R517 | 0611077A10 | RES CHIP 2.25 1/8W |
| R518 | 0611077 A74 | RES CHIP $100051 / 8$ |
| R519 | 0611077 B 15 | RES CHIP 47K 5 1/8W |
| R521 | 0611077A74 | RES CHIP 10005 1/8 |
| R522 | 0611077 A78 | RES CHIP 15005 1/8 |
| R523 | 0611077A82 | RES CHIP 22005 1/8 |
| R524 | 0611077 A98 | RES CHIP 10K 5 1/8W |
| R525 | 0611077A50 | RES CHIP 1005 1/8W |
| R526 | $0611077 \mathrm{B07}$ | RES CHIP 22K $51 / 8 \mathrm{~W}$ |
| R527 | $0611077 B 15$ | RES CHIP 47K 5 1/8W |


| ITEM | MOTOROLA <br> PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { R528 thru } \\ & \text { R531 } \end{aligned}$ | $0611077 \mathrm{B15}$ | RES CHIP 47K 5 1/8W |
| R532 \& R533 | 0611077 A 62 | RES CHIP 3305 1/8W |
| R534 |  | NOTPLACED |
| R535 | 0611077A98 | RES CHIP 10K $51 / 8 \mathrm{~W}$ |
| R536 |  | NOTPLACED |
| R537 | 0611077A82 | RES CHIP $220051 / 8$ |
| R543 | 0611077A74 | RES CHIP $100051 / 8$ |
| R544 | 0611077 A98 | RES CHIP 10K 5 1/8W |
| R545 | 0611077A74 | RES CHIP 10005 1/8 |
| R546 \& R547 | 0611077A98 | RES CHIP 10K 5 1/8W |
| R550 \& R551 | 0611077A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R552 | 0611077B15 | RES CHIP 47K $51 / 8 \mathrm{~W}$ |
| R553 | 0611077A90 | RES CHIP 47005 1/8 |
| R554 | 0611077A80 | RES CHIP $180051 / 8$ |
| R555 \& R557 | 0611077A98 | RES CHIP 10K $51 / 8 \mathrm{~W}$ |
| R560 | 0611077A74 | RES CHIP 10005 1/8 |
| R561 | 0611077B29 | RES CHIP 180K $51 / 8 \mathrm{~W}$ |
| R562 \& R565 | 0611077A50 | RES CHIP 1005 1/8W |
| $\begin{aligned} & \text { R571 thru } \\ & \text { R573 } \end{aligned}$ | 0611077A74 | RES CHIP 10005 1/8 |
| R575 \& R576 | 0611077A50 | RES CHIP 1005 1/8W |
| R577 | 0611077A98 | RES CHIP 10K 5 1/8W |
| R578 \& R579 | 0611077A74 | RES CHIP 10005 1/8 |
| R584 \& R585 | 0611077A50 | RES CHIP 1005 1/8W |
| R590 | 0611077A01 | RES CHIP JUMPER |
| R591 | 0611077A74 | RES CHIP $100051 / 8$ |
| R592 | 0611077 A90 | RES CHIP $470051 / 8$ |
| R601 \& R602 | 0611077B23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R603 | 0611077 B 29 | RES CHIP 180K $51 / 8 \mathrm{~W}$ |
| R604 | 0611077A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R605 | 0611077A90 | RES CHIP $470051 / 8$ |
| R606 | 0611077A98 | RES CHIP 10K 5 1/8W |
| R608 | 0611077A78 | RES CHIP 15005 1/8 |
| R609 | 0611077A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R610 | -------- | NOTPLACED |
| R611 | 0611077 A01 | RES CHIP JUMPER |
| R612 | $0611077 B 07$ | RES CHIP $22 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R613 \& R614 | 0611077A10 | RES CHIP 2.25 1/8W |
| R745 | 0611077A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| U401 \& U402 | 5183222M49 | IC AMP 3403 |
| U450 | 5180065C19 | IC AUDIO PA 4.5 AMP 22W |
| U500 | 5180057 S01 | IC REG/PWR CONT 13" REEL |
| U501 | 5113815 G 02 | IC TIMER 5551455 |
| U502 | 5180057 S02 | IC CMOS CUST DA CONV 13" REEL |
| U503 | 5180056M02 | IC 2.5V PRGMBLE PREC REF SO 8 |
| U522 | 5180057S04 | IC CUST SER I/O 13" REEL |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| U523 | 5113806A20 | IC MUXIDEMUX,TRIPLE $2-$ CHNL |
| U524 | 5182400 T 11 | IC HCMOS 04M52 QUAD SW |
| U525 | 5113806 A20 | IC MUXIDEMUX,TRIPLE 2CHNL |
| U526 | 5113811A11 | IC RS-232-C DVR/RCVR SNGL SUP |
| U530 | $5105492 \times 76$ | IC 8 BIT SHIFT REGISTER |
| U601 | 5113806 A05 | IC FLIP-FLOP,DUAL D |
| U602 | 5105625 U 41 | IC +5V REGULATOR |
| U603 | 5113806 A37 | IC SCHMITT TRIGGER,HEX |
| U604 | 5113816A19 | IC ADJ LO DROPOUT POS REG .8A |
| VR400 | 4813830A28 | DIODE 15V 5\% 225MW MMBZ5245B |
| VR401 | 4813830A24 | DIODE 11V 5\% 225MW MMBZ5241B |
| VR402 | 4813830A28 | DIODE 15V 5\% 225MW MMBZ5245B |
| VR404 thru VR406 | 4813830A28 | DIODE 15V 5\% 225MW MMBZ5245B |
| VR407 | 4805129M27 | DIODE MMBZ5226 RH |
| VR601 | 4813830A14 | DIODE 5.1V 5\% 225MW MMBZ5231B |
| VR603 | -------- | NOTPLACED |
|  | 8485758A04 | BD CKT COMMAND |

Notes:

1. Valid in HLN6558H
2. Valid in HLN5558J
3. Valid in HLN6529J
4. Valid in HN 6562 J

### 7.3 VOCON Section



HLN6458D VOCON Board Schematic (Sheet 1 of 2)



U204-MCU


U206 - SLIC


U402, U403, U414 - DSP SRAM


HLN6458D VOCON Board Component Location Diagrams (Sheet 2 of 2)

| HLN6458D VOCON Board Parts List |  |  |
| :---: | :---: | :---: |
| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| C201 | --- | NOTPLACED |
| C202 | --------- | NOTPLACED |
| $\begin{aligned} & \hline \begin{array}{l} \mathrm{C} 203 \text { \& } \\ \mathrm{C} 204 \\ \hline \end{array}{ }^{2} \\ & \hline \end{aligned}$ | 2113930F39 | CAP CER CHP 33 PF 50V $5 \%$ |
| $\begin{aligned} & \text { C205 thru } \\ & \text { C213 } \end{aligned}$ |  | NOTPLACED |
| C214 | 2113931 F 17 | $\begin{aligned} & \text { CAP CER CHP } 470 \text { PF 5\% } \\ & 50 \mathrm{~V} \end{aligned}$ |
| C215 | 2113930F39 | CAP CER CHP 33 PF 50V $5 \%$ |
| C216 | --------- | NOTPLACED |
| C217 | 2113932K15 | ${ }_{16 \mathrm{~V}}^{\text {CAP CER } . ~} 100$ UF $+80 /-20 \%$ |
| C218 | --------- | NOTPLACED |
| C219 | 2113931F17 | $\begin{aligned} & \text { CAP CER CHP } 470 \text { PF 5\% } \\ & 50 \mathrm{~V} \end{aligned}$ |
| $\begin{aligned} & \hline \mathrm{C} 220 \text { \& } \\ & \mathrm{C} 221 \\ & \hline \end{aligned}$ |  | NOTPLACED |
| $\begin{aligned} & \hline \mathrm{C} 222 \text { \& } \\ & \mathrm{C} 223 \\ & \hline \end{aligned}$ | 2113931 F 17 | $\begin{aligned} & \text { CAP CER CHP } 470 \text { PF 5\% } \\ & 50 \mathrm{~V} \end{aligned}$ |
| $\begin{aligned} & \hline \text { C224 thru } \\ & \text { C226 } \\ & \hline \end{aligned}$ | 2113932K15 | ${ }_{16 \mathrm{~V}}$ CAP CER 100 UF $+80 /-20 \%$ |
| C227 | $2113930 F 36$ | CAP CER CHP 24 PF 50V $5 \%$ |
| C228 | --------- | NOTPLACED |
| C229 | $2113930 F 36$ | CAP CER CHP 24 PF 50V $5 \%$ |
| $\begin{aligned} & \hline \mathrm{C} 230 \text { \& } \\ & \mathrm{C} 231 \\ & \hline \end{aligned}$ | 2113932K15 | CAP CER .100 UF +80/-20\% 16 V |
| $\begin{aligned} & \hline \mathrm{C} 235 \text { \& } \\ & \mathrm{C} 236 \\ & \hline \end{aligned}$ | 2113932K15 | CAP CER . 100 UF +80/-20\% 16 V |
| C237 | 2113931 F 17 | $\begin{aligned} & \text { CAP CER CHP } 470 \text { PF 5\% } \\ & \text { 50V } \end{aligned}$ |
| C238 | 2113932K15 | $\begin{aligned} & \text { CAP CER } 100 \text { UF }+80 /-20 \% \\ & 16 \mathrm{~V} \end{aligned}$ |
| C241 | 2113931 F 17 | $\begin{aligned} & \text { CAP CER CHP } 470 \text { PF 5\% } \\ & 50 \mathrm{~V} \end{aligned}$ |
| $\begin{aligned} & \text { C245 thru } \\ & \text { C247 } \end{aligned}$ | 2113932K15 | CAP CER .100 UF +80/-20\% 16 V |
| C248 | 2113931 F 17 | $\begin{aligned} & \text { CAP CER CHP } 470 \text { PF 5\% } \\ & 50 \mathrm{~V} \end{aligned}$ |
| $\begin{aligned} & \text { C249 thru } \\ & \text { C257 } \\ & \hline \end{aligned}$ |  | NOTPLACED |
| C258 | $2113930 F 39$ | CAP CER CHP 33 PF 50V $5 \%$ |
| C261 | 2113932K15 | $\begin{aligned} & \text { CAP CER . } 100 \text { UF +80/-20\% } \\ & 16 \mathrm{~V} \end{aligned}$ |
| $\begin{aligned} & \hline \mathrm{C} 262 \text { \& } \\ & \mathrm{C} 263 \\ & \hline \end{aligned}$ |  | NOTPLACED |
| C264 | 2113931 F 17 | CAP CER CHP 470 PF 5\% 50V |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{array}{\|l} \hline \text { C265 thru } \\ \text { C267 } \\ \hline \end{array}$ |  | NOTPLACED |
| C268 | 2113931 F 17 | $\begin{aligned} & \text { CAP CER CHP } 470 \text { PF 5\% } \\ & 50 \mathrm{~V} \end{aligned}$ |
| $\begin{array}{\|l} \hline \text { C269 \& } \\ \text { C270 } \\ \hline \end{array}$ | 2113931 F 17 | CAP CER CHP 470 PF 5\% 50 V |
| $\begin{aligned} & \hline \text { C271 \& } \\ & \text { C272 } \\ & \hline \end{aligned}$ |  | NOTPLACED |
| C273 | 2113931 17 | $\begin{aligned} & \text { CAP CER CHP } 470 \text { PF 5\% } \\ & 50 \mathrm{~V} \end{aligned}$ |
| $\begin{aligned} & \text { C274 thru } \\ & \text { C279 } \\ & \hline \end{aligned}$ |  | NOTPLACED |
| $\begin{array}{\|l\|l\|} \hline \text { C281 \& } \\ \text { C282 } \\ \hline \end{array}$ | 2113930F39 | CAP CER CHP 33 PF 50V $5 \%$ |
| $\begin{array}{\|l} \hline \text { C284 thru } \\ \text { C287 } \\ \hline \end{array}$ |  | NOTPLACED |
| $\begin{aligned} & \text { C402 thru } \\ & \text { C406 } \\ & \hline \end{aligned}$ |  | NOTPLACED |
| $\begin{aligned} & \hline \text { C407 thru } \\ & \text { C414 } \end{aligned}$ | 2113932K15 | $\begin{aligned} & \text { CAP CER } 100 \text { UF +80/-20\% } \\ & 16 \mathrm{~V} \end{aligned}$ |
| C415 | $2113743 A 23$ | CAP CHIP . 220 UF 10\% X7R |
| $\begin{array}{\|l} \hline \text { C416 thru } \\ \text { C422 } \\ \hline \end{array}$ | 2113932K15 | CAP CER . 100 UF +80/-20\% 16 V |
| $\begin{aligned} & \text { C423 thru } \\ & \text { C434 } \\ & \hline \end{aligned}$ |  | NOTPLACED |
| C435 | 2113932K15 | $\begin{aligned} & \text { CAP CER } 100 \text { UF +80/-20\% } \\ & 16 \mathrm{~V} \end{aligned}$ |
| $\begin{aligned} & \hline \text { C436 \& } \\ & \text { C437 } \end{aligned}$ |  | NOTPLACED |
| C438 | 2113930F34 | CAP CER CHP 20 PF 50V $5 \%$ |
| C439 | --------- | NOTPLACED |
| C440 | 2113932K15 | $\begin{aligned} & \text { CAP CER } 100 \text { UF }+80 /-20 \% \\ & 16 \mathrm{~V} \end{aligned}$ |
| C441 | -- | NOTPLACED |
| $\begin{aligned} & \hline \text { C442 \& } \\ & \text { C443 } \end{aligned}$ | 2113930F09 | $\begin{aligned} & \text { CAP 1.8 PF } 50 \mathrm{~V}+/-0.1 \mathrm{PF} \\ & 50 \mathrm{~V} \end{aligned}$ |
| $\begin{aligned} & \text { C444 thru } \\ & \text { C448 } \end{aligned}$ | 2113932K15 | $\begin{aligned} & \text { CAP CER } .100 \text { UF }+80 /-20 \% \\ & 16 \mathrm{~V} \end{aligned}$ |
| $\begin{aligned} & \text { C456 thru } \\ & \text { C458 } \end{aligned}$ | 2113932K15 | CAP CER .100 UF $+80 /-20 \%$ 16 V |
| C459 | 2113930F03 | $\begin{aligned} & \text { CAP 1.0 PF 50V +/-0.1 PF } \\ & 50 \mathrm{~V} \end{aligned}$ |
| C460 | --------- | NOTPLACED |
| $\begin{array}{\|l} \hline \text { C461 \& } \\ \text { C462 } \\ \hline \end{array}$ | 2113932K15 | CAP CER .100 UF +80/-20\% 16 V |
| $\begin{aligned} & \text { C463 thru } \\ & \text { C465 } \\ & \hline \end{aligned}$ | --------- | NOTPLACED |
| C466 | 2113932K15 | CAP CER .100 UF $+80 /-20 \%$ 16 V |
| C467 | $2113930 F 24$ | $\begin{aligned} & \mathrm{C} \mathrm{\sim P} 7.5 \mathrm{PF} 50 \mathrm{~V}+/-.25 \mathrm{PF} \\ & 50 \mathrm{~V} \end{aligned}$ |
| C468 | 2113743A23 | CAP CHIP . 220 UF 10\% X7R |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION | ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: |
| C469 |  | NOTPLACED | Q201 thru |  | NOTPLACED |
| C470 | 2311049C07 | CAP TANT CHIP 100UF 10 V | Q203 |  |  |
|  |  | 10\% | Q204 thru | 4805128M12 | TSTR SOT BCW 60B (RH) |
| C471 thru |  | NOTPLACED | Q206 |  |  |
| C475 |  |  | Q207 | 4805718 V 01 | XSTR IC PKG |
| C476 | 2113932K15 | CAP CER . 100 UF +80/-20\% | Q208 | 4805128M12 | TSTR SOT BCW 60B (RH) |
|  |  | 16 V | Q210 | -- | NOTPLACED |
| C478 \& |  | NOTPLACED | Q211 | 4805128M40 | TSTR BCW61BB LH 48G26 |
| C479 |  |  | Q401 \& |  | NOTPLACED |
| C480 | $2113930 F 46$ | CAP CER CHP 62 PF 50V | Q402 |  |  |
| C481 |  | NOTPLACED | R201 thru | 0662057A97 | CHIP RES 100K OHMS 5\% |
| C482 | 2311049A05 | CAP TANT CHIP A/P . 4710 | R203 | 0662057 A 97 | CHIP RES 100 K OHMS 5\% |
|  |  | 25 | R206 | --------- | NOTPLACED |
| C483 | $2113930 F 46$ | CAP CER CHP 62 PF 50V | R207 | 0662057A73 | CHIP RES 10K OHMS 5\% |
|  |  | 5\% | R208 thru |  | NOTPLACED |
| C484 thru |  | NOTPLACED | R210 |  |  |
| C489 |  |  | R212 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| C490 | 2113931 F 17 | CAP CER CHP 470 PF 5\% 50 V | $\begin{aligned} & \text { R213 thru } \\ & \text { R217 } \end{aligned}$ |  | NOTPLACED |
| C491 | 2113931 F 17 | CAP CER CHP 470 PF 5\% | R218 | 0662057A65 | CHIP RES 4700 OHMS 5\% |
|  |  |  | R219 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| CR201 |  | NOTPLACED | R220 | --------- | NOTPLACED |
| CR202 | 4884939C35 | DIODE HOT CAR 39C35 4V | R221 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| CR203 \& |  | NOTPLACED | R222 | 0662057G08 | CHIP RES $82.5 \mathrm{~K} \mathrm{OHMS} 1 \%$ |
| CR204 |  |  | R223 | $0662057 \mathrm{R92}$ | RES CHIP 47.5K.1W 1\% |
| CR401 | 4884939C35 | DIODE HOT CAR $39 C 354 \mathrm{~V}$ | R224 | 0662057A85 | CHIP RES 33K OHMS 5\% |
| CR402 | 4813825 A06 | PIN DIODE 35V | R225 | $0662057 B 47$ | CHIP RES 0 OHMS +-. 050 |
| CR403 |  | NOTPLACED | R225 | 0662057847 | OHMS |
| $\begin{aligned} & \mathrm{J} 201 \& \\ & \mathrm{~J} 401 \end{aligned}$ |  | NOTPLACED | $\begin{aligned} & \text { R226 \& \& } \\ & \text { R227 } \end{aligned}$ | 0662057A97 | CHIP RES 100K OHMS 5\% |
| J501 | 0905516W02 | CONN RECEPTACLE | R228 |  | NOTPLACED |
| J601 \& |  | NOTPLACED | R230 | 0662057A65 | CHIP RES 4700 OHMS 5\% |
| J701 |  |  | R231 | 0662057B22 | CHIP RES 1.0 MEG OHMS |
| J801 | 0913915A11 | RECP SMT LOPRO 25 POS |  | 062057822 | 5\% |
| J901 |  | NOTPLACED | R232 | 0662057A65 | CHIP RES 4700 OHMS 5\% |
| JU201 \& |  | NOTPLACED | R233 | 0662057A85 | CHIP RES 33K OHMS 5\% |
| JU401 |  |  | R234 \& | 0662057A73 | CHIP RES 10K OHMS 5\% |
| JU402 thru <br> JU404 | 0662057B47 | $\begin{aligned} & \text { CHIP RES } 0 \text { OHMS +-. } 050 \\ & \text { OHMS } \end{aligned}$ | R235 |  |  |
| JU405 |  | NOTPLACED | R237 | 0662057A73 | CHIP RES 10K OHMS $5 \%$ |
| JU407 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +-. 050 | $\begin{array}{\|l\|} \hline \mathrm{R} 238 \\ \hline \text { R239 } \end{array}$ | 0662057A97 | CHIP RES 100K OHMS 5\% |
|  |  | OHMS |  | 0662057A65 | CHIP RES 4700 OHMS 5 |
| L201 | 2460578C43 | INDUCTOR CHIP 33.0UH 10\% | $\begin{aligned} & \mathrm{R} 242 \\ & \hline \end{aligned}$ |  |  |
| L203 thru |  | NOTPLACED | $\begin{aligned} & \text { R243 thru } \\ & \text { R246 } \\ & \hline \end{aligned}$ | 0662057A97 | CHIP RES 100K OHMS 5\% |
|  |  |  | R249 | --------- | NOTPLACED |
|  |  | NOTPLACED | R251 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +-. 050 OHMS |
| L401 \& |  | NOTPLACED | R252 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| L403 | 2462587E71 | CHIPINDUCTOR 1800 NH $5 \%$ | $\begin{array}{\|l} \hline \text { R253 thru } \\ \text { R256 } \\ \hline \end{array}$ |  | NOTPLACED |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { R259 \& } \\ & \text { R260 } \\ & \hline \end{aligned}$ | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R261 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R262 | 0662057A89 | CHIP RES 47 K OHMS 5\% |
| R263 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R263 | --------- | NOTPLACED |
| R264 | 0662057A82 | CHIP RES 24K OHMS 5\% |
| R265 \& | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R266 |  |  |
| R267 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R268 | 0662057 A 97 | CHIP RES 100K OHMS 5\% |
| R269 | 0662057A85 | CHIP RES 33K OHMS 5\% |
| R272 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R273 | 0662057 A 97 | CHIP RES 100K OHMS 5\% |
| $\begin{aligned} & \text { R274 thru } \\ & \text { R282 } \end{aligned}$ | 0662057A85 | CHIP RES 33K OHMS 5\% |
| R283 | --------- | NOTPLACED |
| R284 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R285 thru R287 |  | NOTPLACED |
| $\begin{aligned} & \text { R289 thru } \\ & \text { R307 } \end{aligned}$ | --------- | NOTPLACED |
| R402 | 0662057 B 12 | CHIP RES 390K OHMS 5\% |
| R403 | --------- | NOTPLACED |
| R404 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R405 | 0662057B22 | CHIP RES 1.0 MEG OHMS $5 \%$ |
| R406 | --------- | NOTPLACED |
| R407 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R408 | ----- | NOTPLACED |
| R409 thru <br> R414 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R415 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R416 |  | NOTPLACED |
| R417 | 0662057 A 97 | CHIP RES 100K OHMS 5\% |
| R418 | 0662057A49 | CHIP RES 1000 OHMS 5\% |
| R419 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R420 | 0662057 A 97 | CHIP RES 100K OHMS $5 \%$ |
| $\begin{array}{\|l\|} \hline \text { R421 \& } \\ \text { R423 } \end{array}$ |  | NOTPLACED |
| R424 | 0662057A63 | CHIP RES 3900 OHMS 5\% |
| R425 | 0662057A69 | CHIP RES 6800 OHMS 5\% |
| $\begin{array}{\|l} \hline \text { R426 thru } \\ \text { R430 } \end{array}$ |  | NOTPLACED |
| R432 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R433 | 0662057A57 | CHIP RES 2200 OHMS 5\% |
| R434 | --------- | NOTPLACED |
| R435 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R436 thru R438 |  | NOTPLACED |
| $\begin{aligned} & \text { R477 thru } \\ & \text { R480 } \end{aligned}$ |  | NOTPLACED |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R481 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R482 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +-. 050 OHMS |
| R483 thru R485 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| $\begin{aligned} & \text { R490 thru } \\ & \text { R492 } \end{aligned}$ | --------- | NOTPLACED |
| U201 | 5105625 U 3 | IC 32KX8 EE EPROM 28 PIN TSOP |
| U202 | 5105662 U54 | HYBRID $32 \mathrm{~K} \times 8$ SRAM TESTED |
| U204 | 5105662 U52 | HYBRID CC HC11F1 |
| U205 | 5105625 73 | IC 256 K X8 FLS ROM NIN TSOP SD |
| U206 | 5105835 U 0 | CC SLIC5 GLOBTOP DIE 91W30 86P |
| U208 | --------- | NOTPLACED |
| U210 | 5105625 U 73 | IC 256K X8 FLS ROM NIN TSOP SD |
| U211 | 5105279V65 | IC |
| U401 | --------- | NOTPLACED |
| $\begin{array}{\|l} \hline \text { U402 \& } \\ \text { U403 } \\ \hline \end{array}$ | 5105329 V 12 | HYBRID CC BUMPED TEST 5596E99 |
| U404 | 5105625 U 73 | IC 256K X8 FLS ROM NIN TSOP SD |
| U405 | 5105457W70 | CC OMPAC DSP56001A |
| U406 | 5105457W19 | CC ADSIC ABACUS/DSP SUP/OMPAC |
| U407 | 5105492X73 | IC 4.2V VOLTAGE DETECTOR |
| U408 thru <br> U412 |  | NOTPLACED |
| U414 | 5105329 V 12 | HYBRID CC BUMPED TEST $5596 E 99$ |
| U415 | 5105492X05 | IC SNG HI SPD L-MOS OR GATE |
| $\begin{array}{\|l} \hline \text { VR201 thru } \\ \text { VR203 } \end{array}$ | 4813830A15 | DIODE 5.6V 5\% 225MW MMBZ5232B |
| VR204 \& VR205 |  | NOTPLACED |
| VR206 | 4813830A31 | DIODE 18V 5\% 225MW MMBZ5248B |
| VR207 | 4813830A22 | DIODE 9.1V 5\% 225MW MMBZ5239B |
| VR208 | 4813830A24 | DIODE 11V 5\% 225MW MMBZ5241B |
| VR209 | 4813830A15 | DIODE 5.6V 5\% 225MW MMBZ5232B |
| VR210 | --------- | NOTPLACED |
| VR211 thru VR214 | 4813830A15 | DIODE 5.6V 5\% 225MW MMBZ5232B |
| VR215 thru VR223 | --------- | NOTPLACED |


| ITEM | MOTOROLA PART <br> NUMBER | DESCRIPTION |
| :--- | :--- | :--- |
| Y201 | 4805574W01 | XTAL PRL RESONANCE <br> 7.3228 MHZ |
| Y401 | $4805573 \mathrm{W01}$ | CRYSTAL SERIAL <br> RESONANCE |



HLN6458E VOCON Board Schematic (Sheet 1 of 2)




U405 - DSP


U406-ADSIC

HLN6458E VOCON Board Component Location Diagrams (Sheet 1 of 2)


HLN6458E VOCON Board Component Location Diagrams (Sheet 2 of 2)

| HLN6458E VOCON Board Electrical Parts List |  |  | ITEM | MOTOROLA PART NUMBER | DESCRIPTION | ITEM | MOTOROLA PART NUMBER | DESCRIPTION | ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ITEM | MOTOROLA | DESCRIPTION | C467 | 2113930F24 | 7．5pF | Q208 | 4805128M12 | NPN | R270， 271 | 0662057A97 | 100k |
|  |  |  | C468 | 2113743A23 | $0.22 \mu \mathrm{~F}$ | Q210 | －－－－－ | Not Placed | R272 | 0662057A73 | 10k |
|  |  | CAPACITOR，Fixed： unless otherwise stated | C469 | －－－－－ | Not Placed | Q211 | 4805128M40 | PNP | R273 | 0662057A97 | 100k |
|  |  |  | C470 | 2311049 C 07 | 100 F | Q401， 402 | －－－－－ | Not Placed | R274 thru 282 | 0662057A85 | 33k |
| C201， 202 | －－－－－ | Not Placed | C471 thru 475 | －－－－－ | Not Placed |  |  |  | R283 | －－－－－ | Not Placed |
| C203， 204 | 2113930F39 | 33pF | C476 | 2113932K15 | 0.14 F |  |  | RESISTOR：$W \pm 5 \%$ ；1／8W | R284 | 0662057 A97 | 100k |
| C205 thru 213 | －－－－－ | Not Placed | C478， 479 | －－－－－ | Not Placed |  |  | unless otherwise stated | R285 thru 287 | －－－－－ | Not Placed |
| C214 | 2113931 F 17 | 470pF | C480 | 2113930F46 | 62pF | R200 | 0662057A65 | 4.7 k | R288 | 0662057A97 | 100k |
| C215 | 2113930F39 | 33pF | C481 | －－－－－ | Not Placed | R201 thru 205 | 0662057 A97 | 100k | R289 thru 307 | －－－ | Not Placed |
| C216 | －－－－－ | Not Placed | C482 | 2311049A05 | 0．47 ${ }^{\text {F }}$ | R206 | －－－－－ | Not Placed | R402 | 0662057B12 | 390k |
| C217 | 2113932K15 | 0．1险 | C483 | 2113930F39 | 33 pF | R207 | 0662057A73 | 10k | R403 | －－－－－ | Not Placed |
| C218 | －－－－ | Not Placed | C484 thru 489 | －－－－－ | Not Placed | R208 thru 210 | －－－－－ | Not Placed | R404 | 0662057A73 | 10k |
| C219 | $2113931 F 17$ | 470pF | C490， 491 | 2113931 F 17 | 470pF | R211 | 0662057A97 | 100k | R405 | 0662057822 | 1M |
| C220， 221 | －－ | Not Placed | C495 | －－－－－ | Not Placed | R212 | 0662057A73 | 10k | R406 | －－－－－ | Not Placed |
| C222， 223 | 2113931 F 17 | 470pF |  |  |  | R213 thru 217 | －－－－－ | Not Placed | R407 | 0662057 A97 | 100k |
| C224 thru 226 | 2113932K15 | 0．1浐 |  |  | DIODE：See Note 1. | R218 | 0662057A65 | 4.7 k | R408 | －．－－－ | Not Placed |
| C227 | 2113930F36 | 24pF | CR201 | －－－－－ | Not Placed | R219 | 0662057 A97 | 100k | R409 thru 411 | 0662057 A 73 | 10k |
| C228 | －－－－ | Not Placed | CR202 | 4884939 C 35 | Hot Carrier，4V | R220 | －－－－－ | Not Placed | R413， 414 | 0662057A73 | 10k |
| C229 | $2113930 F 36$ | 24pF | CR203， 204 |  | Not Placed | R221 | 0662057 A 3 | 10k | R412 | 0662057A65 | 4．7k |
| C230， 231 | 2113932K15 | 0．1瑗 | CR205 | 4805212N57 | Dual Diode | R222 | 0662057G08 | 82．5k | R415 | 0662057A97 | 100k |
| C236 | 2113932K15 | 0.14 F | CR402 | 4813825 A06 | PIN，35V | R223 | $0662057 \mathrm{R92}$ | 47．5k | R416 | －－－－－ | Not Placed |
| C237 | 2113931 F17 | 470pF | CR403 | －－－－－ | Not Placed | R224 | 0662057A85 | 33k | R417 | 0662057A97 | 100k |
| C238 | 2113932K15 | 0．1年 |  |  |  | R225 | 0662057847 | 0 | R418 | 0662057A49 | 1k |
| C241 | 2113931 F17 | 470pF |  |  | JACK： | R226， 227 | 0662057A97 | 100k | R419 | 0662057A73 | 10k |
| C245 thru 247 | 2113932K15 | 0.14 F | J201 | －－－－－ | Not Placed | R228 | －－－－－ | Not Placed | R420 | 0662057A97 | 100k |
| C248 | $2113931 F 17$ | 470pF | J401 <br> J501 |  | Not Placed | R229 | $0662057 A 97$ | 100k | R421 thru 423 | －－－－－ | Not Placed |
| C249 thru 257 | －－－－－ | Not Placed | J501 | $\frac{0905516 \mathrm{~W} 02}{----}$ | Connector，Receptacle | R230 | 0662057A65 | 4.7 k | R424 | 0662057A63 | 3.9 k |
| C258 | 2113930 F 39 | 33pF | J601 | －－－－－－ | Not Placed | R231 | 0662057822 | 1M | R425 | 0662057A69 | 6.8 k |
| C261 | 2113932K15 | 0．1险 | J701 |  | Not Placed | R232 | 0662057A65 | 4.7 k | R426 thru 431 | －－－－－ | Not Placed |
| C262 thru 269 | －－－－ | Not Placed | J801 | 0913915A11 | Connector，25－Position | R233 | 0662057A85 | 33k | R432 | 0662057A73 | 10k |
| C270 | 2113931 F 17 | 470pF | J901 |  | Not Placed | R234， 235 | 0662057A73 | 10k | R433 | 0662057A57 | 2.2 k |
| C271 thru 279 | －－－－ | Not Placed |  |  | JUMPER： | R236 | 0662057A97 | 100k | R434 | －－－－－ | Not Placed |
| C281， 282 | 2113930F39 | 33pF |  |  | Not Placed | R237 | 0662057A73 | 10k | R435 | 0662057A73 | 10k |
| C284 thru 287 | －－－－－ | Not Placed | JU401 | －－－－－－ | Not Placed | R238 | 0662057 A97 | 100k | R436 thru 438 | －－－－－ | Not Placed |
| C402 thru 406 |  | Not Placed | JU402 thru 404 | 0662057847 | 0 Ohm | R239 | 0662057A65 | 4.7 k | R444，445 | －－－－－ | Not Placed |
| C407 thru 414 | 2113932K15 | 0．14F | JU405 | 0662057847 | Not Placed | R240 thru 242 | －－－－－ | Not Placed | R448， 449 | －－－－ | Not Placed |
| C415 | 2113743A23 | $0.22 \mu \mathrm{~F}$ | JU407， 408 | $0662057 \mathrm{B47}$ | 0 Ohm | R243 thru 248 | 0662057 A97 | 100k | R450 | 0662057A89 | 47k |
| C416 thru 422 | 2113932K15 | $0.1 \mu \mathrm{~F}$ | JJU409 | 0662057847 | Not Placed | R249 | －－．－－ | Not Placed | R477 thru 480 | －－－－－ | Not Placed |
| C423 thru 434 | －－－－－ | Not Placed | JU410， 411 |  | Not Placed | R250 | 0662057A73 | 10k | R481 | 0662057 A97 | 100k |
| C435 | 2113932K15 | 0．1险 | JU410， 411 | －－ | Not Placed | R251 | 0662057847 | 0 | R482 | 0662057847 | 0 |
| C436， 437 | －－－－ | Not Placed |  |  | COIL，RF：unless otherwise stated | R252 | 0662057A97 | 100k | R483 thru 485 | 0662057 A97 | 100k |
| C438 | 2113930F34 | 20pF | L201 | 2460578 C 43 | COIL，RF：unless oinerwise stated | R253 thru 256 | －－－－－ | Not Placed | R490 thru 492 | －－－－－ | Not Placed |
| C439 | －－－－ | Not Placed | L203 thru 206 | 2460578043 | Not Placed | R257 thru 260 | 0662057 A97 | 100k |  |  | INTEGRATED CIRCUIT MODULE：See Note 1. |
| C440 | 2113932K15 | 0.14 F | L208 thru 223 | － | Not Placed | R261 | 0662057A73 | 10k |  |  |  |
| C441 | －－－－－ | Not Placed | L401， 402 |  | Not Placed | R262 | 0662057A89 | 47k | U201 | 5105625 33 | 32k $\times 8$ EEPROM |
| C442， 443 | 2113930F09 | 1．8pF | L403 | 2462587 E71 | 1.8 HH | R263 | 0662057A73 | 10k | U202 | 5105662 L 4 | SRAM（Static RAM） |
| C444 thru 448 | 2113932K15 | 0．1瑗 |  | 2462587 E7 |  | R264 | 0662057A82 | 24k | U204 | 5113802A33 | MCU（Microcontrol Unit－Type |
| C456 thru 458 | 2113932K15 | 0．1阬 |  |  | TRANSISTOR：See Note 1. | R265， 266 | 0662057A97 | 100k |  |  | MC68HC11） |
| C459 | 2113930 F03 | 1pF | Q201 thru 203 | －－－－－ | Not Placed | R267 | 0662057A73 | 10k | U205 | $5105109 Z 15$ | FLASH（TSOP） |
| C460 | $\frac{\cdots---1}{\text { 2113932K15 }}$ | Not Placed | Q204 thru 206 | 4805128M12 | NPN | R268 | 0662057A97 | 100k | U206 | $5105835 \cup 80$ | SLIC（Support－Logic IC） |
| C461， 462 | 2113932K15 | 0．14F | Q207 | 4805718 V 01 | PNP | R269 | 0662057A85 | 33k | U208 | －－－－－ | Not Placed |
| C463 thru 466 | －－－－－ | Not Placed |  |  |  |  |  |  |  |  |  |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| U211 | 5105279V65 | AND Gate |
| U214 | 5105279V65 | AND Gate |
| U401 | ----- | Not Placed |
| U402, 403 | 5105329V12 | $8 \mathrm{x} \times 24$ DSPRAM |
| U404 | 5105625073 | FLASH (TSOP) |
| U405 | 5105457W70 | Digital Signal Processor |
| U406 | 5105457W19 | ADSIC (ABACUS/DSP Support IC) |
| U407 | 5105492X73 | Voltage Detector XSX |
| U408 thru 412 | ----- | Not Placed |
| U414 | 5105329 V 12 | $8 \mathrm{k} \times 24$ DSPRAM |
| U415 | $5105492 \times 05$ | OR Gate |
| 0416 | ----- | Not Placed |
|  |  | DIODE: See Note 1. |
| VR201 thru 203 | 4813830A15 | Zener, 5.6V |
| VR204, 205 | ---- | Not Placed |
| VR206 | 4813830A31 | Zener, 18V |
| VR207 | 4813830A22 | Zener, 9.1V |
| VR208 | 4813830A24 | Zener, 11V |
| VR209 | 4813830A15 | Zener, 5.6V |
| VR210 | ----- | Not Placed |
| VR211 thru 214 | 4813830A15 | Zener, 5.6V |
| VR215 thru 223 | ----- | Not Placed |
|  |  |  |
|  |  | CRYSTAL: See Note 2. |
| Y201 | 4805574W01 | 7.3728 MHz |
| Y401 | 4805573W01 | 33 MHz |
|  |  | MISCELLANEOUS: |
|  | 8405160Y02 | Printed Circuit Board |

Notes

1. For optimum performance, order replacement diodes, transistors, and circuit
2. When ordering crystals, specify carrier frequency, crystal frequency, crystal
type number, and Motororla part number
3. Part value notations
$\mathrm{p}=10^{-12}$
$\mathrm{n}=10^{-9}$
$\mathrm{n}=\mathrm{c}^{\mu=10^{-6}}$
$\mathrm{~m}=10^{-3}$
$m=10^{-3}$
$k=10^{3}$
$M=10^{6}$
4. ITEM refers to the component reference designator. SIDE refers to the loca
5. $\begin{aligned} & \text { tion referes to the component reference designator. SIDE } \\ & \text { then }\end{aligned}$
6. The VOCON Board Kit uses a 6 -layer printed circuit board.




HLN6458F/G VOCON Board Schematic (Sheet 1 of 2)


HLN6458F/G VOCON Board Schematic (Sheet 2 of 2)


U206-SLIC


U405 - DSP


U406-ADSIC


HLN6458F/G VOCON Board Component Location Diagrams (Sheet 2 of 2)

| HLN6458F/G VOCON Board Parts List |  |  |
| :---: | :---: | :---: |
| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| $\begin{aligned} & \hline \text { C201 \& } \\ & \text { C202 } \end{aligned}$ |  | NOTPLACED |
| $\begin{aligned} & \hline \text { C203 \& } \\ & \text { C204 } \\ & \hline \end{aligned}$ | 2113930F39 | CAP CER CHP 33 PF 50V 5\% |
| $\begin{aligned} & \text { C205 thru } \\ & \text { C213 } \end{aligned}$ |  | NOTPLACED |
| C214 | 2113931 F 17 | CAP CER CHP 470 PF 5\% 50 V |
| C215 | $2113930 F 39$ | CAP CER CHP 33 PF 50V 5\% |
| C216 | --------- | NOTPLACED |
| C217 | 2113932K15 | ${ }_{16 \mathrm{~V}}^{\text {CAP CER } . ~} 100$ UF +80/-20\% |
| C218 | ---------- | NOTPLACED |
| C219 | 2113931 F 17 | CAP CER CHP 470 PF 5\% 50 V |
| $\begin{aligned} & \mathrm{C} 220 \& \& \\ & \mathrm{C} 222 \end{aligned}$ |  | NOTPLACED |
| $\begin{aligned} & \mathrm{C} 222 \text { \& } \\ & \mathrm{C} 223 \end{aligned}$ | 2113931 F 17 | CAP CER CHP 470 PF 5\% 50 V |
| $\begin{aligned} & \text { C224 thru } \\ & \text { C226 } \end{aligned}$ | 2113932K15 | CAP CER .100 UF +80/-20\% 16 V |
| C227 | $2113930 F 36$ | CAP CER CHP 24 PF 50V 5\% |
| C228 | --------- | NOTPLACED |
| C229 | $2113930 F 36$ | CAP CER CHP 24 PF 50V 5\% |
| $\begin{aligned} & \hline \text { C230 \& } \\ & \text { C231 } \end{aligned}$ | 2113932K15 | $\begin{aligned} & \text { CAP CER . } 100 \text { UF }+80 /-20 \% \\ & 16 \mathrm{~V} \end{aligned}$ |
| C236 | 2113932K15 | $\begin{aligned} & \text { CAP CER } 100 \text { UF +80/-20\% } \\ & 16 \mathrm{~V} \end{aligned}$ |
| C237 | 2113931 F 17 | $\begin{aligned} & \text { CAP CER CHP } 470 \text { PF 5\% } \\ & 50 \mathrm{~V} \end{aligned}$ |
| C238 | 2113932K15 | ${ }_{16 \mathrm{~V}}^{\text {CAP CER } . ~} 100$ UF +80/-20\% |
| C241 | 2113931 17 | CAP CER CHP 470 PF 5\% 50 V |
| $\begin{aligned} & \text { C245 thru } \\ & \text { C247 } \\ & \hline \end{aligned}$ | 2113932K15 | CAP CER .100 UF +80/-20\% 16 V |
| C248 | 2113931 F17 | CAP CER CHP 470 PF 5\% 50 V |
| $\begin{aligned} & \text { C249 thru } \\ & \text { C257 } \end{aligned}$ |  | NOTPLACED |
| C258 | $2113930 F 39$ | CAP CER CHP 33 PF 50V 5\% |
| C261 | 2113932K15 | ${ }^{\text {CAP CER }} 100$ UF +80/-20\% |
| $\begin{aligned} & \text { C262 thru } \\ & \text { C264 } \\ & \hline \end{aligned}$ | --------- | NOTPLACED |
| C264 | 2113931 17 | CAP CER CHP 470 PF 5\% 50 V |
| $\begin{array}{\|l} \hline \text { C265 thru } \\ \text { C268 } \\ \hline \end{array}$ |  | NOTPLACED |
| C268 | 2113931 17 | $\begin{aligned} & \text { CAP CER CHP } 470 \text { PF 5\% } \\ & 50 \mathrm{~V} \end{aligned}$ |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION | ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: |
| C269 | --------- | NOTPLACED | $\begin{aligned} & \hline \text { C471 thru } \\ & \text { C475 } \\ & \hline \end{aligned}$ |  | NOTPLACED |
| C269 \& | 2113931 F 17 | CAP CER CHP 470 PF 5\% |  |  |  |
| C270 |  | 50 V | C476 | 2113932K15 | $\begin{aligned} & \text { CAP CER . } 100 \text { UF +80/-20\% } \\ & 16 \mathrm{~V} \end{aligned}$ |
| C271 thru |  | NOTPLACED |  |  |  |
| C273 |  |  | $\begin{aligned} & \hline \text { C478 \& } \\ & \text { C479 } \\ & \hline \end{aligned}$ |  | NOTPLACED |
| C273 | 2113931 F 17 | $\begin{aligned} & \text { CAP CER CHP } 470 \text { PF 5\% } \\ & 50 \mathrm{~V} \end{aligned}$ |  |  |  |
|  |  |  | C480 | 2113930F46 | CAP CER CHP 62 PF 50V 5\% |
| $\begin{array}{\|l\|} \hline \text { C274 thru } \\ \text { C279 } \\ \hline \end{array}$ | ----- | NOTPLACED | $\begin{array}{\|l\|} \hline \text { C481 } \\ \hline \text { C482 } \\ \hline \end{array}$ | $\begin{array}{\|l\|} \hline---------- \\ \hline 2311049 A 05 \end{array}$ | NOTPLACED |
|  |  |  |  |  | CAP TANT CHIP A/P .471025 |
| $\begin{array}{\|l} \hline \text { C281 \& } \\ \text { C282 } \\ \hline \end{array}$ | $2113930 F 39$ | CAP CER CHP 33 PF 50V 5\% |  |  |  |
|  |  |  | C483 | $2113930 F 39$ | CAP CER CHP 33 PF 50V 5\% |
| $\begin{array}{\|l} \hline \text { C284 thru } \\ \text { C287 } \\ \hline \end{array}$ |  | NOTPLACED | C484 thru C489 | --------- | NOTPLACED |
| $\begin{aligned} & \text { C402 thru } \\ & \text { C406 } \end{aligned}$ |  | NOTPLACED | $\begin{aligned} & \text { C490 \& } \\ & \text { C491 } \end{aligned}$ | 2113931 F17 | CAP CER CHP 470 PF 5\% 50 V |
| C407 thru | 2113932 K 15 | $\begin{aligned} & \text { CAP CER . } 100 \text { UF +80/-20\% } \\ & 16 \mathrm{~V} \end{aligned}$ | CR201 | ------ |  |
| C414 |  |  | CR202 | 4884939C35 | DIODE HOT CAR 39C35 4V |
| C415 | 2113743A23 | CAP CHIP . 220 UF 10\% X7R | $\begin{aligned} & \hline \text { CR203 \& } \\ & \text { CR204 } \\ & \hline \end{aligned}$ |  | NOTPLACED |
| C416 thru | 2113932K15 | $\begin{aligned} & \text { CAP CER . } 100 \text { UF +80/-20\% } \\ & 16 \mathrm{~V} \\ & \hline \end{aligned}$ |  |  |  |
| C420 |  |  | CR205 | 4805218N57 | DIODE DUAL |
| $\begin{array}{\|l} \hline \text { C422 thru } \\ \text { C434 } \\ \hline \end{array}$ | --------- | NOTPLACED | CR402 | 4813825A06 | PIN DIODE 35V |
|  |  |  | $\begin{array}{\|l\|} \hline \text { CR403 } \\ \hline \text { J201 \& J401 } \\ \hline \end{array}$ | -- | NOTPLACED |
| C435 | 2113932 K 15 | $\begin{aligned} & \text { CAP CER . } 100 \text { UF +80/-20\% } \\ & 16 \mathrm{~V} \end{aligned}$ |  |  | NOTPLACED |
|  |  |  | J501 | 0905516W02 | CONN RECEPTACLE |
| $\begin{aligned} & \hline \text { C436 \& } \\ & \text { C437 } \\ & \hline \end{aligned}$ |  | NOTPLACED | J601 \& J701 <br> J801 | --- |  |
|  |  |  |  | ----------- | RECP SMT LOPRO 25 POS |
| C439 | 2113930F34 | CAP CER CHP 20 PF 50V 5\% | $\begin{array}{\|l\|l\|} \hline 1001 \\ \hline 1101 \end{array}$ |  | NOTPLACED |
| C440 | 2113932K15 | NOTPLACED |  | --------- | NOTPLACED |
|  |  | $\begin{aligned} & \text { CAP CER . } 100 \text { UF +80/-20\% } \\ & 16 \mathrm{~V} \end{aligned}$ | JU201 |  | CHIP RES 0 OHMS +-.050 OHMS |
| C441 | --------- | $\begin{aligned} & \text { NOTPLACED } \\ & \hline \text { CAP 1.8 PF } 50 \mathrm{~V}+/-0.1 \mathrm{PF} \\ & 50 \mathrm{~V} \end{aligned}$ | $\begin{array}{\|l\|} \hline J U 203 \\ \hline J U 204 \\ \hline \end{array}$ | 0662057B47 | NOTPLACED <br> CHIP RES 0 OHMS +-. 050 OHMS |
| $\begin{aligned} & \hline \text { C442 \& } \\ & \text { C443 } \end{aligned}$ |  |  |  |  |  |
| C444 thru | 2113932K15 | $\begin{aligned} & \text { CAP CER } 100 \text { UF }+80 /-20 \% \\ & 16 \mathrm{~V} \end{aligned}$ | JU401 <br> JU402 thru JU404 |  |  |
| C448 |  |  |  |  | NOTPLACED <br> CHIP RES 0 OHMS +.. 050 |
| C457 | 2113931 F 13 | CAP CER CHP 330 PF 5\% 50 V |  | 0662057B47 | OHMS |
| C458 | 2113932K15 | $\begin{aligned} & \text { CAP CER . } 100 \text { UF +80/-20\% } \\ & 16 \mathrm{~V} \end{aligned}$ | $\begin{array}{\|l\|l\|l\|} \hline \text { JU405 } \\ \hline \text { JU407 } \end{array}$ | 0662057B47 | $\begin{aligned} & \text { NOTPLACED } \\ & \begin{array}{l} \text { CHIP RES } 0 \text { OHMS +-. } 050 \\ \text { OHMS } \end{array} \\ & \hline \end{aligned}$ |
|  |  |  |  |  |  |
| C459 | $2113930 F 03$ | $\begin{aligned} & \text { CAP 1.0 PF 50V +/-0.1 PF } \\ & 50 \mathrm{~V} \end{aligned}$ | L201 | 2460578C43 | INDUCTOR CHIP 33.0UH 10\% |
| C460 | --------- | NOTPLACED <br> CAP CER . 100 UF +80/-20\% 16V | $\begin{aligned} & \hline \text { L203 thru } \\ & \text { L206 } \end{aligned}$ |  | NOTPLACED |
| C461 \& | 2113932K15 |  |  |  |  |
| C463 thru | --------- | NOTPLACED | $\begin{array}{\|l} \mathrm{L} 208 \text { thru } \\ \mathrm{L} 223 \end{array}$ |  | NOTPLACED |
| C465 |  |  | $\begin{array}{\|l\|l\|l\|} \hline L 401 ~ \& ~ L 402 ~ \\ \hline L 403 \end{array}$ | 2462587E71 | NOTPLACED |
| C467 | $2113930 F 24$ | $\begin{aligned} & \text { C~P } 7.5 \mathrm{PF} 50 \mathrm{~V}+/-.25 \mathrm{PF} \\ & 50 \mathrm{~V} \end{aligned}$ |  |  | CHIPINDUCTOR 1800 NH |
| C468 | 2113743A23 |  | $\begin{aligned} & \text { Q201 thru } \\ & \text { Q203 } \end{aligned}$ |  | NOTPLACED |
| C469 |  | CAP CHIP . 220 UF 10\% X7R <br> NOTPLAED <br> CAP TANT CHIP 100UF 10V <br> $10 \%$ |  |  |  |
| C470 | 2311049C07 | CAP TANT CHIP 100 UF 10V$10 \%$ | $\begin{aligned} & \text { Q204 thru } \\ & \text { Q206 } \\ & \hline \text { Q207 } \\ & \hline \end{aligned}$ | 4805128M12 | TSTR SOT BCW 60B (RH) |
|  |  |  |  | 4805718V01 | XSTR IC PKG |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| Q208 | 4805128M12 | TSTR SOT BCW 60B (RH) |
| Q210 | --------- | NOTPLACED |
| Q211 | 4805128M40 | TSTR BCW61BB LH 48G26 |
| $\begin{aligned} & \text { Q401 \& } \\ & \text { Q402 } \end{aligned}$ |  | NOTPLACED |
| R199 | --------- | NOTPLACED |
| R200 | 0662057A65 | CHIP RES 4700 OHMS 5\% |
| $\begin{aligned} & \text { R201 thru } \\ & \text { R205 } \end{aligned}$ | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R206 | --------- | NOTPLACED |
| R207 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| $\begin{array}{\|l} \hline \text { R208 thru } \\ \text { R210 } \\ \hline \end{array}$ |  | NOTPLACED |
| R211 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R212 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R213 thru | --------- | NOTPLACED |
| R218 | 0662057A65 | CHIP RES 4700 OHMS 5\% |
| R219 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R220 | --------- | NOTPLACED |
| R221 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R222 | 0662057G08 | CHIP RES 82.5 K OHMS 1\% |
| R223 | 0662057R92 | RES CHIP 47.5K.1W 1\% |
| R224 | 0662057A85 | CHIP RES 33K OHMS 5\% |
| R225 | 0662057B47 | CHIP RES 0 OHMS +-. 050 OHMS |
| R226 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R227 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R228 | --------- | NOTPLACED |
| R229 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R230 | 0662057A65 | CHIP RES 4700 OHMS 5\% |
| R231 | 0662057B22 | CHIP RES 1.0 MEG OHMS 5\% |
| R232 | 0662057A65 | CHIP RES 4700 OHMS 5\% |
| R233 | 0662057A85 | CHIP RES 33K OHMS 5\% |
| $\begin{aligned} & \text { R234 \& } \\ & \text { R235 } \end{aligned}$ | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R236 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R237 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R238 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R239 | 0662057A65 | CHIP RES 4700 OHMS 5\% |
| $\begin{aligned} & \hline \text { R240 thru } \\ & \text { R242 } \\ & \hline \end{aligned}$ |  | NOTPLACED |
| $\begin{aligned} & \text { R243 thru } \\ & \text { R248 } \end{aligned}$ | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R249 | --------- | NOTPLACED |
| R250 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R251 | 0662057B47 | CHIP RES 0 OHMS +-. 050 OHMS |
| R252 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| $\begin{aligned} & \text { R253 thru } \\ & \text { R256 } \\ & \hline \end{aligned}$ |  | NOTPLACED |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{array}{\|l\|} \hline \text { R257 thru } \\ \text { R260 } \\ \hline \end{array}$ | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R261 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R262 | 0662057A89 | CHIP RES 47K OHMS 5\% |
| R263 | 0662057A73 | CHIP RES 10K OHMS 5\% ${ }^{1}$ NOTPLACED ${ }^{2}$ |
| R264 | 0662057A82 | CHIP RES 24K OHMS 5\% |
| $\begin{array}{\|l\|} \hline \text { R265 \& } \\ \text { R266 } \end{array}$ | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R267 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R268 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R269 | 0662057A85 | CHIP RES 33K OHMS 5\% |
| R270 | 0662057 A 97 | CHIP RES 100K OHMS 5\% |
| R271 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R272 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R273 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R274 | 0662057A85 | CHIP RES 33K OHMS $5 \%{ }^{1}$ NOTPLACED ${ }^{2}$ |
| R275 | 0662057A85 | CHIP RES 33K OHMS 5\% |
| R276 | 0662057A85 | CHIP RES 33K OHMS 5\% |
| R277 | 0662057A85 | CHIP RES 33K OHMS 5\% |
| R278 | 0662057A85 | CHIP RES 33K OHMS 5\% |
| R279 | 0662057A85 | CHIP RES 33K OHMS 5\% |
| R280 | 0662057A85 | CHIP RES 33K OHMS 5\% |
| R281 | 0662057A85 | CHIP RES 33K OHMS 5\% |
| R282 | 0662057A85 | CHIP RES 33K OHMS 5\% |
| R283 | NOTPLACED | 64AM DUMMY PART NUMBER |
| R284 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R285 | NOTPLACED | 64AM DUMMY PART NUMBER |
| R286 | NOTPLACED | 64AM DUMMY PART NUMBER |
| R287 | NOTPLACED | 64AM DUMMY PART NUMBER |
| R288 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R289 | NOTPLACED | 64AM DUMMY PART NUMBER |
| R290 | NOTPLACED | 64AM DUMMY PART NUMBER |
| R291 | NOTPLACED | 64AM DUMMY PART NUMBER |
| R292 | NOTPLACED | 64AM DUMMY PART NUMBER |
| R293 | NOTPLACED | 64AM DUMMY PART NUMBER |
| R294 | NOTPLACED | 64AM DUMMY PART NUMBER |
| R295 | NOTPLACED | 64AM DUMMY PART NUMBER |
| $\begin{aligned} & \text { R296 thru } \\ & \text { R307 } \end{aligned}$ |  | NOTPLACED |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R308 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +-. 050 OHMS |
| R402 | 0662057 B 12 | CHIP RES 390K OHMS 5\% |
| R403 |  | NOTPLACED |
| R404 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R405 | 0662057B22 | CHIP RES 1.0 MEG OHMS 5\% |
| R406 |  | NOTPLACED |
| R407 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R408 |  | NOTPLACED |
| R409 thru <br> R411 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R412 | 0662057A65 | CHIP RES 4700 OHMS 5\% |
|  <br> R414 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R415 | 0662057 A 97 | CHIP RES 100K OHMS 5\% |
| R416 | --------- | NOTPLACED |
| R417 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R418 | 0662057A49 | CHIP RES 1000 OHMS 5\% |
| R419 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| R420 | 0662057 A 97 | CHIP RES 100K OHMS 5\% |
| R421 thru | --------- | NOTPLACED |
| R424 | 0662057A63 | CHIP RES 3900 OHMS 5\% |
| R425 | 0662057A69 | CHIP RES 6800 OHMS 5\% |
| R426 thru R431 |  | NOTPLACED |
| R432 | 0662057 A 73 | CHIP RES 10K OHMS 5\% |
| R433 | 0662057A57 | CHIP RES 2200 OHMS 5\% |
| R434 | --------- | NOTPLACED |
| R435 | 0662057A73 | CHIP RES 10K OHMS 5\% |
| $\begin{array}{\|l} \hline \text { R436 thru } \\ \text { R438 } \\ \hline \end{array}$ |  | NOTPLACED |
| R450 | 0662057A89 | CHIP RES 47K OHMS 5\% |
| $\begin{aligned} & \begin{array}{l} \text { R459 \& } \\ \text { R460 } \end{array} \\ & \hline \end{aligned}$ | 0662057A73 | CHIP RES 10K OHMS 5\% |
| $\begin{array}{\|l} \hline \text { R477 thru } \\ \text { R480 } \\ \hline \end{array}$ |  | NOTPLACED |
| R481 | 0662057A97 | CHIP RES 100K OHMS 5\% |
| R482 | $0662057 B 47$ | CHIP RES 0 OHMS +-. 050 OHMS |
| $\begin{array}{\|l} \hline \text { R483 thru } \\ \text { R485 } \\ \hline \end{array}$ | 0662057 A97 | CHIP RES 100K OHMS 5\% |
| $\begin{array}{\|l} \hline \text { R490 thru } \\ \text { R492 } \\ \hline \end{array}$ |  | NOTPLACED |
| U201 | $5105109 Z 72$ | IC 32X8 EEPROM 28 PIN TSOP |
| U202 | 5185963A21 5185748L01 | IC, $32 \mathrm{~K} \times 8$ SRAM ${ }^{1}$ 32 K X 8 SRAM 28 PIN TSOP 2 |
| U204 | 5113802A75 | HC11F1 MICROPROCESSOR (TAPE/ REEL) |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| U205 | 5185963A84 | IC 512K X 16/1M X 8 SMART VLTG |
| U206 | 5105835 U 80 | CC SLIC5 GLOBTOP DIE 91W30 86P |
| U208 |  | NOTPLACED |
| $\begin{aligned} & \begin{array}{l} \text { U211 \& } \\ \text { U214 } \end{array} \\ & \hline \end{aligned}$ | 5105279V65 | IC |
| $\begin{aligned} & \hline \text { U215 \& } \\ & \text { U216 } \\ & \hline \end{aligned}$ |  | NOTPLACED |
| U401 |  | NOTPLACED |
| U402 | $\begin{aligned} & 5105462 \mathrm{G} 87 \\ & 5185963 \mathrm{~A} 18 \end{aligned}$ | $\begin{aligned} & \text { IC 32K X } 8 \text { LW PWR SRAM } \\ & \text { TSOP } 1 \\ & \text { IC 32K X } 8 \text { LW PWR SRAM } \\ & \text { TSOP 2 } \end{aligned}$ |
| U403 | $\begin{aligned} & 5105462 \mathrm{G} 87 \\ & 5185963 \mathrm{~A} 18 \end{aligned}$ | ```IC 32K X 8 LW PWR SRAM TSOP }\mp@subsup{}{}{1 IC 32K X 8 LW PWR SRAM TSOP 2``` |
| U404 | 5105625 U 73 | IC 256K X8 FLS ROM NIN TSOP SD |
| U405 | 5105457W70 | CC OMPAC DSP56001A |
| U406 | 5105457W19 | CC ADSIC ABACUS/DSP SUP/OMPAC |
| U407 | $5105492 \times 73$ | IC 4.2V VOLTAGE DETECTOR |
| $\begin{aligned} & \text { U408 thru } \\ & \text { U412 } \\ & \hline \end{aligned}$ |  | NOTPLACED |
| U414 | 5105462G87 <br> 518963A18 | IC 32K X 8 LW PWR SRAM TSOP ${ }^{1}$ <br> IC 32K X 8 LW PWR SRAM TSOP ${ }^{2}$ |
| U417 | --------- | NOTPLACED |
| $\begin{array}{\|l} \hline \text { VR201 thru } \\ \text { VR203 } \\ \hline \end{array}$ | 4813830A15 | DIODE 5.6V 5\% 225MW MMBZ5232B |
|  <br> VR205 |  | NOTPLACED |
| VR206 | 4813830A31 | DIODE 18V 5\% 225MW MMBZ5248B |
| VR207 | 4813830A22 | DIODE 9.1V 5\% 225MW MMBZ5239B |
| VR208 | 4813830A24 | DIODE 11V 5\% 225MW MMBZ5241B |
| VR209 | 4813830A15 | DIODE 5.6V 5\% 225MW MMBZ5232B |
| VR210 | --------- | NOTPLACED |
| VR211 | 4813830A15 | DIODE 5.6V 5\% 225MW MMBZ5232B |
| VR212 thru VR214 | 4813830A15 | DIODE 5.6V 5\% 225MW MMBZ5232B_ |
| VR215 thru VR223 |  | NOTPLACED |
| Y201 | 4805574W01 | XTAL PRL RESONANCE 7.3728 MHZ |


| ITEM | MOTOROLA <br> PART NUMBER | DESCRIPTION |
| :--- | :--- | :--- |
| Y401 | 4805573 W01 | CRYSTAL SERIAL <br> RESONANCE |
|  | 8405160 Y 03 | BD CKT VOCON CTRL |

Notes:

1. Valid in HLN6458F only
2. Valid in HLN6458G only


HLN6458H VOCON Board Schematic (Sheet 1 of 2)


HLN6458H VOCON Board Schematic (Sheet 2 of 2)


HLN6458H VOCON Board Component Location Diagrams

HLN6458H VOCON Board Electrical Parts List

| ITEM | MOTOROLA PART NUMBER | description |
| :---: | :---: | :---: |
|  |  | CAPACITOR，Fixed： unless otherwise stated |
| C201， 202 | －－－－－ | Not Placed |
| C203， 204 | 2113930F39 | 33 pF |
| C205 thru 213 | －－－－－ | Not Placed |
| C214 | 2113931 F 17 | 470pF |
| C215 | 2113930F39 | 33pF |
| C216 | －－－ | Not Placed |
| C217 | 2113932K15 | 0.1 \％ |
| C218 | －－－－－ | Not Placed |
| C219 | 2113931 F 17 | 470pF |
| C220， 221 | －－－－－ | Not Placed |
| C222， 223 | 2113931 F 17 | 470pF |
| C224 thru 226 | 2113932K15 |  |
| C227 | 2113930 F36 | 24pF |
| C228 | －－－－－ | Not Placed |
| C229 | 2113930F36 | 24pF |
| C230， 231 | 2113932K15 | 0．1瑗 |
| C236 | 2113932 K 15 | 0．1出 |
| C237 | 2113931717 | 470pF |
| C238 | 2113932K15 | 0．1瑗 |
| C241 | 2113931717 | 470pF |
| C245 thru 247 | 2113932 K 15 | 0．1瑗 |
| C248 | 2113931 F 17 | 470pF |
| C249 thru 257 | －－－－－ | Not Placed |
| C258 | 2113930F39 | 33 pF |
| C261 | 2113932K15 | 0．1出 |
| C262 thru 269 | －－ | Not Placed |
| C270 | 2113931 F 17 | 470pF |
| C271 thru 279 | －－－－－ | Not Placed |
| C281， 282 | 2113930F39 | 33pF |
| C284 thru 287 | －－－－－ | Not Placed |
| C400 | 2113932 K 15 | 0．1瑗 |
| C401 | 2113931717 | 470pF |
| C402 thru 406 | －－－－－ | Not Placed |
| C407 thru 414 | 2113932K15 | 0．1瑗 |
| C415 | 2113743A23 | $0.22 \mu \mathrm{~F}$ |


| ITEM | MOTOROLA PART NUMBER | description |
| :---: | :---: | :---: |
| C416 thru 420 | 2113932K15 | 0．14F |
| C421 | $2113931 F 17$ | 470pF |
| C422 thru 434 | －－－－－ | Not Placed |
| C435 | 2113932K15 | 0．1瑗 |
| C436， 437 | －－－－－ | Not Placed |
| C438 | 2113930 F34 | 20pF |
| C439 | －－ | Not Placed |
| C440 | 2113732K15 | 0．14F |
| C441 | －－－－－ | Not Placed |
| C442， 443 | 2113930 F09 | 1．8pF |
| C444 thru 456 | 2113932K15 | 0．14F |
| C457 | 2113931F13 | 330pF |
| C458 | 2113932 K 15 | 0．1瑗 |
| C459 | 2113930F03 | 1pF |
| C460 |  | Not Placed |
| C461， 462 | 2113932K15 | $0.1 \mu \mathrm{~F}$ |
| C463 thru 465 | －－－ | Not Placed |
| C466 | 2113932K15 | $0.1 \mu \mathrm{~F}$ |
| C467 | $2113930 F 21$ | 5．6pF |
| C468 | 2113743A23 | $0.22 \mu \mathrm{~F}$ |
| C469 | －－ | Not Placed |
| C470 | 2311049C07 | 100hF |
| C471 thru 475 | －－－－－ | Not Placed |
| C476， 477 | 2113932K15 | 0.1 \％ |
| C478，479 | －－－－－ | Not Placed |
| C480 | 2113930F46 | 62pF |
| C481 | －－－－－ | Not Placed |
| C482 | 2311049A05 | 0．47 F |
| C483 | $2113930 F 39$ | 33pF |
| C484 thru 489 | －－－－－ | Not Placed |
| C490， 491 | 2113931 F17 | 470pF |
| C492 | －－－－－ | Not Placed |
| C493 | 2113932K15 | $0.1 \mu \mathrm{~F}$ |
|  |  | DIODE：See Note 1. |
| CR201 | －－－－－ | Not Placed |
| CR202 | 4884939C35 | Hot Carrier，4V |
| CR203， 204 | －－－－－ | Not Placed |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| CR205 | 4805212N57 | Dual Diode |
| CR402 | 4813825A06 | PIN，35V |
| CR403 | －－－－－ | Not Placed |
|  |  | JACK： |
| J201 | －－－－ | Not Placed |
| J401 | －－－－－ | Not Placed |
| J501 | 0905516W02 | Connector，Receptacle |
| J601 | －－－． | Not Placed |
| J701 | －－－－－ | Not Placed |
| J801 | 0913915A11 | Connector，25－Position |
| J901 | －－－－－ | Not Placed |
|  |  | JUMPER： |
| JU201 | －－－－－ | Not Placed |
| JU210， 211 | －－－－ | Not Placed |
| JU401 | －－－－－ | Not Placed |
| $\begin{aligned} & \text { JU402 thru } \\ & 404 \end{aligned}$ | 0662057847 | 0 Ohm |
| JU405 | －－－－－ | Not Placed |
| JU407 | 0662057847 | 0 Ohm |
|  |  | COIL，RF： unless otherwise stated |
| L201 | 2460578 C43 | 33 HH |
| L203 thru 206 | －－－－－ | Not Placed |
| L208 thru 223 | －－－－－ | Not Placed |
| L401， 402 | －－－－－ | Not Placed |
| L403 | 2462587 E71 | ${ }^{1.8 \mu H}$ |
|  |  | TRANSISTOR：See Note 1. |
| Q201 thru 203 | －－－－－ | Not Placed |
| Q204 thru 206 | 4805128M12 | NPN |
| Q207 | 4805718V01 | PNP |
| Q208 | 4805128M12 | NPN |
| Q210 | －－－－－ | Not Placed |
| Q211 | 4805128M40 | PNP |
| Q401， 402 | －－－－－ | Not Placed |
| Q403 | 4805128M12 | NPN |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
|  |  | RESISTOR： $\mathrm{W} \pm 5 \%$ ；1／8W unless otherwise stated |
| R199 | －－－－－ | Not Placed |
| R200 | 0662057A65 | 4．7k |
| R201， 202 | 0662057A97 | 100k |
| R203 | －－－－－ | Not Placed |
| R204 | 0662057A97 | 100k |
| R205， 206 | －－－－－ | Not Placed |
| R207 | 0662057A73 | 10k |
| R208 thru 210 | －－－－－ | Not Placed |
| R211 | 0662057A97 | 100k |
| R212 | 0662057A73 | 10k |
| R213 thru 217 | －－－－－ | Not Placed |
| R218 | 0662057A65 | 4．7k |
| R219 | 0662057A97 | 100k |
| R220 | －－－－－ | Not Placed |
| R221 | 0662057A73 | 10k |
| R222 | 0662057G08 | 82．5k |
| R223 | 0662057R92 | 47．5k |
| R224 | 0662057A85 | 33k |
| R225 | $0662057 B 47$ | 0 |
| R226， 227 | 0662057A97 | 100k |
| R228 | －－－ | Not Placed |
| R229 | 0662057A97 | 100k |
| R230 | 0662057A65 | 4．7k |
| R231 | 0662057B22 | 1M |
| R232 | 0662057A65 | 4．7k |
| R233 | 0662057A85 | 33k |
| R234， 235 | 0662057A73 | 10k |
| R236 | 0662057A97 | 100k |
| R237 | 0662057A73 | 10k |
| R238 | 0662057A97 | 100k |
| R239 | 0662057A65 | 4．7k |
| R240 thru 242 | －－－－－ | Not Placed |
| R243 thru 248 | 0662057A97 | 100k |
| R249 | －－－－－ | Not Placed |
| R250 | 0662057A73 | 10k |



### 7.4 ASTRO Spectra Plus VOCON Section



ASTRO Spectra Plus Top Level Schematic (Sheet 1 of 2)


ASTRO Spectra Plus Top Level Schematic (Sheet 2 of 2)



ASTRO Spectra Plus RF Interface Schematic (Sheet 2 of 2)



ASTRO Spectra Plus Digital/USB Schematic (Sheet 2 of 2)



ASTRO Spectra Plus Voltage Conversion Schematic



ASTRO Spectra Plus VOCON Component Location Diagram, Top View


ASTRO Spectra Plus VOCON Component Location Diagram, Bottom View

| HLN6837A ASTRO Spectra Plus VOCON Electrical Parts List |  |  | ITEM | MOTOROLA PART NUMBER | DESCRIPTION | ITEM | MOTOROLA PART NUMBER | DESCRIPTION | ITEM | MOTOROLA <br> PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ITEM | MOTOROLA PART NUMBER | DESCRIPTION | C305 | 2113743M24 | CAP CHIP 100000 PF +80-20\% Y5V | C401 | 2113743M24 | ```CAP CHIP 100000 PF +80-20%``` | C600 | 2113743M24 | CAP CHIP 100000 PF +80-20\% Y5V |
| C200 | 2113743M24 | CAP CHIP 100000 PF +80-20\% Y5V | C306 | 2113743M24 | $\begin{aligned} & \text { CAP CHIP } 100000 \text { PF +80-20\% } \\ & \text { Y5V } \\ & \hline \end{aligned}$ | C402 | 2113928E01 | CAP CER CHIP 1.0 UF $10 \% 10 \mathrm{~V}$ | C601 | 2113743M24 | ```CAP CHIP 100000 PF +80-20% Y5V``` |
| C201 | 2113930 F 18 | CAP 4.3 PF 50V +/-. 25 PF 50V | C307 | 2113743M24 | CAP CHIP 100000 PF $+80-20 \%$ Y5V | C404 | 2113743N38 | CAP CHIP 33.0 PF 5\% COG | C602 | 2113743M24 | CAP CHIP 100000 PF +80-20\% Y5V |
| C202 | 2113743M24 | $\begin{aligned} & \text { CAP CHIP } 100000 \text { PF }+80-20 \% \\ & \text { Y5V } \end{aligned}$ | C308 | 2113743M24 | CAP CHIP 100000 PF +80-20\% Y5V | C405 | 2113743M24 | $\begin{aligned} & \text { CAP CHIP } 100000 \text { PF +80-20\% } \\ & \text { Y5V } \end{aligned}$ | D300 | 4805129M90 | DIODE SOT PKGD |
| C203 | 0662057C01 | CHIP RES 0 OHMS +.050 OHMS |  |  |  | C406 | 2113743M24 | CAP CHIP 100000 PF +80-20\%Y5V | D304 | 4805129M90 | DIODE SOT PKGD |
| C204 | 0662057C01 | CHIP RES 0 OHMS +.050 OHMS | C309 | 3743L09 | CAP CHIP 470 PF 10\% X7R |  |  |  | J501 | 0905516W02 | CONN RECEPTACLE |
| C205 | 2113743M24 | $\begin{aligned} & \text { CAP CHIP } 100000 \text { PF +80-20\% } \\ & \text { Y5V } \end{aligned}$ | C310 | 2113743M24 | CAP CHIP 100000 PF +80-20\% Y5V | C407 | $2113743 \mathrm{L41}$ | CAP CHIP 10000 PF 10\% X7R | L200 | 2462587N58 | CHIP IND 270 NH 5\% |
|  |  |  | C311 | 2113743M24 | CAP CHIP 100000 PF +80-20\%Y5V | C415 | 2185802B01 | CAP 10V 4.7UF | L300 | 2480067M02 | CHK RF CHIP BEAD INDUCTOR |
| C206 | 2113743M24 | $\begin{aligned} & \text { CAP CHIP } 100000 \text { PF +80-20\% } \\ & \text { Y5V } \end{aligned}$ |  |  |  | C417 | 2185802B01 | $\begin{aligned} & \text { CAP 10V 4.7UF } \\ & \hline \text { CAP CHIP 100000 PF +80-20\% } \\ & \text { Y5V } \end{aligned}$ | L301 | 2480067M02 | CHK RF CHIP BEAD INDUCTOR |
|  |  |  | C312 | 2113743M | $\begin{aligned} & \text { CAP CHIP } 100000 \text { PF +80-20\% } \\ & \text { Y5V } \end{aligned}$ | C420 | 2113743M24 |  | L400 | 2462587N68 | CHIP IND 1000 NH 5\% |
| C207 | 2113743L17 | $\begin{array}{\|l\|} \hline \text { CAP CHIP } 1000 \text { PF } 10 \% \text { X7R } \\ \hline \text { CAP CHIP } 100000 \text { PF +80-20\% } \\ \text { Y5V } \end{array}$ |  |  |  |  |  |  | P1 | 0913915A11 | RECP SMT LOPRO 25 POS |
| C208 | 2113743M24 |  | C313 | 2113743M24 | $\begin{aligned} & \text { CAP CHIP } 100000 \text { PF +80-20\% } \\ & \text { Y5V } \end{aligned}$ | C422 | 2113743M24 | CAP CHIP 100000 PF +80-20\% Y5V | $\begin{array}{\|l\|} \hline \text { P3 } \\ \hline \text { PCB } \\ \hline \end{array}$ | 8485146D01 | NOTPLACED |
| C209 | 2113743M24 | ```CAP CHIP 100000 PF +80-20\% Y5V``` | $\begin{array}{\|l\|} \hline \text { C314 } \\ \hline \text { C315 } \\ \hline \end{array}$ | 2113743N38 | $\begin{array}{\|l\|} \hline \text { CAP CHIP } 33.0 \text { PF } 5 \% \text { COG } \\ \hline \text { CAP CER CHIP } 1.0 \text { UF } 10 \% 10 \mathrm{~V} \end{array}$ | C423 | 2113743N28 | CAP CHIP 12.0 PF 5\% COG |  |  | PCB, ASTRO/SPECTRA PLUS VOCON |
|  |  |  |  | 2113928E01 |  | C424 | 2113743N28 | CAP CHIP 12.0 PF 5\% COG | Q200 | 4813824A10 | TSTR NPN 40V .2A GEN PURP |
| C210 | 2113743 N26 | CAP CHIP 10.0 PF 5\% COG | C316 |  | CAP CHIP 100000 PF +80-20\% Y5V | C425 | 2185802B01 | CAP 10V 4.7UF | Q201 | $\frac{4805128 \mathrm{M} 12}{4805128 \mathrm{M} 12}$ | TSTR SOT BCW 60B (RH) |
| C211 | 2113743L41 | CAP CHIP 10000 PF $10 \%$ X7R |  |  |  | C427 | 2113743N50 | $\begin{aligned} & \text { CAP CHIP } 100000 \text { PF }+80-20 \% \\ & \text { Y5V } \\ & \hline \end{aligned}$ | Q202 |  | TSTR SOT BCW 60B (RH) |
| C212 | 2113743M24 | CAP CHIP 100000 PF +80-20\% Y5V | C317 | 2113743M24 | $\begin{aligned} & \text { CAP CHIP } 100000 \text { PF +80-20\% } \\ & \text { Y5V } \end{aligned}$ | C428 | 2113743M24 |  | Q203 | 4805128M12 | TSTR SOT BCW 60B (RH) <br> TSTR SOT BCW 60B (RH) |
| C214 | 2113743M24 | CAP CHIP 100000 PF +80-20\% Y5V |  | 2113743M24 | CAP CHIP 100000 PF +80-20\% | $\begin{array}{\|l\|} \hline \mathrm{C} 429 \\ \hline \mathrm{C} 430 \\ \hline \end{array}$ | 2113743N54 | CAP CHIP 150 PF 5\% COG | $\begin{aligned} & \hline \text { Q300 } \\ & \hline \text { Q301 } \end{aligned}$ | 4880048M01 | TSTR NPN DIG $47 \mathrm{~K} / 47 \mathrm{~K}$ |
| C215 | 2113743L33 | CAP CHIP 4700 PF 10\% X7R | C319 | 2113743M24 | CAP CHIP 100000 PF +80-20\%Y5V | C430 | 2185802B01 | CAP 10V 4.7UF <br> CAP CER CHP . 10 UF 10\% 16V |  | $\frac{4880048 \mathrm{M} 01}{4880048 \mathrm{M} 01}$ | TSTR NPN DIG 47K/47K |
| C216 | 2113743L09 | CAP CHIP 470 PF $10 \%$ X7R <br> CAP CHIP 100000 PF +80-20\% Y5V |  |  |  | $\begin{array}{\|l} \hline \text { C432 } \\ \hline \text { C433 } \\ \hline \end{array}$ | 2113743L41 | CAP CHIP 10000 PF 10\% X7R | Q302 |  | TSTR NPN DIG 47K/47K |
| C217 | 2113743M24 |  |  | 2113743M24 | CAP CHIP 100000 PF $+80-20 \%$ Y5V |  | 2113932E20 | CAP CER CHP . 10 UF 10\% 16V | Q303 | 4885844C02 | XSTR P-CH MOSFET 30V |
| C218 | 2113743M24 | ```CAP CHIP 100000 PF +80-20%``` |  | 2113743M24 | $\begin{aligned} & \text { CAP CHIP } 100000 \text { PF +80-20\% } \\ & \text { Y5V } \end{aligned}$ | $\begin{array}{\|l\|} \hline \mathrm{C} 434 \\ \hline \mathrm{C} 435 \\ \hline \end{array}$ |  | CAP CER CHP 10 UF 10\% 16V |  |  | 80MOHM@4.5V |
|  |  |  |  |  |  |  | 2113932E20 |  | Q601 | 4880048M01 | TSTR NPN DIG 47K/47K |
| C219 | 2113743M24 | $\begin{aligned} & \text { CAP CHIP } 100000 \text { PF }+80-20 \% \\ & \text { Y5V } \end{aligned}$ | C322 | 2113743M24 | CAP CHIP 100000 PF +80-20\%Y5V | $\begin{array}{\|l\|} \hline \text { C436 } \\ \hline \text { C437 } \\ \hline \end{array}$ | 2113743L41 | CAP CHIP 10000 PF $10 \%$ X7R | $\begin{array}{\|l} \hline \text { R200 } \\ \hline \text { R201 } \\ \hline \end{array}$ | 0662057N23 | RES. CHIP 100K 5\% 20X40 |
|  |  |  |  |  |  |  | 2113932E20 |  |  | 0662057N23 | RES. CHIP 100K 5\% 20X40 |
| C220 | 2113743M24 | $\begin{aligned} & \text { CAP CHIP } 100000 \text { PF +80-20\% } \\ & \text { Y5V } \end{aligned}$ | C323 | 2113743M24 | $\begin{aligned} & \text { CAP CHIP } 100000 \text { PF }+80-20 \% \\ & \text { Y5V } \end{aligned}$ | $\begin{array}{\|l} \hline \mathrm{C} 438 \\ \hline \text { C439 } \\ \hline \end{array}$ | 2113743L41 | CAP CHIP 10000 PF 10\% X7R | R202 | 0662057M81 | RES. CHIP 2000 5\% 20X40 |
|  |  |  |  |  |  |  | 2113743N30 | CAP CHIP 15.0 PF 5\% COG | R203- |  | NOTPLACED |
| C221- |  | NOTPLACED | C324 | 2113743M24 | $\begin{aligned} & \text { CAP CHIP } 100000 \text { PF +80-20\% } \\ & \text { Y5V } \end{aligned}$ | C440 |  | NOTPLACED | R206 |  |  |
| C222 |  |  |  |  |  | C441 | 2113932E20 | CAP CER CHP . 10 UF 10\% 16V | R207 | 0662057N05 | RES. CHIP 18K 5\% 20X40 |
| C223 | 2113743M24 | CAP CHIP 100000 PF +80-20\% Y5V | C325 | 2113743M24 | CAP CHIP 100000 PF +80-20\% Y5V | C444 | 2113743 N 50 | CAP CHIP 100 PF 5\% COG | R208 | 0662057 N10 | RES. CHIP 30K 5\% 20X40 |
| C300 | 2113928E01 | CAP CER CHIP 1.0 UF $10 \% 10 \mathrm{~V}$ | C326 | 2113930F59 | CAP CER CHP 220 PF 50V 5\% | C500 | 2113743M24 | CAP CHIP 100000 PF +80-20\% | R209 | 0662057N10 | RES. CHIP 30K 5\% 20X40 |
| C301 | 2113743N38 | CAP CHIP 33.0 PF 5\% COG | C327 | 2113743M24 | CAP CHIP 100000 PF +80-20\% |  |  |  | R213 | 0662057A35 | CHIP RES 270 OHMS 5\% |
| C302 | 2113743M24 | CAP CHIP 100000 PF +80-20\% |  |  | Y5V | C501 | 2113743M24 | CAP CHIP 100000 PF +80-20\% Y5V | R214 | 0662057A35 | CHIP RES 270 OHMS 5\% |
|  |  | Y5V | C328 | 2113743M24 | HIP 100000 PF +80-20\% |  |  |  | R215 | 0662057A13 | CHIP RES 33 OHMS 5\% |
| C303 | 2113743M24 | CAP CHIP 100000 PF +80-20\% |  |  |  |  |  | Y5V | R216 | 0662057A13 | CHIP RES 33 OHMS 5\% |
|  |  | Y5V | C330 | 2113743L09 | CAP CHIP 470 PF 10\% X7R | C503 | 2113743M24 | CAP CHIP 100000 PF +80-20\% | R219 | 0662057A37 | CHIP RES 330 OHMS 5\% |
| C304 | 2113743M24 | CAP CHIP 100000 PF +80-20\% | C331 | 2113743L09 | CAP CHIP 470 PF 10\% X7R |  |  | Y5V | R220 | 0662057 A 37 | CHIP RES 330 OHMS 5\% |
|  |  | Y5V | C400 | 2113743M24 | CAP CHIP 100000 PF +80-20\% | C504 | 2113743M24 | CAP CHIP 100000 PF +80-20\% | R221 | 0662057N05 | RES. CHIP 18K 5\% 20X40 |
|  |  |  |  |  |  |  |  |  | R222 | 0662057N23 | RES. CHIP 100K 5\% 20X40 |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R223 | 0662057M98 | RES. CHIP 10K 5\% 20X40 |
| R224 | 0662057 N 23 | RES. CHIP 100K 5\% 20X40 |
| R226 | 0662057 N10 | RES. CHIP 30K 5\% 20X40 |
| R229 | 0662057N23 | RES. CHIP 100K 5\% 20X40 |
| R230 | 0662057 N 23 | RES. CHIP 100K 5\% 20X40 |
| R231 | 0662057N10 | RES. CHIP 30K 5\% 20X40 |
| R232 | 0662057M98 | RES. CHIP 10K 5\% 20X40 |
| R234 | 0662057M50 | RES. CHIP 100 5\% 20X40 |
| R240 | 0662057M86 | RES. CHIP 3300 5X 20X40 |
| R241 | -------- | NOTPLACED |
| R242 | 0662057M64 | RES. CHIP 390 5\% 20X40 |
| R243 | --------- | NOTPLACED |
| $\begin{array}{\|l\|} \hline \text { R245 } \\ \text { R249 } \end{array}$ | --------- | NOTPLACED |
| R250 | 0662057M90 | RES. CHIP 4700 5\% 20X40 |
| R301 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +. 050 OHMS |
| R303 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +-. 050 OHMS |
| R304 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +.. 050 OHMS |
| R306 | ---------- | NOTPLACED |
| R307 | 0662057N15 | RES. CHIP 47K 5\% 20X40 |
| R308 | 0662057 N 15 | RES. CHIP 47K 5\% 20X40 |
| R309 |  | NOTPLACED |
| R310 | 0662057N15 | RES. CHIP 47K 5\% 20X40 |
| R311 | 0662057M98 | RES. CHIP 10K 5\% 20X40 |
| R313 | 0662057M78 | RES. CHIP 1500 5\% 20X40 |
| R314 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +.. 050 OHMS |
| R315 | 0662057 N15 | RES. CHIP 47K 5\% 20X40 |
| R316 | 0662057M34 | RES. CHIP 22 5\% 20X40 |
| R317 | 0662057M34 | RES. CHIP 22 5\% 20X40 |
| R318 | 0662057B47 | $\begin{aligned} & \text { CHIP RES } 0 \text { OHMS +-. } 050 \\ & \text { OHMS } \end{aligned}$ |
| $\begin{array}{\|l\|} \hline \text { R320 - } \\ \text { R321 } \end{array}$ | ---------- | NOTPLACED |
| R322 | 0662057 N 23 | RES. CHIP 100K 5\% 20X40 |
| R323 | 0662057 B 47 | $\begin{aligned} & \text { CHIP RES } 0 \text { OHMS +-. } 050 \\ & \text { OHMS } \end{aligned}$ |
| R325 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +-. 050 OHMS |
| R329 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +-. 050 OHMS |
| R331 | 0662057B47 | CHIP RES 0 OHMS +-. 050 OHMS |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R332 | 0662057M98 | RES. CHIP 10K 5\% 20X40 |
| R333 | 0662057N15 | RES. CHIP 47K 5\% 20X40 |
| R340 | 0662057 B 47 | CHIP RES 0 OHMS +-. 050 OHMS |
| R341 | $0662057 B 47$ | CHIP RES 0 OHMS +-. 050 OHMS |
| R342 |  | NOTPLACED |
| R343 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +-. 050 OHMS |
| R344 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +-. 050 OHMS |
| R345 | 0662057M74 | RES. CHIP 1000 5\% 20X40 |
| R346 | 0662057 N 15 | RES. CHIP 47K 5\% 20X40 |
| R347 | 0662057N15 | RES. CHIP 47K 5\% 20X40 |
| R348 |  | NOTPLACED |
| R349 | 0662057N15 | RES. CHIP 47K 5\% 20X40 |
| R400 | 0662057M86 | RES. CHIP 3300 5X 20X40 |
| R401 | 0662057M86 | RES. CHIP 3300 5X 20X40 |
| R402 | 0662057N23 | RES. CHIP 100K 5\% 20X40 |
| R403 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +-. 050 OHMS |
| R404 | 0662057M91 | RES CHIP 5100 5\% 20X40 |
| R405 | 0662057M82 | RES. CHIP 2200 5\% 20X40 |
| R406 | 0662057N23 | RES. CHIP 100K 5\% 20X40 |
| R407 | 0662057M98 | RES. CHIP 10K $5 \% 20 \times 40$ |
| R408 | 0662057N05 | RES. CHIP 18K 5\% 20X40 |
| R409 | 0662057N23 | RES. CHIP 100K 5\% 20X40 |
| R410 | 0662057M98 | RES. CHIP 10K 5\% 20X40 |
| R416 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +-. 050 OHMS |
| R419 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +-. 050 OHMS |
| R420 | 0662057 B 47 | CHIP RES 0 OHMS +-. 050 OHMS |
| R421 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +-. 050 OHMS |
| R425 | $0662057 B 46$ | CHIP RES 10.0 MEG OHMS 5\% |
| R426 | 0662057N39 | RES. CHIP 470K 5\% 20X40 |
| R427 | 0662057B46 | CHIP RES 10.0 MEG OHMS 5\% |
| R428 | 0662057M50 | RES. CHIP 100 5\% 20X40 |
| R429 | ----------- | NOTPLACED |
| R430 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS + -. 050 OHMS |
| R434 | 0662057M81 | RES. CHIP 2000 5\% 20X40 |
| R435 | 0662057M74 | RES. CHIP 1000 5\% 20X40 |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R436 | $0662057 \mathrm{B47}$ | CHIP RES 0 OHMS +-. 050 OHMS |
| R437 | 0662057N07 | RES. CHIP 22K 5\% 20X40 |
| R438 | 0662057N07 | RES. CHIP 22K 5\% 20X40 |
| R441 | 0662057N39 | RES. CHIP 470K 5\% 20X40 |
| R442 | 0662057N39 | RES. CHIP 470K 5\% 20X40 |
| R443 | $0662057 \mathrm{B46}$ | CHIP RES 10.0 MEG OHMS 5\% |
| R451 | 0662057N13 | RES. CHIP 39K 5\% 20X40 |
| R452 | 0662057N06 | RES. CHIP 20K 5\% 20X40 |
| R454 | 0662057 N 11 | RES. CHIP 33K 5\% 20X40 |
| R455 | 0662057N15 | RES. CHIP 47K 5\% 20X40 |
| R456 | 0662057B47 | CHIP RES 0 OHMS +-. 050 OHMS |
| R500 | 0662057M81 | RES. CHIP 2000 5\% 20X40 |
| R600 | 0662057B47 | CHIP RES 0 OHMS +-. 050 OHMS |
| R603 | --------- | NOTPLACED |
| R606 | 0662057N23 | RES. CHIP 100K 5\% 20X40 |
| SC400 | --------- | NOTPLACED |
| U200 | 5185963A50 | IC CMOS KRSIC |
| U201 | 5105457W85 | CC ADDAG IC |
| U202 | 5113818A14 | IC DL OP AMP RAIL TO RAIL |
| U300 | 5109841C69 | IC 256 BGA $17 \times 17$ PACKAGE PATRIOT |
| U301 | 5185368C95 | IC BURST FLASH 32MB |
| U302 | 5185130 C 38 | IC SRAM 4MG |
| U303 | $5105109 Z 31$ | IC QUAD 2:1 MUX/DEMUX |
| U304 | 5185368C53 | IC USB TRANSCEIVER 14 PIN TSSOP14 |
| U305 | 5109522E53 | IC SNGL BUF NC7S125P5X SC70 |
| U306 | 5113837A15 | IC 3.3V QUAD BUFFER |
| U307 | 5113837A02 | IC NOR QUAD 2 IN MC 74LCX02DTR |
| U400 | 5185368C55 | ICDIGITAL EEPOT 8 PIN S08 |
| U401 | 5185368C55 | ICDIGITAL EEPOT 8 PIN S08 |
| U402 | 5105109Z38 | 3 VOLT LINEAR PCM CODEC FILTER |
| U404 | 5113818A14 | IC DL OP AMP RAIL TO RAIL |
| U408 | 5185623B01 | HIGH SPEED CMOS INVERTER |
| U409 | 5185353D93 | IC AHCMOS SINGER GATE BUFFERED INVERTER |
| U410 | 5185353D55 | IC VOLTAGE REGULATOR 100MZ ADJ |
| U411 | 5185353D55 | IC VOLTAGE REGULATOR 100MZ ADJ |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| U412 | 5185353D46 | IF 4.3V VOLTAGE DETECTOR |
| U500 | 5113805 C 01 | IC LN DRVR/RCVR 3V |
| U501 | 5113805 C 01 | IC LN DRVR/RCVR 3V |
| U502 | 5113805C01 | IC LN DRVR/RCVR 3V |
| U503 | 5113837 A 07 | IC LINE DRVR OCT3 MC74LCX244DT |
| U504 | 5113837A07 | IC LINE DRVR OCT3 MC74LCX244DT |
| U600 | 5113805B16 | IC CNTR DL 4 STAGE BIN HC393D |
| U601 | 5113806 A05 | IC FLIP-FLOP,DUAL D |
| U602 | 5113805B44 | IC OR QUAD 2-IN |
| VR301 | 4813830A11 | DIODE 3.9V 5\% 225MW MMBZ5228B |
| VR302 | 4813830A28 | DIODE 15V 5\% 225MW MMBZ5245B_ |
| VR303 | 4813830A15 | DIODE 5.6V 5\% 225MW MMBZ5232B_ |
| VR304 | 4813830A22 | DIODE 9.1V 5\% 225MW MMBZ5239B |
| VR305 | 4813830A15 | DIODE 5.6V 5\% 225MW MMBZ5232B |
| Y400 | 4885071E01 | MODULE REFERENCE <br> OSCILLATOR 16.8 MHZ 5X3.2 MM |
| Y401 | 4809995L05 | XTAL QUARTZ 32.768KHZ CC4V-T1 |

### 7.5 VCO Section



HLD6061D and HLD6062D VHF VCO Hybrid Schematic


HLD6061D and HLD6062D VHF VCO Hybrid Component Location Diagram

## HLD6061D and HLD6062D Parts List

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { C3647 thru } \\ & \text { C3652 } \\ & \hline \end{aligned}$ | 2113740B65 | CAP CHIP REEL CL1 $+/-30$ 470 |
| C3653 | $\begin{aligned} & 2113740 \mathrm{~B} 73 \\ & 2113740 \mathrm{~B} 65 \end{aligned}$ | CAP CHIP REEL CL1 $+/-30$ $10000^{1}$ CAP CHIP REEL CL1 $+/-30$ $470^{2}$ |
| C3654 | $\begin{aligned} & \hline 2113740 \mathrm{~B} 69 \\ & \text { 2111078B59 } \end{aligned}$ | CAP CHIP REEL CL1 $+/-30$ $680{ }^{1}$ CAP CHII RF $^{100 V^{2}}{ }^{2} 570$ NPO |
| $\begin{aligned} & \hline \text { C3655 \& } \\ & \text { C3656 } \\ & \hline \end{aligned}$ | 2111078B59 | $\begin{aligned} & \text { CAP CHIP RF } 4705 \text { NPO } \\ & \text { 100V } \end{aligned}$ |
| C3657 | $\begin{aligned} & 2111078 \mathrm{~B} 59 \\ & 2113740 \mathrm{~B} 65 \end{aligned}$ | CAP CHIP RF 4705 NPO $100 V^{1}$ <br> CAP CHIP REEL CL1 +/-30 $470{ }^{2}$ |
| C3658 | $\begin{aligned} & 2111078 \mathrm{~B} 59 \\ & 2113740 \mathrm{~B} 65 \end{aligned}$ | CAP CHIP RF 4705 NPO $100 \mathrm{~V}^{1}$ <br> CAP CHIP REEL CL1 +/-30 <br> $470{ }^{2}$ |
| C3659 | $\begin{aligned} & 2113740 \mathrm{~B} 11 \\ & 2113740 \mathrm{~B} 09 \end{aligned}$ | CAP CHIP REEL CL1 $+/-30$ $2.7^{1}$ CAP CHIP REEL CL1 $+/-30$ $2.2^{2}$ |
| C3660 | 2113740B11 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 2.7 \end{aligned}$ |
| C3661 | 2113740B19 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 5.6 \end{aligned}$ |
| $\begin{aligned} & \hline \text { C3662 \& } \\ & \text { C3663 } \\ & \hline \end{aligned}$ | $2113740 \mathrm{B65}$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C3664 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C3665 | $\begin{aligned} & \hline \text { 2111078B59 } \\ & \text { 2111078B31 } \end{aligned}$ | CAP CHIP RF 4705 NPO $100 V^{1}$ <br> CAP CHIP RF 365 NPO $100 V^{2}$ |
| C3666 | 2111078 B 23 | $\begin{aligned} & \text { CAP CHIP RF } 245 \text { NPO } \\ & 100 \mathrm{~V} \end{aligned}$ |
| C3667 | 2111078B31 | CAP CHIP RF 365 NPO 100 V |
| C3668 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C3669 | 2111078B05 | $\begin{aligned} & \text { CAP CHIP RF 4.7 . } 25 \text { NPO } \\ & 100 \mathrm{~V} \end{aligned}$ |
| C3670 | 2111078 B11 | $\begin{aligned} & \text { CAP CHIP RF } 8.2 .5 \text { NPO } \\ & 100 \mathrm{~V} \end{aligned}$ |
| C3671 | $2113740 \mathrm{B65}$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C3672 | 2113740B05 | CAP CHIP REEL CL1 $+/-30$ 1.5 |

Notes:

1. Valid in HLD6061D only
2. Valid in HLD6062D only.


HLD4342B and HLD4343B VHF VCO Carrier Schematic Diagram


HLD4342D and HLD4343D VHF VCO Carrier Schematic Diagram


Side 2

HLD4342B/HLD4343B VHF VCO Carrier Component Location Diagram


Side 1


Side 2

| HLD4342B and HLD4343B VHF VCO Parts List |  |  |
| :---: | :---: | :---: |
| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| C3645 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C3655 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3656 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C3657 | $2113740 B 28$ | CAP CHIP REEL CL1 +/-30 13 |
| C3658 | 2113740 B36 | CAP CHIP REEL CL1 $+/-3030$ |
| C3659 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C3660 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C636 | 2113741N21 | CAP CHIP CL2 X7R 10\% 1000 |
| $\begin{aligned} & \text { C637 \& } \\ & \text { C638 } \end{aligned}$ | 2113740B76 | $\begin{aligned} & \text { CAP CHIP REEL CL1 }+/-30 \\ & 1500 \end{aligned}$ |
| C639 | 2311049A37 | CAP TANT CHIP 12020 |
| $\begin{aligned} & \hline \text { C646 \& } \\ & \text { C647 } \end{aligned}$ | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R } 10 \% \\ & 10000 \end{aligned}$ |
| C648 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C649 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C650 | 2113740865 | CAP CHIP REEL CL1 +/-30 470 |
| C651 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| C652 | --------- | NOTPLACED |
| C653 | 2113740865 | CAP CHIP REEL CL1 +/-30 470 |
| C654 | 2160521 H 41 | CAP CHIP 220K 802025 V Y5V |
| C661 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C662 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C663 \& C664 |  | NOTPLACED |
| C665 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C666 | 2160521 H 41 | CAP CHIP 220K 802025 V Y5V |
| C668 thru C672 | 2113740B76 | CAP CHIP REEL CL1 +/-30 1500 |
| C673 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C674 | 2113740B65 | NOTPLACED ${ }^{1}$ <br> ${ }_{2}^{\text {CAP CHIP REEL CL1 +/-30 }} 470$ |
| C675 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C676 | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } 470 \\ & 1 \\ & \text { NOTPLACED }{ }^{2} \end{aligned}$ |
| $\begin{aligned} & \hline \text { C677 \& } \\ & \text { C679 } \\ & \hline \end{aligned}$ | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C685 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| C686 | 2311049.J07 | CAP TANT CHIP 3.31020 |
| C687 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C688 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C689 | 2113740B44 | CAP CHIP REEL CL1 +/-30 62 |
| C690 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R } 10 \% \\ & 10000 \end{aligned}$ |
| C691 | $2113740 B 11$ | CAP CHIP REEL CL1 +/-30 2.7 |
| C692 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| 00 | --------- | NOTPLACED |
| C701 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R } 10 \% \\ & 100000 \end{aligned}$ |
| $\begin{aligned} & \hline \text { C702 \& } \\ & \text { C703 } \\ & \hline \end{aligned}$ | $2113740 B 76$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 1500 \end{aligned}$ |
| CR601 \& CR602 | 4880154K03 | DIODE DUAL SCHOTTKY MIXER |
| J3641 | 0980110M01 | RECP COAX PCBMOUNT |
| J642 | 0980212N03 | RECP COAX PCB MOUNT |
| JU670 \& JU671 |  | NOTPLACED |
| L3663 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| $\begin{aligned} & \hline \text { L3664 \& } \\ & \hline \text { L3665 } \\ & \hline \end{aligned}$ | 2480140E04 | INDUCTOR CHIP 65 NH |
| L660 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| $\begin{array}{\|l\|} \hline \text { L661 \& } \\ \hline \text { L662 } \end{array}$ | 2480140E03 | INDUCTOR CHIP 50 NH |
| $\begin{array}{\|l\|} \hline \text { L666 thru } \\ \text { L674 } \end{array}$ | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L675 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L676 | 2405452 C 87 | COIL CHIP INDUCTOR 395 |
| L677 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L678 | 2480140E03 | INDUCTOR CHIP 50 NH |
| L679 | 2405452 C 87 | COIL CHIP INDUCTOR 395 |
| L681 | 2480140E03 | INDUCTOR CHIP 50 NH |
| $\begin{aligned} & \hline \text { L682 thru } \\ & \text { L685 } \\ & \hline \end{aligned}$ | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| P0601 | 2880099M02 | PLUG VERTICAL |
| P0602 | 4280088P01 | $\begin{aligned} & \text { NOTPLACED }{ }^{1} \\ & \text { CLIP COAX }{ }^{2} \end{aligned}$ |
| Q3683 | 4813827A03 | TSTR NPN SML SIG MMBR901LT1 7X |
| $\begin{aligned} & \hline \text { Q642 \& } \\ & \text { Q643 } \end{aligned}$ | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q675 | 4802000P02 | NEC SILICON TR 2SC3357 RE |
| $\begin{array}{\|l\|l} \hline \text { Q677 \& } \\ \text { Q678 } \\ \hline \end{array}$ | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q681 | 4813827 A03 | TSTR NPN SML SIG MMBR901LT1 7X |
| Q682 | 4813827A03 | TSTR NPN SML SIG MMBR901LT1 7X |
| R3663 | 0611077A62 | RES CHIP 3305 1/8W |
| R3664 | 0611077A82 | RES CHIP $220051 / 8$ |
| R3665 | --------- | NOTPLACED |
| R3666 | 0611077A30 | RES CHIP $1551 / 8 \mathrm{~W}$ |
| R3667 | 0611077A66 | RES CHIP 4705 1/8W |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R3668 | 0611077 A90 | RES CHIP $470051 / 8$ |
| R3669 | -------- | NOTPLACED |
| R636 | 0611077 A94 | RES CHIP $680051 / 8$ |
| R637 | 0611077 B07 | RES CHIP $22 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R639 | 0611077 B15 | RES CHIP 47K $51 / 8 \mathrm{~W}$ |
| R641 | 0611077A58 | RES CHIP 2205 1/8W |
| R642 | 0611077 A90 | RES CHIP 47005 1/8 |
| R643 | 0611077A74 | RES CHIP $100051 / 8$ |
| R644 | 0611077 A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R645 | 0611077 A58 | RES CHIP 2205 1/8W |
| R646 \& R647 | 0611077 A98 | RES CHIP 10K 5 1/8W |
| R648 \& R649 | 0611077A70 | RES CHIP 6805 1/8W |
| R650 | 0611077 A98 | RES CHIP 10K $51 / 8 \mathrm{~W}$ |
| R651 | 0611077A54 | RES CHIP 1505 1/8W |
| R652 | 0611077 A86 | RES CHIP $330051 / 8$ |
| R652 | --------- | NOTPLACED |
| R653 | 0611077A62 | RES CHIP 3305 1/8W |
| R654 | 0611077 A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R655 | 0611077 A32 | RES CHIP 185 1/8W |
| R656 | 0611077A50 | RES CHIP 1005 1/8W |
| R657 | 0611077 A30 | RES CHIP 155 1/8W |
| R658 |  | NOTPLACED |
| R659 | 0611077 A78 | RES CHIP $150051 / 8$ |
| R660 | 0611077 A90 | RES CHIP $470051 / 8$ |
| R661 | 0611077 A30 | RES CHIP $1551 / 8 \mathrm{~W}$ |
| R662 | ----- | NOTPLACED |
| R670 | 0611077A50 | RES CHIP 1005 1/8W |
| R671 | --------- | NOTPLACED |
| R672 | 0611077A58 | NOTPLACED ${ }^{1}$ RES CHIP $22051 / 8 W^{2}$ |
| R673 | 0611077A58 | NOTPLACED ${ }^{1}$ RES CHIP $22051 / 8 W^{2}$ |
| $\begin{aligned} & \hline \text { R674 \& } \\ & \text { R675 } \end{aligned}$ | 0611077 A50 | RES CHIP 1005 1/8W |
| R677 | 0611077 A36 | RES CHIP 275 1/8W |
| $\begin{array}{\|l} \hline \text { R678 \& } \\ \text { R679 } \end{array}$ | 0611077 A62 | RES CHIP 3305 1/8W |
| R680 | 0611077 A26 | RES CHIP 105 1/8W |
| $\begin{aligned} & \hline \text { R681 \& } \\ & \text { R682 } \end{aligned}$ | 0611077 A74 | RES CHIP $100051 / 8$ |
| R683 | 0611077 A50 | RES CHIP 1005 1/8W |
| R684 | 0611077 A86 | RES CHIP $330051 / 8$ |
| R685 | 0611077 A98 | RES CHIP 10K 5 1/8W |
| R699 | 0611077A38 | RES CHIP 335 1/8W |
| R700 | 0611077 A62 | RES CHIP $33051 / 8 \mathrm{~W}$ |
| T601 \& | 2405548Q06 | TRANSFORMER, RF |
|  | 1280954 T43 | SPECTRA VHF VCO ASSEMBLY |

Notes

1. Valid in HLD4342B
2. Valid in HLD4343B

HLD4342D and HLD4343D VHF VCO Parts List

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C3645 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C3655 | $2113740 B 65$ |  |
| C3656 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C3657 | 2113740 B 28 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 13 \end{aligned}$ |
| C3658 | 2113740 B 36 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 30 \end{aligned}$ |
| C3659 | 2113740B39 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 39 \end{aligned}$ |
| C3660 | $2113740 B 65$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C636 | $2113740 B 76$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 1500^{1} \end{aligned}$ |
| C637 \& C638 | $2113740 B 76$ | CAP CHIP REEL CL1 $+/-30$ 1500 |
| C639 | 2311049A37 | CAP TANT CHIP 12020 |
| C646 \& C647 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C648 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C649 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C650 | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C651 | 2113740B21 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 6.8 \end{aligned}$ |
| C652 | --------- | NOTPLACED |
| C653 | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C654 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000^{1} \\ & \hline \end{aligned}$ |
| C661 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C662 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C663 |  | NOTPLACED ${ }^{2}$ |
| C664 | --- | NOTPLACED |
| C665 \& C666 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C668 \& C669 | $2113740 B 76$ | $\begin{aligned} & \text { 000CAP CHIP REEL CL1 } \\ & +/-301500 \end{aligned}$ |
| $\begin{aligned} & \text { C670 thru } \\ & \text { C672 } \end{aligned}$ | $2113740 B 76$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 1500 \end{aligned}$ |
| C673 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C674 | 2113740B65 | NOTPLACED ${ }^{1}$ CAP CHIP REEL CL1 $+/-30$ $470^{2}$ |
| C675 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C676 | $2113740 \mathrm{B65}$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 }+/-30 \\ & 470^{1} \\ & \text { NOTPLACED }{ }^{2} \end{aligned}$ |
| C677 \& C679 | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C685 | 2113740B21 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 6.8 \end{aligned}$ |
| C686 | 2311049, 07 | CAP TANT CHIP 3.31020 |
| C687 | 2113740B21 | $\begin{aligned} & \text { CAP CHIP REEL CL } 1+/-30 \\ & 6.8 \end{aligned}$ |
| C688 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \\ & \hline \end{aligned}$ |
| C689 | 2113740B44 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 62 \end{aligned}$ |
| C690 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C691 | $2113740 B 11$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 2.7 \end{aligned}$ |
| C692 | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C700 | --------- | NOTPLACED ${ }^{2}$ |
| C701 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C702 \& C703 | $2113740 B 76$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 1500 \end{aligned}$ |
| C704 | 2113740B30 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 16 \end{aligned}$ |
| C705 | 2113740B29 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 15 \end{aligned}$ |
| CR601 \& CR602 | 4880154K03 | DIODE DUAL SCHOTTKY MIXER |
| J3641 | 0980110M01 | RECP COAX PCBMOUNT |
| J642 | 0980212N03 | RECP COAX PCB MOUNT |
| $\begin{array}{\|l\|l} \hline \text { JU670 \& } \\ \text { JU671 } \\ \hline \end{array}$ |  | NOTPLACED |
| L3663 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| $\begin{aligned} & \hline \text { L3664 \& } \\ & \text { L3665 } \\ & \hline \end{aligned}$ | 2480140E04 | INDUCTOR CHIP 65 NH |
| L660 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L661 \& L662 | 2480140E03 | INDUCTOR CHIP 50 NH |
| $\begin{array}{\|l} \hline \text { L666 thru } \\ \text { L674 } \\ \hline \end{array}$ | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L675 | 2480140E01 | OOOINDUCTOR CHIP 1.2 UH |
| L676 | 2405452C87 | COIL CHIP INDUCTOR 39 <br> 5 |
| L677 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L678 | 2480140E03 | INDUCTOR CHIP 50 NH |


| ITEM | MOTOROLA PART <br> NUMBER | DESCRIPTION |
| :--- | :--- | :--- |
| L679 | 2405452 C87 | COIL CHIP INDUCTOR 39 <br> 5 |
| L681 | 2480140 E03 | INDUCTOR CHIP 50 NH |
| L682 thru <br> L685 | 2480067 M01 | CHK RF CHIP BEAD <br> INDUCTOR 28MZ |
| L686 | 2405452 C87 | COIL CHIP INDUCTOR 39 <br> 5 |
| P0601 | 2880099 M02 | PLUG VERTICAL |
| P0602 | -------- | NOTPLACED <br> CLIP COAX |
| Q280088P01 |  |  |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R662 | --------- | NOTPLACED |
| R670 | 0611077A50 | RES CHIP 1005 1/8W |
| R671 | --------- | NOTPLACED |
| R672 \& R673 | 0611077A58 | RES CHIP $22051 / 8 W^{2}$ |
| R674 | 0611077A48 | RES CHIP 825 1/8W |
| R675 | 0611077A50 | RES CHIP $10051 / 8 \mathrm{~W}$ |
| R677 | 0611077A26 | RES CHIP 105 1/8W |
| R678 \& R679 | 0611077A62 | RES CHIP 3305 1/8W |
| R680 | 0611077A26 | RES CHIP 105 1/8W |
| R681 | 0611077A74 | RES CHIP $100051 / 8$ |
| R682 | 0611077 A74 | RES CHIP $100051 / 8$ |
| R683 | 0611077 A48 | RES CHIP 825 1/8W |
| R684 | 0611077 A86 | RES CHIP $330051 / 8$ |
| R685 | 0611077A98 | RES CHIP 10 K 5 1/8W |
| R699 | 0611077A43 | RES CHIP 105 1/8W |
| R700 | 0611077A62 | RES CHIP 3305 1/8W |
| R701 | 0611077 A26 | RES CHIP 105 1/8W |
| R702 | 0611077 A98 | RES CHIP 10K $51 / 8 \mathrm{~W}^{1}$ |
| T601 | 2405548Q06 | TRANSFORMER, RF |
| T602 | 2405548Q06 | TRANSFORMER, RF |
|  | 1280954 T43 | SPECTRA VHF VCO ASSEMBLY |

1. Valid in HLD4342D only
2. Valid in HLD4343D only


UHF VCO Ranges 1, 2, 3, and 4 Hybrid Schematic
Schematics, Component Location Diagrams, and Parts Lists:

Note: These components are shown for reference only. Hybrid assemblies are not considered repairable.

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C9610 | 2111059 B 11 | CAP CHIP 21D84547A11 A/I |
| C9611 | $\begin{aligned} & \hline 2111078 \mathrm{~B} 29 \\ & 2111078 \mathrm{~B} 27 \end{aligned}$ | $\begin{aligned} & \text { CAP CHIP RF } 335 \text { NPO } \\ & 100 \mathrm{~N}^{1} \\ & \text { CAP CHIP RF } 305 \text { NPO }_{100 \mathrm{~V}^{2}} \end{aligned}$ |
| C9612 | 2111078B14 | $\begin{aligned} & \text { CAP CHIP RF } 115 \text { NPO } \\ & 100 \mathrm{~V} \end{aligned}$ |
| C9613 | 2111078B20 | $\begin{aligned} & \text { CAP CHIP RF } 185 \text { NPO } \\ & \text { 100V } \end{aligned}$ |
| C9614 | 2111078B59 | CAP CHIP RF 4705 NPO 100V |
| C9615 | 2113740B65 | CAP CHIP REEL CL1 $+/-30$ 470 |
| C9616 | 2111078B14 <br> 2111078B15 | $\begin{aligned} & \text { CAP CHIP RF } 115 \text { NPO } \\ & 100 \mathrm{~N}^{1} \\ & \text { CAP CHIP RF } 125 \text { NPO }_{100 \mathrm{~V}^{2}} \end{aligned}$ |
| C9617 | 2111078B59 | CAP CHIP RF 4705 NPO 100 V |
| C9618 | 2111059B11 | CAP CHIP 21D84547A11 A/l |
| C9619 | 2113740B65 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C9621 | $\begin{aligned} & 2113740 \mathrm{~B} 07 \\ & 2113740 \mathrm{~B} 11 \end{aligned}$ | CAP CHIP REEL CL1 $+/-30$ $1.8^{1}$ CAP CHIP REEL CL1 $+/-30$ $2.7^{2}$ |
| C9622 | $\begin{aligned} & 2113740 \mathrm{~B} 29 \\ & 2113740 \mathrm{~B} 27 \end{aligned}$ | CAP CHIP REEL CL1 $+/-30$ $15{ }^{1}$ CAP CHIP REEL CL1 $^{1 /-30}$ $12^{2}$ |
| C9623 | $2113740 B 05$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 1.5 \end{aligned}$ |
| C9624 | $2113740 B 76$ | CAP CHIP REEL CL1 $+/-30$ 1500 |
| C9625 | 2111059B11 | CAP CHIP 21D84547A11 A/I |
| C9626 | 2111078B59 | CAP CHIP RF 4705 NPO 100 V |
| C9628 | $2113740 B 65$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| $\begin{aligned} & \text { C9630 \& } \\ & \text { C9631 } \end{aligned}$ | 2111078B59 | CAP CHIP RF 4705 NPO 100 V |
| $\begin{aligned} & \text { C9632 \& } \\ & \text { C9633 } \end{aligned}$ | $2113740 B 65$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 470 \end{aligned}$ |
| C9635 | 2113740B11 | ${ }_{2.7}^{\text {CAP CHIP REEL CL1 }+/-30}$ |
| $\begin{aligned} & \hline \text { C9636 \& } \\ & \text { C9638 } \\ & \hline \end{aligned}$ | $2111059 B 11$ | CAP CHIP 21D84547A11 A/I |
| CR9610 | 4813825A05 | DIODE 30V HOT CARRIER MMBD301L |
| $\begin{aligned} & \hline \text { CR9616 \& } \\ & \text { CR9617 } \\ & \hline \end{aligned}$ | 4880006E13 | DIODE VAR SI SMBV1050 350MW |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| CR9621 | 4880006 E 13 | DIODE VAR SI SMBV1050 350MW |
| $\begin{aligned} & \hline \text { CR9626 \& } \\ & \text { CR9630 } \end{aligned}$ | 4880142L01 | DIODE PIN SOT 23 MMBV 3401 |
| $\begin{aligned} & \text { L9610 \& } \\ & \text { L9611 } \\ & \hline \end{aligned}$ | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L9616 | 2480140E04 | INDUCTOR CHIP 65 NH |
| $\begin{aligned} & \hline \text { L9618 \& } \\ & \text { L9619 } \end{aligned}$ | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L9621 | 2480140E03 | INDUCTOR CHIP $50 \mathrm{NH}^{1}$ |
| $\begin{aligned} & \text { L9628, } \\ & \text { L9632, \& } \\ & \text { L9633 } \end{aligned}$ | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| $\begin{array}{\|l\|} \hline \begin{array}{l} \text { MP9611 } \\ \text { thru } \\ \text { MP9614, } \\ \hline \end{array} \\ \hline \end{array}$ | 0780299L01 | FRAME LEAD J STRAP |
|  <br> M9619 | 0780299L01 | FRAME LEAD J STRAP |
| MP9624 \& M9625 | 0780299L01 | FRAME LEAD J STRAP |
| MP9628 | 0780299L01 | FRAME LEAD J STRAP |
| MP9632 \& M9633 | 0780299L01 | FRAME LEAD J STRAP |
| $\begin{array}{\|l\|} \hline \text { MP9636 } \\ \text { thru M9639 } \\ \hline \end{array}$ | 0780299L01 | FRAME LEAD J STRAP |
| Q9610 | 4880141L06 | TSTR NC MOS FET SOT23 TGR MMBF |
| Q9635 | 4813827A03 | TSTR NPN SML SIG MMBR901LT1 7X |
| R9611 | 0611077 A46 | RES CHIP 685 1/8W |
| R9612 | 0611077A32 | RES CHIP $1851 / 8 \mathrm{~W}$ |
| R9613 | 0611077 B07 | RES CHIP 22K 5 1/8W |
| $\begin{aligned} & \text { R9624 \& } \\ & \text { R9625 } \end{aligned}$ | 0611077 B07 | RES CHIP 22K 5 1/8W |
| R9635 | 0611077 A01 | RES CHIP JUMPER |
| R9636 | 0611077A82 | RES CHIP 22005 1/8 |
| R9637 | 0611077 A74 | RES CHIP 10005 1/8 |
| $\begin{aligned} & \text { R9638 \& } \\ & \text { R9639 } \end{aligned}$ | 0611077A50 | RES CHIP 1005 1/8W |
| RT9611 | 0680149M01 | THERMISTOR CHIP 470 OHMS |
|  | 8480034P02 | $\begin{aligned} & \text { HYBRID CKT BD UHF VCO } \\ & \text { R1 } \end{aligned}$ |

Notes:

1. Valid in HLE6101A only
2. Valid in HLE6102A only


Range 3


Range 4


PIN Switch Ano
outrut ATt ENATORS


UHF VCO Ranges 1, 2, 3, and 4 Schematic Diagram


HLE6045B Range 1 and HLE6046B Range 2 UHF VCO Component Location Diagram




## HLE6000D and HLE6041D UHF VCO Parts List

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { C5630 \& } \\ & \text { C5631 } \end{aligned}$ | 2113741N45 | $\begin{aligned} & \hline \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C5640 | 2311049 J 26 | CAP TANT CHIP 102016 |
| C5641 | 2311049A35 | CAP TANT CHIP 0.472025 |
| $\begin{aligned} & \text { C5642 \& } \\ & \text { C5643 } \end{aligned}$ | $2113740 B 76$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 1500 \end{aligned}$ |
| C5646 | 2113740B49 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 100 \end{aligned}$ |
| C5650 | 2311049A37 | CAP TANT CHIP 12020 |
| $\begin{aligned} & \text { C5651 thru } \\ & \text { C5658 } \\ & \hline \end{aligned}$ | 2113740B49 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 100 \end{aligned}$ |
| C5661 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C5662 | 2113740B28 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 13 \end{aligned}$ |
| $\begin{aligned} & \text { C5663 thru } \\ & \text { C5665 } \\ & \hline \end{aligned}$ | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \\ & \hline \end{aligned}$ |
| C5666 | 2311049 J 26 | CAP TANT CHIP 102016 |
| C5670 | 2113740B34 | CAP CHIP REEL CL1 $+/-30$ 24 |
| C5671 | 2113740B39 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 39 \end{aligned}$ |
| C5672 | $2113740 B 17$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 4.7 \end{aligned}$ |
| C5673 | $2113740 B 27$ | ${ }_{12}$ CAP CHIP REEL CL1 +/-30 |
| C5674 | 2113740B29 | CAP CHIP REEL CL1 $+/-30$ 15 |
| C5675 | 2113740B25 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 10 \end{aligned}$ |
| C5676 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \\ & \hline \end{aligned}$ |
| C5677 | 2113740B09 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 2.2 \end{aligned}$ |
| C5680 | $2113740 B 27$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 12 \end{aligned}$ |
| C5682 thru | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \\ & \hline \end{aligned}$ |
| $\begin{aligned} & \text { C5685 \& } \\ & \text { C5686 } \end{aligned}$ | 2113740B19 | CAP CHIP REEL CL1 $+/-30$ 5.6 |
| $\begin{aligned} & \text { C5687 \& } \\ & \text { C5688 } \end{aligned}$ | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| $\begin{aligned} & \text { C5690 thru } \\ & \text { C5693 } \\ & \hline \end{aligned}$ | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| $\begin{aligned} & \hline \text { C5695 thru } \\ & \text { C5699 } \\ & \hline \end{aligned}$ | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| $\begin{array}{\|l\|} \hline \text { CR5640 \& } \\ \text { CR5641 } \\ \hline \end{array}$ | 4880066M01 | DIODE SIGNAL RLS4148 |
| $\begin{array}{\|l\|} \hline \text { CR5690 \& } \\ \text { CR5691 } \\ \hline \end{array}$ | 4880154K05 | DIODE CMN CATH DUAL PIN SOT23 |
| J5641 | 0980110M01 | RECP COAX PCBMOUNT |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| J5642 | 0980212N03 | RECP COAX PCB MOUNT |
| $\begin{aligned} & \hline \text { JU5641 \& } \\ & \text { JU5642 } \\ & \hline \end{aligned}$ |  | NOTPLACED |
| JU5643 | 0611077A01 | RES CHIP JUMPER ${ }^{1}$ NOTPLACED ${ }^{2}$ |
| JU5644 | 0611077A01 | $\begin{aligned} & \text { NOTPLACED }^{1} \\ & \text { RES CHIP JUMPER }{ }^{2} \\ & \hline \end{aligned}$ |
| JU5668 | 0611077A01 | RES CHIP JUMPER |
| JU5669 | --------- | NOTPLACED |
| $\begin{array}{\|l} \hline \text { JU5680 thru } \\ \text { Ju5683 } \\ \hline \end{array}$ | 0611077A01 | RES CHIP JUMPER |
| L5660 | 2480140E04 | INDUCTOR CHIP 65 NH |
| L5670 | 2480091 G21 | COIL AIRWOUND |
| L5671 | 2480091G24 | COIL AIRWOUND |
| $\begin{aligned} & \hline \text { L5672 \& } \\ & \text { L5673 } \end{aligned}$ | 2480091 G21 | COIL AIRWOUND |
| L5674 | 2480091G06 | COIL AIRWOUND |
| L5680 | 2480091 G20 | COIL AIRWOUND |
| L5681 | 2480091 G24 | COIL AIRWOUND |
| L5682 | 2480091 G32 | COIL AIRWOUND |
| L5683 | --------- | NOTPLACED |
| MP5655 | 1180117M01 | ADHESIVE PREFORM VCO |
| MP5676 | 1580223N02 | COVER SHIELD |
| P0601 | 2880099M02 | PLUG VERTICAL |
| Q5630 | 4813827A03 | TSTR NPN SML SIG MMBR901LT1 $7 \times$ |
| $\begin{aligned} & \text { Q5640 \& } \\ & \text { Q5650 } \end{aligned}$ | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q5651 | 4880141L02 | TSTR NPN SOT23 LO PROFILE TAPE |
| Q5652 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q5653 | 4880141L02 | TSTR NPN SOT23 LO PROFILE TAPE |
| Q5660 | 4813827A03 | TSTR NPN SML SIG MMBR901LT1 $7 \times$ |
| $\begin{aligned} & \hline \text { Q5670 \& } \\ & \text { Q5680 } \end{aligned}$ | 4880182 D 50 | TSTR SI SORF 4V 750MW 960MHZ |
| Q5681 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| R5630 | 0611077A32 | RES CHIP 185 1/8W |
| R5631 | 0611077A46 | RES CHIP 685 1/8W |
| R5632 | 0611077A32 | RES CHIP 185 1/8W |
| R5633 | 0611077A50 | RES CHIP 1005 1/8W |
| R5634 | 0611077A50 | RES CHIP 1005 1/8W |
| R5635 | 0611077A82 | RES CHIP 22005 1/8 |
| R5636 | 0611077A74 | RES CHIP 10005 1/8 |
| R5639 | $\begin{aligned} & \text { 0611077A01 } \\ & \text { 0611077A94 } \end{aligned}$ | RES CHIP JUMPER ${ }^{1}$ <br> RES CHIP $680051 / 8^{2}$ |
| R5640 | 0611077A98 | RES CHIP 10K 5 1/8W |
| R5641 | 0611077A90 | RES CHIP $470051 / 8$ |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R5642 | 0611077A98 | RES CHIP 10K 5 1/8W |
| R5643 | $0611077 \mathrm{B07}$ | RES CHIP 22K $51 / 8 \mathrm{~W}^{1}$ |
|  | 0611077A90 | RES CHIP $470051 / 8^{2}$ |
| R5645 | 0611077A32 | RES CHIP 185 1/8W |
| R5646 | 0611077 B 19 | RES CHIP $68 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R5647 | 0611077A90 | RES CHIP $470051 / 8$ |
| R5648 | 0611077B31 | RES CHIP 220K $51 / 8 \mathrm{~W}$ |
| R5649 | $0611077 \mathrm{B47}$ | RES CHIP 1 MEG 5 1/8W |
| R5650 | 0611077A98 | RES CHIP 10K 5 1/8W |
| R5651 | $0611077 \mathrm{B47}$ | RES CHIP 1 MEG $51 / 8 \mathrm{~W}$ |
| R5652 | 0611077A60 | RES CHIP $27051 / 8 \mathrm{~W}$ |
| R5653 | 0611077 B 23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R5654 | 0611077A60 | RES CHIP 2705 1/8W |
| R5655 | 0611077 447 | RES CHIP 1 MEG 5 1/8W |
| R5656 | 0611077 A68 | RES CHIP $56051 / 8 \mathrm{~W}$ |
| R5657 | 0611077 B23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R5658 | 0611077A98 | RES CHIP 10K 5 1/8W |
| R5659 | 0611077 B 23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R5660 | $0611077 B 47$ | RES CHIP 1 MEG $51 / 8 \mathrm{~W}$ |
| R5661 \& R5662 | 0611077A46 | RES CHIP 685 1/8W |
| R5664 | 0611077 A82 | RES CHIP $220051 / 8$ |
| R5665 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5666 \& R5667 | 0611077A50 | RES CHIP 1005 1/8W |
| R5670 | 0611077A90 | RES CHIP $470051 / 8$ |
| R5671 \& | 0611077A74 | RES CHIP $100051 / 8$ |
| R5674 | 0611077A36 | RES CHIP 275 1/8W |
| R5675 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5676 | 0611077A68 | RES CHIP 5605 1/8W |
| R5677 | 0611077A30 | RES CHIP 155 1/8W |
| R5678 | 0611077A68 | RES CHIP $56051 / 8 \mathrm{~W}$ |
| $\begin{array}{\|l\|} \hline \text { R5679 \& } \\ \text { R5680 } \end{array}$ | 0611077A74 | RES CHIP $100051 / 8$ |
| R5681 | 0611077 A82 | RES CHIP $220051 / 8$ |
| R5682 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5683 | 0611077A46 | RES CHIP 685 1/8W |
| R5684 | 0611077A50 | RES CHIP 1005 1/8W |
| R5686 | 0611077A46 | RES CHIP 685 1/8W |
| R5687 | 0611077A50 | RES CHIP 1005 1/8W |
| R5688 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5689 | 0611077A68 | RES CHIP $56051 / 8 \mathrm{~W}$ |
| R5690 | 0611077A26 | RES CHIP 105 1/8W |
| R5691 | 0611077A68 | RES CHIP 5605 1/8W |
| $\begin{aligned} & \text { R5692 thru } \\ & \text { R5696 } \end{aligned}$ | 0611077A74 | RES CHIP $100051 / 8$ |
| R5697 | 0611077A58 | RES CHIP 2205 1/8W |
| R5698 | 0611077A40 | RES CHIP 395 1/8W |
| R5699 | 0611077A58 | RES CHIP 2205 1/8W |
|  | 3280247N01 | GASKET VCO |


| ITEM | MOTOROLA PART <br> NUMBER | DESCRIPTION |
| :--- | :--- | :--- |
|  | 8480208 N37 | BD CKT UHF VCO CAR <br> BFRPNL |
|  | 8480208 N07 | BD CKT UHF VCO CAR <br> BFR |

Notes:
HLE6000D only 2. Valid in HLE6041D only


HLF6080B 800 MHz VCO Schematic Diagram


Side 1
Side 2

HLF6080B 800 MHz VCO Component Location Diagram

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { C635 thru } \\ & \text { C637 } \\ & \hline \end{aligned}$ | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| $\begin{aligned} & \text { C638 \& } \\ & \text { C639 } \end{aligned}$ | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C640 | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C649 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C650 |  | NOTPLACED |
| C651 | 2113740B69 | CAP CHIP REEL CL1 +/-30 680 |
| C660 |  | NOTPLACED |
| $\begin{array}{\|l\|} \hline \text { C661 thru } \\ \text { C663 } \\ \hline \end{array}$ | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C665 | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C666 |  | NOTPLACED |
| $\begin{aligned} & \hline \text { C667 \& } \\ & \text { C668 } \\ & \hline \end{aligned}$ | 2113740B76 | $\begin{aligned} & \text { CAP CHIP REEL CL1 }+/-30 \\ & 1500 \end{aligned}$ |
| C669 | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C670 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| $\begin{aligned} & \text { C671 thru } \\ & \text { C674 } \\ & \hline \end{aligned}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C675 | 2113740 B 30 | CAP CHIP REEL CL1 +/-30 16 |
| C676 | 2113740B36 | CAP CHIP REEL CL1 +/-30 30 |
| C677 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C678 | 2113740B05 | CAP CHIP REEL CL1 +/-30 1.5 |
| C679 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C680 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C681 | 2113740B15 | CAP CHIP REEL CL1 + +-30 3.9 |
| C682 | 2113740B28 | CAP CHIP REEL CL1 $+/-3013$ |
| C683 | 2113740B11 | CAP CHIP REEL CL1 +/-30 2.7 |
| C684 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R } 10 \% \\ & 10000 \end{aligned}$ |
| C685 | 2111078 B27 | CAP CHIP RF 305 NPO 100V |
| C686 | 2113740B15 | CAP CHIP REEL CL1 +/-30 3.9 |
| $\begin{aligned} & \mathrm{C} 687 \text { \& } \\ & \mathrm{C} 688 \end{aligned}$ | 2113740B36 | CAP CHIP REEL CL1 +/-30 30 |
| C689 | 2113740 B 25 | CAP CHIP REEL CL1 +/-30 10 |
| $\begin{array}{\|l} \hline \text { C690 \& } \\ \text { C691 } \\ \hline \end{array}$ | 2111078 B27 | CAP CHIP RF 305 NPO 100V |
| $\begin{array}{\|l\|} \hline \text { C692 thru } \\ \text { C695 } \\ \hline \end{array}$ | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C696 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \\ & \hline \end{aligned}$ |
| C697 | 2311049A37 | CAP TANT CHIP 12020 |
| $\begin{aligned} & \hline \text { C698 \& } \\ & \text { C699 } \\ & \hline \end{aligned}$ | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| $\begin{aligned} & \text { CR675 } \\ & \text { thru } \\ & \text { CR678 } \end{aligned}$ | 4880142L01 | DIODE PIN SOT 23 MMBV 3401 |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{array}{\|l\|l} \hline \text { CR690 } \\ \text { thru } \\ \text { CR694 } \end{array}$ | 4880066M01 | DIODE SIGNAL RLS4148 |
| E9677 | 8480154P01 | BD CKT 800 VCO CARRIER |
| E9677 | 8480154P31 | BD CKT 800 VCO CARRIER PNL |
| J641 | 0980110M01 | RECP COAX PCBMOUNT |
| J642 | 0980212N03 | RECP COAX PCB MOUNT |
| JU645 | 0611077A01 | RES CHIP JUMPER |
| JU646 | --------- | NOTPLACED |
| JU647 | 0611077A01 | RES CHIP JUMPER |
| JU648 | --------- | NOTPLACED |
| L675 | 2480091 G20 | COIL AIRWOUND |
| L676 | 2480091 G06 | COIL AIRWOUND |
| L677 | 2480091 G06 | COIL AIRWOUND |
| L678 | 2480140E04 | INDUCTOR CHIP 65 NH |
| L679 | 2480091 G20 | COIL AIRWOUND |
| L680 | 2480091 G06 | COIL AIRWOUND |
| L681 | 2480140E06 | INDUCTOR CHIP 130 NH |
| L682 | 2480091 G01 | COIL AIRWOUND |
| L683 | 2480091 G20 | COIL AIRWOUND |
| L684 | 2480140E06 | INDUCTOR CHIP 130 NH |
| L685 | 2480091 G01 | COIL AIRWOUND |
| L686 | --------- | NOTPLACED |
| MP9655 | 1180117M01 | ADHESIVE PREFORM VCO |
| MP9676 | 1580223N02 | COVER SHIELD |
| P601 | 2880099M02 | PLUG VERTICAL |
| Q643 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q644 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q645 | --------- | NOTPLACED |
| Q646 | 4880141L02 | TSTR NPN SOT23 LO PROFILE TAPE |
| Q647 | 4880141L02 | TSTR NPN SOT23 LO PROFILE TAPE |
| Q660 | 4813827A03 | TSTR NPN SML SIG MMBR901LT1 7X |
| Q675 | 4880182D50 | TSTR SI SORF 4V 750MW 960 MHZ |
| Q676 | 4802000P02 | NEC SILICON TR 2SC3357 RE |
| $\begin{aligned} & \text { Q691 thru } \\ & \text { Q693 } \\ & \hline \end{aligned}$ | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| R621 | $0611077 B 47$ | RES CHIP 1 MEG 5 1/8W |
| R622 | $0611077 B 23$ | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R643 | 0611077 B 23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| $\begin{array}{\|l} \hline \text { R644 \& } \\ \text { R649 } \\ \hline \end{array}$ | 0611077A90 | RES CHIP $470051 / 8$ |
| R650 | NOTPLACED | 64AM DUMMY PART NUMBER |
| R651 | 0611077A94 | RES CHIP $680051 / 8$ |
| $\begin{array}{\|l} \hline \text { R656 \& } \\ \text { R657 } \\ \hline \end{array}$ | $0611077 B 47$ | RES CHIP 1 MEG 5 1/8W |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R662 | --------- | NOTPLACED |
| R663 | 0611077A82 | RES CHIP $220051 / 8$ |
| R664 | 0611077A74 | RES CHIP $100051 / 8$ |
| R665 \& R666 | 0611077A50 | RES CHIP 1005 1/8W |
| R668 | $0611077 B 23$ | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R669 | 0611077A54 | RES CHIP 1505 1/8W |
| R670 | 0611077A50 | RES CHIP $10051 / 8 \mathrm{~W}$ |
| R671 | 0611077A98 | RES CHIP 10K 5 1/8W |
| R672 | 0611077A90 | RES CHIP $470051 / 8$ |
| R673 | 0611077A98 | RES CHIP 10K 5 1/8W |
| R674 | 0611077A70 | RES CHIP $68051 / 8 \mathrm{~W}$ |
| R675 | 0611077A78 | RES CHIP $150051 / 8$ |
| R676 | 0611077A74 | RES CHIP $100051 / 8$ |
| R677 | 0611077A70 | RES CHIP $68051 / 8 \mathrm{~W}$ |
| R678 | 0611077A74 | RES CHIP $100051 / 8$ |
| R679 | 0611077A94 | RES CHIP $680051 / 8$ |
| R680 | 0611077A74 | RES CHIP 10005 1/8 |
| R681 | 0611077A82 | RES CHIP $220051 / 8$ |
| R682 | 0611077 B47 | RES CHIP 1 MEG 5 1/8W |
| R683 | 0611077A74 | RES CHIP $100051 / 8$ |
| R684 | 0611077 A82 | RES CHIP $220051 / 8$ |
| R685 | 0611077A74 | RES CHIP $100051 / 8$ |
| R686 | 0611077A82 | RES CHIP $220051 / 8$ |
| R687 | 0611077A50 | RES CHIP $10051 / 8 \mathrm{~W}$ |
| R688 | 0611077A46 | RES CHIP 685 1/8W |
| R689 | 0611077A50 | RES CHIP 1005 1/8W |
| R690 | 0611077A70 | RES CHIP $68051 / 8 \mathrm{~W}$ |
| R691 \& R692 | 0611077A70 | RES CHIP 6805 1/8W |
| R693 | 0611077A58 | RES CHIP 2205 186 |
| R694 | 0611077A90 | RES CHIP $470051 / 8$ |
| R695 | 0611077A74 | RES CHIP $100051 / 8$ |
| $\begin{array}{\|l} \hline \text { R696 \& } \\ \text { R697 } \\ \hline \end{array}$ | 0611077A70 | RES CHIP 6805 1/8W |
| R698 | 0611077A36 | RES CHIP 275 1/8W |
| R699 | 0611077A50 | RES CHIP $10051 / 8 \mathrm{~W}$ |
|  | 3280247N01 | GASKET VCO |

### 7.6 RX Front-End Section



axns 50 - -8

HRD6001E/6002E/6011E/6012E VHF Receiver Front-End Schematic


HRD6001E/6002E/6011E/6012E VHF Component Location Diagram

| HRD6001E VHF RXFE Parts List |  |  | ITEM | MOTOROLA PART NUMBER | DESCRIPTION | HRD6002E Parts List |  |  | ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ITEM | MOTOROLA PART NUMBER | DESCRIPTION | Q3200 \& | 4880141L01 | TSTR PNP SOT23 LO | ITEM | MOTOROLA PART NUMBER | DESCRIPTION | J3102 | 0980110M01 | RECP COAX PCBMOUNT |
|  |  |  | Q3201 |  | PROFILE TAPE |  |  |  | JU3175 | 0611077 A01 | RES CHIP JUMPER ${ }^{3}$ |
| C3100 \& C3101 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 | Q3202 | 4802000P02 | NEC SILICON TR 2SC3357 | C3100 | 2113740B31 | CAP CHIP REEL CL1 +/-30 18 | JU3177 \& |  | NOTPLACED |
| C3102 | 2113740B30 | CAP CHIP REEL CL1 +/-30 16 | R3100 | 0611077A26 | RES CHIP $1051 / 8 \mathrm{~W}$ | C3102 | 2113740B31 | CAP CHIP REEL CL1 +/-30 18 | JU3178 |  |  |
| C3103 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 | R3101 \& | 0611077A70 | RES CHIP 6805 1/8W | C3103 | 2113740B35 | CAP CHIP REEL CL1 +/-30 27 | JU3176, | 0611077A01 | RES CHIP JUMPER |
| C3104 | $2113740 B 11$ | CAP CHIP REEL CL1 +/-30 | R3125 |  |  | C3104 | $\begin{aligned} & 211374 \mathrm{~B} 009 \\ & 2113740 \mathrm{~B} 11 \end{aligned}$ | ```CAP CHIP REEL CL1 +/-30 2.2 }\mp@subsup{}{}{3 CAP CHIP REEL CL1 +/-30 2.7``` | JU3181 ${ }^{\text {a }}$ |  |  |
|  |  | 2.7 | R3126 | 0611077 A26 | RES CHIP 105 1/8W |  |  |  | JU3180 \& |  | NOTPLACED ${ }^{3}$ |
| C3105 \& | 2113740 B 09 | CAP CHIP REEL CL1 +/-30 | R3127 | 0611077A70 | RES CHIP 6805 1/8W |  |  |  | JU3181 |  |  |
| C3106 |  | 2.2 | R3128 | 0611077 A43 | RES CHIP 515 1/8W |  |  |  | JU3182 | 0611077A01 | RES CHIP JUMPER ${ }^{3}$ |
| C3107 | 2113740B11 | CAP CHIP REEL CL1 +/-30 2.7 | R3130 | 0611077A01 | RES CHIP JUMPER | $\begin{aligned} & \text { C3105 thru } \\ & \text { C3106 } \end{aligned}$ | $2113740 B 11$ | CAP CHIP REEL CL1 +/-30 2.7 | $\begin{array}{\|l\|} \hline \text { JU3209 \& } \\ \text { JU3210 } \\ \hline \end{array}$ | 0611077A01 | RES CHIP JUMPER ${ }^{2}$ |
| C3108 thru | 2111078B15 | CAP CHIP RF 125 NPO 100V | R3200 | 0611077A43 | RES CHIP $5151 / 8 \mathrm{~W}$ | C3107 | 2113740 B 13 | CAP CHIP REEL CL1 +/-30 3.3 | L3100 thru | 2480148M04 |  |
| C3110 |  |  | R3201 | 0611077A98 | RES CHIP 10 K 5118 W |  |  |  |  |  | IND 3 1/2 TURNS FER CORE 62NH |
| C3125 | 2113740B29 | CAP CHIP REEL CL1 +/-30 15 | R3202 | 06110777A43 | RES CHIP $47 \mathrm{~K} 51 / 8 \mathrm{~W}$ | C3108 | 2111078B16 | CAP CHIP RF 135 NPO 100V CAP CHIP RF 165 NPO 100 V | $\begin{array}{\|l} \hline \mathrm{L} 3125 \text { \& } \\ \mathrm{L} 3126 \end{array}$ | 2480140E06 | INDUCTOR CHIP 130 NH |
| C3126 | 2113740B35 | CAP CHIP REEL CL1 +/-30 27 | R3204 | 0611077A01 | RES CHIP JUMPER |  |  |  |  |  |  |
|  |  | CAP CHIP REEL CL1 +/-30 12 | R3206 | 0611077 A46 | RES CHIP 685 1/8W | $\begin{aligned} & \text { C3109 \& } \\ & \text { C3110 } \end{aligned}$ | 2111078B19 | CAP CHIP RF 135 NPO 100V |  | 2480091G23 | COIL AIRWOUND |
|  |  | CAP CHIP REEL CL1 +/-30 16 | R3208 | 0611077 A26 | RES CHIP 105 1/8W | C3125 | 2113740B29 | CAP CHIP REEL CL1 +/-30 15 | L3200 | 2480140 E 04 | INDUCTOR CHIP 65 NH |
| C3201 | 2113740B24 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 9.1 \end{aligned}$ | R3209 | 0611077 A72 | RES CHIP 8205 1/8W | C3126 | 2113740B35 | CAP CHIP REEL CL1 +/-30 27 |  | 2480140E06 | INDUCTOR CHIP |
|  |  |  | T3125 | 2405548Q06 | TRANSFORMER, RF | $\begin{aligned} & \text { C3150 thru } \\ & \text { C3153 } \end{aligned}$ | $2113740 B 27$ | CAP CHIP REEL CL1 +/-30 12 | L3201 | 2480140E03 | INDUCTOR CHIP 50 NH |
| C3202 \& | 2113740B49 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 100 \end{aligned}$ | T3126 | 2405548Q06 | TRANSFORMER, RF |  |  |  | L3202 | $\begin{aligned} & \hline 2480140 E 04 \\ & \text { 2480140E06 } \end{aligned}$ | INDUCTOR CHIP $65 \mathrm{NH}^{3}$ INDUCTOR CHIP $130 \mathrm{NH}^{3}$ |
| С3203 |  |  | W3101 | 3080037R05 | CABLE ASSEMBLY | C3154 | 2111078B25 | CAP CHIP RF 275 NPO 100V | L3203 | 2480140E07 | INDUCTOR CHIP $680 \mathrm{NH}^{3}$INDUCTOR CHIP $130 \mathrm{NH}^{3}$ |
| C3205 thru | 2113741N45 | CAP CHIP CL2 X7R $10 \%$10000 | W3103 | 3080152M07 | CBL COAX ASSEMBLY <br> SPECTRA VHF FRONT END ASSEM |  | 2113740B25 |  |  | 2480140 E06 |  |
| C3208 |  |  |  |  |  |  |  |  | L3204 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| C3209 \& | 0611077A01 | RES CHIP JUMPER |  |  |  |  |  | CAP CHIP REEL CL1 +/-30 16 | M3000 \&M3001 | 2980014A03 | CLIP COAX TERMINAL ${ }^{3}$ |
| C3210 |  |  |  | MXW7635 | HRD6001B/6011B PL | C3201 | 2113740B30 | CAP CHIP REEL CL1 +/-30 $8.2^{3}$ <br> CAP CHIP REEL CL1 +/-30 <br> 9.1 |  |  |  |
| CR3125 | $\begin{array}{\|l\|l\|} \hline 4880236 \mathrm{E} 17 \\ \hline 4880154 \mathrm{~K} 03 \\ \hline \end{array}$ | DIODE QUAD RING |  | 8480067 S32 | PCB VHF RX PNL |  | 2113740B24 |  | Q3200 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| CR3200 \& |  | DIODE DUAL SCHOTTKYMIXER |  | 5480072 G 01 | LABEL BD CKT PRTD |  |  |  |  |  |  |
| CR3201 |  |  |  |  |  | C3202 | $\begin{aligned} & \text { 2113740B49 } \\ & \text { 2113740B65 } \end{aligned}$ | CAP CHIP REEL CL1 +/-30100CAP CHIP REEL CL1 $+/-30$$470^{3}$ | Q3201 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| $\begin{array}{\|l} \hline \text { E3103 \& } \\ \text { E3104 } \end{array}$ | 2980014A03 | CLIP COAX TERMINAL |  |  |  |  |  |  |  |  |  |
|  <br> E3106 | 2680003M03 | SHLD HI IF TIN PLATED |  |  |  |  |  |  | Q3202 | $\begin{aligned} & \hline 4882971 R 01 \\ & 4802000 \mathrm{P} 02 \end{aligned}$ | TSTR NPN 71R01 RF NEC SILICON TR 2SC3357 RE ${ }^{7}$ |
| E3107 | 2680004M03 | SHLD OUTPUT MATCH TIN PLATED |  |  |  | C3203 | $2113740 B 49$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 }+/-30 \\ & 100 \end{aligned}$ | $\begin{array}{\|l\|} \hline \text { R3100 } \\ \hline \text { R3101 } \\ \hline \text { R3125 } \end{array}$ | 0611077A26 | RES CHIP 105 1/8W |
|  |  |  |  |  |  | $\begin{aligned} & \text { C3205 thru } \\ & \text { C3208 } \\ & \hline \end{aligned}$ |  |  |  |  |  |
| J3102 | 0980110M01 | RECP COAX PCBMOUNT |  |  |  |  | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \\ & \hline \end{aligned}$ |  | $\begin{aligned} & \text { 0611077A70 } \\ & \hline \text { 0611077A70 } \\ & \text { 0611077A68 } \end{aligned}$ | RES CHIP 6805 1/8W <br> RES CHIP $56051 / 8 W^{3}$ |
| JU3175 \& | 0611077A01 | RES CHIP JUMPER |  |  |  |  |  |  |  |  |  |
| JU3178 |  |  |  |  |  | C3209 | 2113740B31 | ${ }_{18}{ }^{3}$ CAP CHIP REEL CL1 +/-30 | R3126 <br> R3127 | 0611077A26 | RES CHIP $1051 / 8 \mathrm{~W}$ |
|  | 2480148M07 | RES CHIP JUMPER <br> IND 4 1/2 TURNS FER CORE 82NH |  |  |  | C3210 | 2113740 B 32 | ${ }_{3}^{\text {CAP CHIP REEL CL1 +/-30 } 20}$ |  | $\begin{array}{\|l\|l\|l\|} \hline 0611077 A 70 \\ \text { 0611077A68 } \\ \hline \end{array}$ | RES CHIP 6805 1/8W RES CHIP $56051 / 8 W^{3}$ |
| L3100 thru |  |  |  |  |  | CR3125 |  | DIODE QUAD RING ${ }^{1}$ DIODE DUAL SCHOTTKY MIXER ${ }^{4}$ | R3128 | 0611077A43 | RES CHIP 515 1/8W |
| L3104 |  |  |  |  |  | $\begin{aligned} & \text { CR3126 \& } \\ & \text { C3127 } \end{aligned}$ | 4880236E17 |  | $\begin{array}{\|l\|} \hline \text { R3130 } \\ \hline \text { R31311 \& } \\ \text { R3132 } \end{array}$ | 0611077A01 |  |
| $\begin{aligned} & \hline \text { L3125 \& } \\ & \text { L3126 } \end{aligned}$ | 2480140E06 | INDUCTOR CHIP 130 NH |  |  |  |  |  |  |  |  | NOTPLACED ${ }^{3}$ |
|  | 2480091 G23 | COIL AIRWOUND |  |  |  | $\begin{aligned} & \text { CR3200 \& } \\ & \text { CR3201 } \\ & \hline \end{aligned}$ | 4880154K03 | DIODE DUAL SCHOTTKY MIXER ${ }^{4}$ | R3200 | 0611077A43 | RES CHIP 515 1/8W |
|  | 2480140E04 | INDUCTOR CHIP 65 NH |  |  |  | $\begin{aligned} & \hline \text { E3103 \& } \\ & \text { E3104 } \\ & \hline \end{aligned}$ | 2980014A03 | CLIP COAX TERMINAL ${ }^{6}$ | R3201 |  |  |
| L3201 | 2480140E03 | INDUCTOR CHIP 50 NH |  |  |  |  |  |  | R3202 | 0611077B15 | RES CHIP $47 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| L3202 | 2480140E04 | INDUCTOR CHIP 65 NH |  |  |  | $\begin{array}{\|l\|l\|} \hline \text { E3105 \& } \\ \text { E3106 } \\ \hline \end{array}$ | 2680003M03 | SHLD HIIF TIN PLATED ${ }^{6}$ | R3203 <br> R3204 |  | RES CHIP 515 1/8W |
| L3203 |  |  |  |  |  |  |  |  |  | 0611077A01 | RES CHIP JUMPER |
| L3204 | $\begin{array}{\|l\|} \hline 2480140 \mathrm{E} 07 \\ \hline 2480140 \mathrm{E} 01 \\ \hline \end{array}$ | INDUCTOR CHIP 1.2 UH |  |  |  | E3107 | 2680004M03 | SHLD OUTPUT MATCH TIN | R3205 | 0611077A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
|  |  |  |  |  |  |  |  | PLATED ${ }^{6}$ | R3206 | 0611077A36 | RES CHIP $2751 / 8 \mathrm{~W}^{5}$ |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R3208 | $\begin{aligned} & \hline \text { 0611077A26 } \\ & \text { 0611077A32 } \end{aligned}$ | $\begin{aligned} & \text { RES CHIP } 105 \text { 1/8W } \\ & \text { RES CHIP } 1851 / 8 \mathrm{~W}{ }^{1} \end{aligned}$ |
| R3209 | $\begin{aligned} & \text { 0611077A68 } \\ & \text { 0611077A72 } \end{aligned}$ | RES CHIP 5605 1/8W RES CHIP $82051 / 8 W^{4}$ |
| R3210 | 0611077A68 | RES CHIP 5605 1/8W ${ }^{4}$ RES CHIP $82051 / 8 W^{3}$ |
| SH1 | 2680003M03 | SHLD HI IF TIN PLATED ${ }^{3}$ |
| SH2 | 2680004M03 | SHLD OUTPUT MATCH TIN PLATED ${ }^{3}$ |
| SH3 | 2680003M03 | SHLD HI IF TIN PLATED ${ }^{3}$ |
| T3125 | 2405548Q06 | TRANSFORMER, $\mathrm{RF}^{6}$ |
| T3126 | 2405548Q06 | TRANSFORMER, RF |
| T3127 | 2405548Q06 | TRANSFORMER, $\mathrm{RF}^{3}$ |
| W3101 | $3080037 \mathrm{R05}$ | CABLE ASSEMBLY ${ }^{6}$ |
| W3103 | 3080152M07 | CBL COAX ASSEMBLY ${ }^{6}$ |
|  | 1280954 T42 | SPECTRA VHF FRONT END ASSEM |

Notes:

1. Valid in HRD6002C only.
2. Valid in HRD6002E only.
3. Valid in HRD6002G only.
4. Not valid in HRD6002E
5. Not valid in HRD6002E
6. Not valid in HRD6002G
7. Valid in HRD6002E and HRD6002G only.

HRD6011E Parts List

| ITEM | MOTOROLA PART NUBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { C3100 \& } \\ & \text { C3101 } \end{aligned}$ | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C3102 | 2113740 B 30 | CAP CHIP REEL CL1 +/-30 16 |
| C3103 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C3104 | 2113740 B 11 |  |
| $\begin{aligned} & \text { C3105 \& } \\ & \text { C3106 } \end{aligned}$ | 2113740B09 | CAP CHIP REEL CL1 $+/-30$ 2.2 |
| C3107 | 2113740B11 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 2.7 \end{aligned}$ |
| C3108 | 2111078B16 | CAP CHIP RF 135 NPO 100V |
| C3109 | 2111078 B15 | CAP CHIP RF 125 NPO 100V |
| C3110 | 2111078 B16 | CAP CHIP RF 135 NPO 100V |
| C3125 | 2113740B29 | CAP CHIP REEL CL1 +/-30 15 |
| C3126 | 2113740B35 | CAP CHIP REEL CL1 +/-30 27 |
| $\begin{array}{\|l} \text { C3150 } \\ \text { thru } \\ \text { C3153 } \end{array}$ | $2113740 B 27$ | CAP CHIP REEL CL1 +/-30 12 |
| C3154 | 2111078 B25 | CAP CHIP RF 275 NPO 100V |
| C3200 | 2113740B30 | CAP CHIP REEL CL1 +/-30 16 |
| C3201 | 2113740B24 | CAP CHIP REEL CL1 $+/-30$ 9.1 |


| ITEM | MOTOROLA PART NUBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { C3202 \& } \\ & \text { C3203 } \end{aligned}$ | $2113740 B 49$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 100 \end{aligned}$ |
| $\begin{aligned} & \text { C3205 } \\ & \text { thru } \\ & \text { C3208 } \end{aligned}$ | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| $\begin{aligned} & \hline \text { C3209 \& } \\ & \text { C3210 } \\ & \hline \end{aligned}$ | 0611077A01 | RES CHIP JUMPER ${ }^{1}$ |
| CR3125 | 4880236E17 | DIODE QUAD RING |
| CR3200 | 4880154K03 | DIODE DUAL SCHOTTKY MIXER |
| CR3201 | 4880154K03 | $\begin{aligned} & \text { DIODE DUAL SCHOTTKY } \\ & \text { MIXER } 1 \end{aligned}$ |
| $\begin{aligned} & \hline \text { E3103 \& } \\ & \text { E3104 } \\ & \hline \end{aligned}$ | 2980014A03 | CLIP COAX TERMINAL |
| $\begin{array}{\|l} \hline \text { E3105 \& } \\ \text { E3106 } \end{array}$ | 2680003M03 | SHLD HI IF TIN PLATED |
| E3107 | 2680004M03 | SHLD OUTPUT MATCH TIN PLATED |
| J3102 | 0980110M01 | RECP COAX PCBMOUNT |
| JU3177 \& JU3178 | 0611077A01 | RES CHIP JUMPER |
| JU3180 \& JU3182 | 0611077A01 | RES CHIP JUMPER |
| $\begin{array}{\|l\|} \hline \text { L3100 thru } \\ \text { L3104 } \end{array}$ | 2480148M07 | IND 4 1/2 TURNS FER CORE 82NH |
| $\begin{aligned} & \hline \text { L3125 \& } \\ & \text { L3126 } \\ & \hline \end{aligned}$ | 2480140E06 | INDUCTOR CHIP 130 NH |
| $\begin{aligned} & \hline \text { L3150 \& } \\ & \text { L3151 } \\ & \hline \end{aligned}$ | 2480091G23 | COIL AIRWOUND |
| L3200 | 2480140E04 | INDUCTOR CHIP 65 NH |
| L3201 | 2480140E03 | INDUCTOR CHIP 50 NH |
| L3202 | 2480140E04 | INDUCTOR CHIP 65 NH |
| L3203 | 2480140E07 | INDUCTOR CHIP 680 NH |
| L3204 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| $\begin{aligned} & \text { Q3200 \& } \\ & \text { Q3201 } \end{aligned}$ | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q3202 | 4882971R01 | TSTR NPN 71R01 RF |
| $\begin{aligned} & \text { R3100 \& } \\ & \text { R3126 } \end{aligned}$ | 0611077A01 | RES CHIP JUMPER |
| R3128 | 0611077A43 | RES CHIP 515 1/8W |
| R3130 | 0611077A01 | RES CHIP JUMPER |
| R3201 | 0611077A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R3202 | 0611077B15 | RES CHIP $47 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R3203 | 0611077A43 | RES CHIP 515 1/8W |
| R3204 | 0611077A01 | RES CHIP JUMPER |
| R3205 | 0611077A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| $\begin{aligned} & \hline \text { R3206 \& } \\ & \text { R3208 } \end{aligned}$ | 0611077A26 | RES CHIP 105 1/8W |
| R3209 | 0611077A72 | RES CHIP 8205 1/8W |
| R3210 | 0611077A72 | RES CHIP $82051 / 8 W^{2}$ |
| T3125 | 2405548Q06 | TRANSFORMER, RF |
| T3126 | 2405548Q06 | TRANSFORMER, RF |
| W3101 | 3080037R05 | CABLE ASSEMBLY |


| ITEM | MOTOROLA <br> PART NUBER | DESCRIPTION |
| :---: | :---: | :--- |
| W3103 | 3080152 M07 | CBL COAX ASSEMBLY |
|  | $1280954 T 42$ | SPECTRA VHF FRONT END <br> ASSEM |

Notes:

1. Valid in HRD6011D only
2. Valid in HRD6011C only

HRD6012E Parts List

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C3100 | 2113740B31 | CAP CHIP REEL CL1 +/-30 18 |
| C3101 | 2113740B35 | CAP CHIP REEL CL1 +/-30 27 |
| C3102 | 2113740B31 | CAP CHIP REEL CL1 +/-30 18 |
| C3103 | 2113740B35 | CAP CHIP REEL CL1 +/-30 27 |
| C3104 | 2113740B09 | CAP CHIP REEL CL1 $+/-30$ 2. $2^{3}$ CAP CHIP REEL CL1 $+/-30$ 2.7 |
| $\begin{aligned} & \text { C3105 thru } \\ & \text { C3106 } \\ & \hline \end{aligned}$ | 2113740B11 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 2.7 \end{aligned}$ |
| C3107 | 2113740B13 | CAP CHIP REEL CL1 $+/-30$ 3.3 |
| C3108 | 2111078B16 <br> 2111078B19 | CAP CHIP RF 135 NPO 100V 3 CAP CHIP RF 165 NPO 100 V |
| $\begin{aligned} & \hline \text { C3109 \& } \\ & \text { C3110 } \\ & \hline \end{aligned}$ | 2111078B16 | CAP CHIP RF 135 NPO 100V |
| C3125 | 2113740B29 | CAP CHIP REEL CL1 +/-30 15 |
| C3126 | $2113740 B 35$ | CAP CHIP REEL CL1 +/-30 27 |
| $\begin{aligned} & \text { C3150 thru } \\ & \text { C3153 } \end{aligned}$ | 2113740B27 | CAP CHIP REEL CL1 +/-30 12 |
| C3154 | 2111078 B25 | CAP CHIP RF 275 NPO 100V |
| C3200 | 2113740 B25 2113740B30 | CAP CHIP REEL CL1 +/-30 10 3 CAP CHIP REEL CL1 $+/-3016$ |
| C3201 | $\begin{array}{\|l\|} \hline 2113740 \mathrm{~B} 24 \\ 2113740 \mathrm{~B} 25 \end{array}$ | CAP CHIP REEL CL1 +/-30 9.1 <br> ${ }_{3}^{\text {CAP CHIP REEL CL1 +/-30 }} 10$ |
| C3202 | $\begin{array}{\|l\|} \hline 2113740 B 49 \\ 2113740 B 65 \end{array}$ | CAP CHIP REEL CL1 $+/-30$ 100 CAP CHIP REEL CL1 $+/-30$ $470^{3}$ |
| C3203 | 2113740B49 | CAP CHIP REEL CL1 $+/-30$ 100 |
| $\begin{array}{\|l\|} \hline \text { C3205 thru } \\ \text { C3208 } \end{array}$ | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C3209 | $\begin{aligned} & \text { 0611077A01 } \\ & 2113740 \mathrm{~B} 31 \end{aligned}$ | RES CHIP JUMPER <br> ${ }_{3}$ CAP CHIP REEL CL1 +/-30 18 |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C3210 | 0611077A01 2113740B32 | $\begin{aligned} & \hline \text { RES CHIP JUMPER } \\ & \text { CAP CHIP REEL CL1 +/-30 } 20 \\ & 3 \end{aligned}$ |
| CR3125 | 4880236E17 | DIODE QUAD RING ${ }^{1}$ |
| $\begin{aligned} & \text { CR3126 \& } \\ & \text { CR3127 } \end{aligned}$ | 4880154K03 | $\begin{aligned} & \text { DIODE DUAL SCHOTTKY } \\ & \text { MIXER } 4 \end{aligned}$ |
| $\begin{aligned} & \text { CR3200 \& } \\ & \text { CR3201 } \end{aligned}$ | 4880154K03 | DIODE DUAL SCHOTTKY MIXER |
| $\begin{aligned} & \text { E3103 \& } \\ & \text { E3104 } \end{aligned}$ | 2980014A03 | CLIP COAX TERMINAL ${ }^{5}$ |
| $\begin{array}{\|l} \hline \text { E3105 \& } \\ \text { E3106 } \\ \hline \end{array}$ | 2680003M03 | SHLD HIIF TIN PLATED ${ }^{5}$ |
| E3107 | 2680004M03 | SHLD OUTPUT MATCH TIN PLATED ${ }^{5}$ |
| J3102 | 0980110M01 | RECP COAX PCBMOUNT |
| JU3175 | --------- | NOTPLACED ${ }^{3}$ |
| JU3176 \& JU3177 | 0611077A01 | RES CHIP JUMPER |
| JU3178 |  | NOTPLACED ${ }^{3}$ |
| JU3179 | 0611077A01 | RES CHIP JUMPER |
| JU3180 | ---- | NOTPLACED ${ }^{3}$ |
| JU3181 | 0611077A01 | RES CHIP JUMPER ${ }^{3}$ |
| JU3182 | 0611077A01 | $\begin{aligned} & \text { RES CHIP JUMPER } \\ & \text { NOTPLACED }{ }^{3} \end{aligned}$ |
| $\begin{array}{\|l\|} \hline \text { L3100 thru } \\ \text { L3104 } \\ \hline \end{array}$ | 2480148M04 | IND 3 1/2 TURNS FER CORE 62NH |
| $\begin{aligned} & \hline \text { L3125 \& } \\ & \text { L3126 } \end{aligned}$ | 2480140E06 | INDUCTOR CHIP 130 NH |
| $\begin{aligned} & \hline \begin{array}{l} L 3150 ~ \& ~ \\ L 3151 \end{array} \\ & \hline \end{aligned}$ | 2480091 G23 | COIL AIRWOUND |
| L3200 | $\begin{aligned} & 2480140 E 04 \\ & 2480140 E 06 \end{aligned}$ | INDUCTOR CHIP $65 \mathrm{NH}^{3}$ INDUCTOR CHIP $130 \mathrm{NH}^{3}$ |
| L3201 | 2480140E03 | INDUCTOR CHIP 50 NH |
| L3202 | $\begin{array}{\|l\|} \hline 2480140 \mathrm{E} 04 \\ 2480140 \mathrm{E} 06 \\ \hline \end{array}$ | INDUCTOR CHIP $65 \mathrm{NH}^{3}$ INDUCTOR CHIP $130 \mathrm{NH}^{3}$ |
| L3203 | $\begin{aligned} & 2480140 \text { E06 } \\ & 2480140 E 07 \\ & \hline \end{aligned}$ | INDUCTOR CHIP $130 \mathrm{NH}^{3}$ INDUCTOR CHIP 680 NH |
| L3204 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
|  <br> M3001 | 2980014A03 | CLIP COAX TERMINAL ${ }^{3}$ |
| $\begin{aligned} & \text { Q3200 \& } \\ & \text { Q3201 } \\ & \hline \end{aligned}$ | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q3202 | $\begin{array}{\|l} \hline 4880141 \mathrm{~L} 01 \\ 4802000 \mathrm{P} 02 \\ 4882971 \mathrm{R} 01 \end{array}$ | ```TSTR PNP SOT23 LO PROFILE TAPE2 NEC SILICON TR 2SC3357 RE 3 TSTR NPN 71R01 RF }\mp@subsup{}{}{1``` |
| $\begin{aligned} & \text { R3101, } \\ & \text { R31127 \& } \\ & \text { R3125 } \\ & \hline \end{aligned}$ |  | NOTPLACED ${ }^{3}$ |
| $\begin{aligned} & \text { R3100 \& } \\ & \text { R3126 } \end{aligned}$ | 0611077A01 | RES CHIP JUMPER |
| R3128 | 0611077A43 | RES CHIP 515 1/8W |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R3130 | 0611077A01 | RES CHIP JUMPER |
| $\begin{aligned} & \text { R3131 \& } \\ & \text { R3132 } \end{aligned}$ |  | NOTPLACED ${ }^{3}$ |
| R3200 | --------- | NOTPLACED ${ }^{3}$ |
| R3201 | 0611077 A 98 | RES CHIP 10K $51 / 8 \mathrm{~W}$ |
| R3202 | 0611077 B 15 | RES CHIP 47K $51 / 8 \mathrm{~W}$ |
| R3203 | 0611077A43 | RES CHIP 515 1/8W |
| R3204 | 0611077 A 01 | RES CHIP JUMPER |
| R3205 | 0611077A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R3206 | $\begin{aligned} & \hline \text { 0611077A10 } \\ & \text { 0611077A26 } \end{aligned}$ | RES CHIP 2.25 1/8W ${ }^{3}$ RES CHIP $1051 / 8 \mathrm{~W}$ |
| R3208 | 0611077A26 | RES CHIP 105 1/8W |
| R3209 | 0611077A72 | RES CHIP 8205 1/8W |
| R3210 | --------- | NOTPLACED ${ }^{3}$ |
| SH1 | 2680003M03 | SHLD HII IF TIN PLATED ${ }^{3}$ |
| SH2 | 2680004M03 | SHLD OUTPUT MATCH TIN PLATED ${ }^{3}$ |
| SH3 | 2680003M03 | SHLD HI IF TIN PLATED ${ }^{3}$ |
| T3125 | 2405548Q06 | TRANSFORMER, $\mathrm{RF}^{5}$ |
| T3126 | 2405548Q06 | TRANSFORMER, RF |
| T3127 | 2405548Q06 | TRANSFORMER, $\mathrm{RF}^{3}$ |
| W3101 | 3080037R05 | CABLE ASSEMBLY ${ }^{5}$ |
| W3103 | 3080152M07 | CBL COAX ASSEMBLY ${ }^{5}$ |
|  | 8480067S02 | PCB VHF RX ${ }^{1}$ |
|  | 8480067 S32 | PCB VHF RX PNL ${ }^{2}$ |
|  | 1280954 T42 | SPECTRA VHF FRONT END ASSEM |

Notes:

1. Valid in HRD6012D only.
2. Valid in HRD6012E only.
3. Valid in HRD6012G only.
4. Noy valid in HRD6012G.



HRD6001G/6002G/6011G/6012G VHF Receiver Front-End Component Location Diagram




HRE6001B/6002C/6003B/6004B/6011B/6012B/6014B UHF Receiver Front-End Hybrid Component Location Diagram

| HRE6011B UHF RXFE Pre-Amp Parts List |  |  |
| :---: | :---: | :---: |
| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| C100 | $2113740 B 76$ | CAP CHIP REEL CL1 +/-30 1500 |
| C101 | 2113740B19 | CAP CHIP REEL CL1 $+/-30$ 5.6 |
| C102 | 2113740B76 | CAP CHIP REEL CL1 +/-30 1500 |
| C103 | $2113740 B 48$ | CAP CHIP REEL CL1 +/-30 91 |
| C126 | 2113740 B11 | CAP CHIP REEL CL1 +/-30 2.7 |
| $\begin{array}{\|l} \hline \begin{array}{l} \text { C127 } \\ \text { thru } \\ \text { C129 } \end{array} \\ \hline \end{array}$ | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C130 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C131 | $2113740 B 76$ | CAP CHIP REEL CL1 +/-30 1500 |
| C132 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| $\begin{array}{\|l\|l\|l\|} \hline \text { C133 } \\ \text { thru } \\ \text { C135 } \\ \hline \end{array}$ | 2113740 B 17 | CAP CHIP REEL CL1 $+/-30$ 4.7 |
| C136 | $2113740 B 46$ | CAP CHIP REEL CL1 +/-30 75 |
| C137 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C138 | $2113740 B 17$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 4.7 \end{aligned}$ |
| CR100 | 4882958R78 | DIODE 48R82958R01 A/P 5.6 V |
| E125 | 2680015P02 | SHLD RXFE UHF TIN PLATED |
| E126 | 2680002M02 | SHLD INJ FLTR TIN PLATED |
| HY126 | 9180209N01 | FLTR BANDPASS CERAMIC 417 MHZ |
| JU5125 | --------- | PART NOT USED |
| L125 | 2480140E10 | INDUCTOR CHIP 100NH |
| $\begin{aligned} & \hline \text { L126 \& } \\ & \hline \text { L127 } \\ & \hline \end{aligned}$ | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L128 | 2480091 G21 | COIL AIRWOUND |
| L129 | 2480091 G33 | COIL AIRWOUND |
| MP126 | 0780091P01 | BRACKET RX FE UHF |
| MP127 | 0780214N01 | FRAME PRESELECTOR LEAD |
| MP128 | 4280191N01 | CLIP RECEIVER GROUNDING |
| MP129 | 4280013P01 | RETAINER WIRE |
| MP130 | 4380025P01 | SPACER WIRE |
| $\begin{aligned} & \hline \text { Q100 \& } \\ & \text { Q125 } \\ & \hline \end{aligned}$ | 4802000P02 | NEC SILICON TR 2SC3357 RE |
| Q126 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| $\begin{array}{\|l\|} \hline \text { R100 \& } \\ \text { R101 } \\ \hline \end{array}$ | 0611077A26 | RES CHIP 105 1/8W |
| R102 | 0611077A70 | RES CHIP 6805 1/8W |



HRE6012B UHF RXFE Pre-Amp Parts List

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C100 | $2113740 B 76$ | $\begin{aligned} & \hline \hline \text { CAP CHIP REEL CL1 +/-30 } \\ & 1500 \end{aligned}$ |
| C101 | 2113740B19 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 5.6 \end{aligned}$ |
| C102 | $2113740 B 76$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 1500 \end{aligned}$ |
| C103 | $2113740 B 48$ | CAP CHIP REEL CL1 +/-30 91 |
| C126 | 2113740B09 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 2.2 \end{aligned}$ |
| $\begin{aligned} & \text { C127 thru } \\ & \text { C129 } \\ & \hline \end{aligned}$ | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C130 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C131 | 2113740B76 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 1500 \end{aligned}$ |
| C132 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C132 | 2113740B76 | CAP CHIP REEL CL1 $+/-30$ 1500 |
| $\begin{aligned} & \hline \text { C133 \& } \\ & \text { C134 } \\ & \hline \end{aligned}$ | 2113740 B 13 | CAP CHIP REEL CL1 +/-30 3.3 |
| C135 | 2113740 B 13 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 3.3 \end{aligned}$ |
| C136 | $2113740 B 46$ | CAP CHIP REEL CL1 +/-30 75 |
| C137 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R } 10 \% \\ & 10000 \end{aligned}$ |
| C138 | 2113740B09 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 2.2 \end{aligned}$ |
| CR100 | 4882958R78 | DIODE 48R82958R01 A/P 5.6V |
| E125 | 2680015P02 | SHLD RXFE UHF TIN PLATED |
| E126 | 2680002M02 | SHLD INJ FLTR TIN PLATED |
| HY126 | 9180209N02 | FLTR BANDPASS CERAMIC 455 MHZ |
| L125 | 2480140E10 | INDUCTOR CHIP 100NH |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{array}{\|l\|} \hline \text { L126 \& } \\ \text { L127 } \\ \hline \end{array}$ | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L128 | 2480091 G21 | COIL AIRWOUND |
| L129 | 2480091 G33 | COIL AIRWOUND |
| MP126 | 0780091P01 | BRACKET RX FE UHF |
| MP127 | 0780214N01 | FRAME PRESELECTOR LEAD |
| MP128 | 4280191N01 | CLIP RECEIVER GROUNDING |
| MP129 | 4280013P01 | RETAINER WIRE |
| MP130 | 4380025P01 | SPACER WIRE |
| $\begin{aligned} & \text { Q100 \& } \\ & \text { Q125 } \end{aligned}$ | 4802000P02 | NEC SILICON TR 2SC3357 RE |
| Q126 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| $\begin{array}{\|l} \hline \text { R100 \& } \\ \text { R101 } \end{array}$ | 0611077 A26 | RES CHIP 105 1/8W |
| R102 | 0611077A70 | RES CHIP 6805 1/8W |
| R103 | 0611077A62 | RES CHIP $33051 / 8 \mathrm{~W}$ |
| R104 | 0611077A43 | RES CHIP 515 1/8W |
| R126 | 0611077A74 | RES CHIP $100051 / 8$ |
| R127 | 0611077A36 | RES CHIP 275 1/8W |
| R128 | 0611077A52 | RES CHIP 1205 1/8W |
| R129 | 0611077A80 | RES CHIP $180051 / 8$ |
| R130 | 0611077 A94 | RES CHIP $680051 / 8$ |
| R131 | 0611077 A50 | RES CHIP 1005 1/8W |
| R132 | 0611077 A43 | RES CHIP 515 1/8W |
| W128 | 3080152M07 | CBL COAX ASSEMBLY |
|  | 0980212N02 | RECP COAX PCB MOUNT |
|  | 3080037 R 05 | CABLE ASSEMBLY |
|  | 0180757T43 | SHLD W/CONTACT |
|  | 8480241N02 | HYBRID CKT BD UHF F/E |

## HRE6014B UHF RXFE Pre-Amp Parts List

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C100 | $2113740 B 76$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 1500 \end{aligned}$ |
| C101 | 2113740B19 | CAP CHIP REEL CL1 $+/-30$ 5.6 |
| C102 | $2113740 B 76$ | CAP CHIP REEL CL1 $+/-30$ 1500 |
| C103 | $2113740 B 48$ | CAP CHIP REEL CL1 +/-30 91 |
| C126 | 2113740B09 | CAP CHIP REEL CL1 $+/-30$ 2.2 |
| $\begin{aligned} & \text { C127 thru } \\ & \text { C129 } \end{aligned}$ | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C130 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C131 | 2113740B76 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 1500 \end{aligned}$ |
| C132 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { C133 thru } \\ & \text { C135 } \\ & \hline \end{aligned}$ | $2113740 B 13$ | CAP CHIP REEL CL1 +/-30 3.3 |
| C136 | $2113740 B 46$ | CAP CHIP REEL CL1 +/-30 75 |
| C137 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C138 | 2113740B09 | CAP CHIP REEL CL1 $+/-30$ 2.2 |
| CR100 | 4882958R78 | DIODE 48R82958R01 A/P 5.6 V |
| E125 | 2680015P02 | SHLD RXFE UHF TIN PLATED |
| E126 | 2680002M02 | SHLD INJ FLTR TIN PLATED |
| HY126 | 9180209N04 | FLTR BANDPASS CERAMIC 497 MHZ |
| L125 | 2480140E10 | INDUCTOR CHIP 100NH |
| $\begin{array}{\|l\|l} \hline \text { L126 \& } \\ \text { L127 } \\ \hline \end{array}$ | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L128 | 2480091G21 | COIL AIRWOUND |
| L129 | 2480091G33 | COIL AIRWOUND |
| MP126 | 0780091P01 | BRACKET RX FE UHF |
| MP127 | 0780214N01 | FRAME PRESELECTOR LEAD |
| MP128 | 4280191N01 | CLIP RECEIVER GROUNDING |
| MP129 | 4280013P01 | RETAINER WIRE |
| MP130 | 4380025P01 | SPACER WIRE |
| $\begin{aligned} & \hline \text { Q100 \& } \\ & \text { Q125 } \\ & \hline \end{aligned}$ | 4802000P02 | $\underset{\text { RE }}{\text { NEC SILICON TR 2SC3357 }}$ |
| Q126 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| R100 | 0611077 A 26 | RES CHIP $1051 / 8 \mathrm{~W}$ |
| R101 | 0611077A26 | RES CHIP 105 1/8W |
| R102 | 0611077A70 | RES CHIP 6805 1/8W |
| R103 | 0611077A62 | RES CHIP 3305 1/8W |
| R104 | 0611077A43 | RES CHIP 515 1/8W |
| R126 | 0611077A74 | RES CHIP 10005 1/8 |
| R127 | 0611077A36 | RES CHIP 275 1/8W |
| R128 | 0611077A52 | RES CHIP 1205 1/8W |
| R129 | 0611077A80 | RES CHIP 18005 1/8 |
| R130 | 0611077A94 | RES CHIP $680051 / 8$ |
| R131 | 0611077A50 | RES CHIP 1005 1/8W |
| R132 | 0611077 A43 | RES CHIP $5151 / 8 \mathrm{~W}$ |
| W128 | 3080152M07 | CBL COAX ASSEMBLY |
| W642 | 3080037 R 05 | CABLE ASSEMBLY |
|  | 0980212N02 | RECP COAX PCB MOUNT |
|  | 8480241N02 | HYBRID CKT BD UHF F/E |

## HRE6001B and HRE6002C UHF RXFE Standard Parts

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C125 | $2113740 B 76$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 1500 \end{aligned}$ |
| C126 | $\begin{aligned} & 2113740 B 09 \\ & 2113740 B 11 \end{aligned}$ | CAP CHIP REEL CL1 $+/-30$ $2.7^{2}$ CAP $^{2}$ CHIP REEL CL1 $+/-30$ $2.7^{1}$ |
| C127 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R } 10 \% \\ & 10000^{1} \end{aligned}$ |
| $\begin{aligned} & \hline \text { C128 \& } \\ & \text { C129 } \\ & \hline \end{aligned}$ | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 10000 \end{aligned}$ |
| C130 | 2113740 B 34 | CAP CHIP REEL CL1 +/-30 24 |
| C131 | $2113740 B 76$ | CAP CHIP REEL CL1 +/-30 1500 |
| C132 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| $\begin{array}{\|l} \hline \text { C133 thru } \\ \text { C135 } \end{array}$ | $\begin{aligned} & 2113740 \mathrm{~B} 13 \\ & 2113740 \mathrm{~B} 17 \end{aligned}$ | CAP CHIP REEL CL1 +/-30 $3.3^{2}$ <br> CAP CHIP REEL CL1 +/-30 <br> $4.7^{1}$ |
| C136 | $2113740 B 46$ | CAP CHIP REEL CL1 +/-30 75 |
| C137 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R } 10 \% \\ & 10000 \end{aligned}$ |
| C138 | $\begin{aligned} & \text { 2113740B09 } \\ & 2113740 \mathrm{~B} 17 \end{aligned}$ | CAP CHIP REEL CL1 $+/-30$ 2. ${ }^{2}$ CAP CHIP REEL CL1 $+/-30$ $4.7^{1}$ |
| E125 | 2680015P02 | SHLD RXFE UHF TIN PLATED |
| E126 | 2680002M02 | SHLD INJ FLTR TIN PLATED |
| HY126 | 9180209N02 | FLTR BANDPASS CERAMIC 455 MHZ ${ }^{2}$ |
| HY5126 | 9180209N01 | FLTR BANDPASS CERAMIC 417 MHZ ${ }^{1}$ |
| JU125 | 0611077A01 | RES CHIP JUMPER ${ }^{1}$ |
| JU5125 |  | PART NOT USED ${ }^{1}$ |
| L125 | 2480140E10 | INDUCTOR CHIP 100NH |
| $\begin{array}{\|l\|l\|} \hline \text { L126 \& } \\ \text { L127 } \end{array}$ | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L128 | 2480091G21 | COIL AIRWOUND |
| L129 | 2480091633 | COIL AIRWOUND |
| MP127 | 0780214N01 | FRAME PRESELECTOR LEAD |
| MP128 | 4280191N01 | CLIP RECEIVER GROUNDING |
| MP129 | 4280013P01 | RETAINER WIRE |
| MP130 | 4380025P01 | SPACER WIRE |
| MP156 | 0780091P01 | BRACKET RXFE UHF |
| Q125 | 4802000P02 | NEC SILICON TR 2SC3357 RE |
| Q126 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |



Notes:

1. Valid in HRE6001B only

HRE6003B and HRE6004B UHF RXFE Standard Parts List

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { C100, } \\ & \text { C101\& } \\ & \text { C103 } \end{aligned}$ |  | NOTPLACED |
| C125 | $2113740 B 76$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 1500 \end{aligned}$ |
| C126 | 2113740B09 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 2.2 \end{aligned}$ |
| $\begin{aligned} & \text { C127 thru } \\ & \text { C129 } \\ & \hline \end{aligned}$ | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C130 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C131 | $2113740 B 76$ | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 1500 \end{aligned}$ |
| C132 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| $\begin{aligned} & \text { C133 thru } \\ & \text { C135 } \end{aligned}$ | 2113740B13 | CAP CHIP REEL CL1 $+/-30$ 3.3 |
| C136 | 2113740B46 | CAP CHIP REEL CL1 +/-30 75 |
| C137 | 2113741N45 | $\begin{aligned} & \text { CAP CHIP CL2 X7R } 10 \% \\ & 10000 \end{aligned}$ |
| C138 | 2113740B09 | $\begin{aligned} & \text { CAP CHIP REEL CL1 +/-30 } \\ & 2.2 \end{aligned}$ |
| CR100 | --------- | NOTPLACED |
| E125 | 2680015P02 | SHLD RXFE UHF TIN PLATED |
| E126 | 2680002M02 | SHLD INJ FLTR TIN PLATED |
| HY126 | 9180209N03 | FLTR BANDPASS CERAMIC 465 MHZ ${ }^{1}$ <br> FLTR BANDPASS CERAMIC 497 MHZ ${ }^{2}$ |
| L125 | 2480140E10 | INDUCTOR CHIP 100NH |
| L126 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L127 | 2480140E01 | INDUCTOR CHIP 1.2 UH |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| L128 | 2480091G21 | COIL AIRWOUND |
| L129 | 2480091 G33 | COIL AIRWOUND |
| MP126 | 0780091P01 | BRACKET RX FE UHF |
| MP127 | 0780214N01 | FRAME PRESELECTOR LEAD |
| MP128 | 4280191N01 | CLIP RECEIVER GROUNDING |
| MP129 | 4280013P01 | RETAINER WIRE |
| MP130 | 4380025P01 | SPACER WIRE |
| Q100 | ------- | NOTPLACED |
| Q125 | 4802000P02 | NEC SILICON TR 2SC3357 RE |
| Q126 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| $\begin{array}{\|l\|} \hline \text { R100 thru } \\ \text { R104 } \end{array}$ | --------- | NOTPLACED |
| R126 | 0611077A74 | RES CHIP $100051 / 8$ |
| R127 | 0611077A36 | RES CHIP 275 1/8W |
| R128 | 0611077A52 | RES CHIP 1205 1/8W |
| R129 | 0611077A80 | RES CHIP 18005 1/8 |
| R130 | 0611077A94 | RES CHIP $680051 / 8$ |
| R131 | 0611077A50 | RES CHIP $10051 / 8 \mathrm{~W}$ |
| R132 | 0611077A26 0611077A43 | RES CHIP $1051 / 8 W^{2}$ RES CHIP $5151 / 8 W^{1}$ |
| W128 | 3080152M07 | CBL COAX ASSEMBLY |
| W642 | $3080037 \mathrm{R05}$ | CABLE ASSEMBLY |
|  | 0980212N02 | RECP COAX PCB MOUNT |
|  | 0980110M01 | RECP COAX PCBMOUNT |
|  | 8480241N02 | HYBRID CKT BD UHF F/E |

Notes:
Valid in HRE6003B only


HRF6004B/C 800 MHz Receiver Front-End Schematic Diagram


HRF6004B/C 800 MHz Receiver Front-End Component Location Diagram

## HRF6004B/C $\mathbf{8 0 0} \mathbf{~ M H z ~ R X F E ~ P a r t s ~ L i s t ~}$

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C8126 | 2111078 B42 | CAP CHIP RF 1005 NPO 100 V |
| C8129 | 2111059 D 57 | CAP CHIP 21D84873H57 All |
| C8130 | 2111059B11 | CAP CHIP 21D84547A11 A/l |
| C8131 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R } 10 \% \\ & 100000 \end{aligned}$ |
| $\begin{aligned} & \text { C8132 } \\ & \text { thru } \\ & \text { C8136 } \end{aligned}$ | 2111059 B 11 | CAP CHIP 21D84547A11 A/l |
| E8126 | 9180087M02 | FILTER CER 6 P 938MHZ |
| E8127 | 2680002M02 | SHLD INJ FLTR TIN PLATED |
| E8130 | 2680010N02 | SHLD RCVR FNT END TIN PLATED |
| J9127 | 0980110M01 | RECP COAX PCBMOUNT |
| L8129 | 2480140E06 | INDUCTOR CHIP 130 NH |
| L8130 | 2480140E10 | INDUCTOR CHIP 100NH |
| L8131 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L8132 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| MP8126 | 0780259M02 | FRAME TIN PLATED |
| MP8127 | 6480100P01 | SBSTR RECEIVER FRONT END |
| MP8128 | 0780031P01 | BRKT RCVR FRONT END |
| Q8126 | 4802000P02 | NEC SILICON TR 2SC3357 RE |
| Q8127 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q8128 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| R8126 | 0611077 B 15 | RES CHIP 47K $51 / 8 \mathrm{~W}$ |
| R8127 | 0611077A98 | RES CHIP 10K $51 / 8 \mathrm{~W}$ |
| R8128 | 0611077A50 | RES CHIP 1005 1/8W |
| R8129 | 0611077A54 | RES CHIP 1505 1/8W |
| R8130 | 0611077A38 | RES CHIP $3351 / 8 \mathrm{~W}$ |
| R8131 | 0611077A54 | RES CHIP 1505 1/8W |
| R8132 | 0611077A01 | RES CHIP JUMPER |
| R8134 | 0611077A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R8135 | 0611077A64 | RES CHIP 3905 1/8W |
| W8127 | 3080037R05 | CABLE ASSEMBLY |
| W8128 | 3080152M07 | CBL COAX ASSEMBLY |
|  | 4285615C01 | CLIP, GROUND |
|  | 8480106P02 | BD THK FLM MIXER |

### 7.7 Power Amplifier Section



HLD6022C VHF 50 Watt PA Schematic


HLD6022C VHF 50-Watt PA Component Location Diagram, Side 1

VIEWED FROM SIDE 2


HLD6022C VHF 50-Watt PA Component Location Diagram, Side 2

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C3801 | 2113741N21 | CAP CHIP CL2 X7R 10\% 1000 |
| $\begin{aligned} & \hline \text { C3802 } \\ & \text { thru } \\ & \text { C3805 } \\ & \hline \end{aligned}$ | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C3806 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3807 | $2113740 B 41$ | CAP CHIP REEL CL1 +/-30 47 |
| C3808 | 2113740B48 | CAP CHIP REEL CL1 +/-30 91 |
| $\begin{array}{\|l\|} \hline \text { C3810 \& } \\ \text { C3811 } \\ \hline \end{array}$ | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3812 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| $\begin{aligned} & \hline \text { C3813 \& } \\ & \text { C3815 } \end{aligned}$ | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| $\begin{aligned} & \text { C3816 \& } \\ & \text { C3817 } \end{aligned}$ | 2113740B44 | CAP CHIP REEL CL1 +/-30 62 |
| C3818 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C3819 | $2113740 B 44$ | CAP CHIP REEL CL1 +/-30 62 |
| $\begin{aligned} & \text { C3831 \& } \\ & \text { C3832 } \end{aligned}$ |  | NOTPLACED |
| C3834 | 2113743G24 | CAP CHIP 2.2 UF 16V +80-20\% |
| $\begin{aligned} & C 3835 \& \\ & C 3836 \end{aligned}$ |  | NOTPLACED |
| $\begin{array}{\|l} \hline \text { C3850 } \\ \text { thru } \\ \text { C3855 } \end{array}$ | 2111078B49 | CAP CHIP RF 1805 NPO 100V |
| C3855 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3856 | 2380090M24 | CAP ALU 1020 50V SURF MT |
| C3857 | 2113741N45 | CAP CHIP CL2 X7R $10 \% 10000$ |
| C3858 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3859 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| $\begin{array}{\|l} \hline \text { C3875 \& } \\ \text { C3876 } \\ \hline \end{array}$ | 2111078B49 | CAP CHIP RF 1805 NPO 100V |
| C3877 | 2111078B42 | CAP CHIP RF 1005 NPO 100V |
| C3878 | 2111078 B27 | CAP CHIP RF 305 NPO 100V |
| C3879 | 2111078B59 | CAP CHIP RF 4705 NPO 100V |
| C3880 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3881 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R } 10 \% \\ & 100000 \end{aligned}$ |
| C3882 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| $\begin{array}{\|l} \hline \begin{array}{l} \text { C3883 } \\ \text { thru } \\ \text { C3885 } \end{array} \\ \hline \end{array}$ | $2113740 B 65$ | CAP CHIP REEL CL1 +/-30 470 |
| $\begin{aligned} & \hline \text { C3890 } \\ & \text { thru } \\ & \text { C3901 } \end{aligned}$ | $2113740 \mathrm{B65}$ | CAP CHIP REEL CL1 +/-30 470 |
| C3902 | 2113740B36 | CAP CHIP REEL CL1 +/-30 30 |
| $\begin{aligned} & \text { C3903 \& } \\ & \text { C3904 } \\ & \hline \end{aligned}$ | $2113740 B 65$ | CAP CHIP REEL CL1 +/-30 470 |
| C3910 | 2111078B59 | CAP CHIP RF 4705 NPO 100 V |
| C3911 | 2111078B19 | CAP CHIP RF 165 NPO 100V |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C3912 | 2111078B19 | CAP CHIP RF 165 NPO 100V |
| C3913 | 2111078B31 | CAP CHIP RF 365 NPO 100V |
| C3914 | 2111078 B23 | CAP CHIP RF 245 NPO 100V |
| $\begin{aligned} & \text { C3920 \& } \\ & \text { C3921 } \end{aligned}$ | 2111078 B22 | CAP CHIP RF 225 NPO 100V |
| $\begin{array}{\|l} \hline \text { C3922 } \\ \text { thru } \\ \text { C3924 } \end{array}$ | $2113740 B 65$ | CAP CHIP REEL CL1 +/-30 470 |
| C3925 | 2111078 B19 | CAP CHIP RF 165 NPO 100V |
| $\begin{aligned} & \text { C3926 \& } \\ & \text { C3927 } \end{aligned}$ | 2113740865 | CAP CHIP REEL CL1 +/-30 470 |
| CR3875 | 4880236E07 | DIODE TRANSIENT SUP |
| CR3900 | 4813825A05 | DIODE 30V HOT CARRIER MMBD301L |
| $\begin{array}{\|l\|} \hline \text { CR3920 \& } \\ \text { CR3921 } \end{array}$ | 4880236E24 | DIODE PIN |
| CR3922 | 4805119G18 | DIODE SMBD1023 STEP RECOVERY |
| E3800 | 2680187N02 | HEATSINK, TO39 |
| E3801 | 2680188N02 | SHLD HAR FILTER FENCE TIN PLAT |
| E3803 | 8480176N34 | BD CK 50W PA PNL |
| $\begin{aligned} & \text { E3850 } \\ & \text { ETru } \\ & \text { E3855 } \end{aligned}$ | 2980014A03 | CLIP COAX TERMINAL |
| L3801 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L3802 | 2480091 G23 | COIL AIRWOUND |
| L3806 | 2484657R01 | INDUCTOR BEAD CHIP |
| L3807 | 2480091G23 | COIL AIRWOUND |
| L3810 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| $\begin{array}{\|l\|l} \hline \text { L3811 \& } \\ \text { L3851 } \end{array}$ | 2480091G24 | COIL AIRWOUND |
| L3852 | 2484657R01 | INDUCTOR BEAD CHIP |
| L3853 | 2480091G23 | COIL AIRWOUND |
| L3854 | 2484657R01 | INDUCTOR BEAD CHIP |
| L3875 | 2480090G03 | COIL AIRWOUND |
| L3876 | 2484657R01 | INDUCTOR BEAD CHIP |
| L3877 | 2480090G03 | COIL AIRWOUND |
| L3890 | 2484657R01 | INDUCTOR BEAD CHIP |
|  | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L3904 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L3911 | 2480090G15 | COIL AIRWOUND |
| L3912 | 2480090G10 | COIL AIRWOUND |
| L3913 | 2480090G15 | COIL AIRWOUND |
| L3914 | 2480090G13 | COIL AIRWOUND |
| $\begin{array}{\|l\|l\|} \hline \text { L3920 \& } \\ \text { L3921 } \end{array}$ | 2480090G14 | COIL AIRWOUND |
| L3922 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| P0850 | 2880102M06 | PLUG VERTICAL 12 POSITION |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| P0853 | 0980103M04 | RECEPTICAL RIGHT ANGLE 12 POS |
| Q3801 | 4880182D50 | TSTR SI SORF 4V 750MW 960 MHZ |
| Q3802 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q3804 | 4800869859 | TSTR NPN 69859 |
| Q3806 | 4880141L02 | TSTR NPN SOT23 LO PROFILE TAPE |
| Q3850 | 4880225C28 | TSTR RF MRF2628 IN FLANGE PKG |
| Q3875 | 4884411L04 | TSTR M1104 |
| R3801 | 0611077A26 | RES CHIP 105 1/8W |
| R3802 | 0611077A54 | RES CHIP 1505 1/8W |
| R3803 | 0611077A43 | RES CHIP 515 1/8W |
| R3804 | 0611077 A26 | RES CHIP 105 1/8W |
| R3805 | 0611077A82 | RES CHIP $220051 / 8$ |
| R3806 | 0611077A26 | RES CHIP 105 1/8W |
| R3807 | 0611077 A86 | RES CHIP $330051 / 8$ |
| R3808 | 0611077A82 | RES CHIP $220051 / 8$ |
| R3809 | 0611077 A74 | RES CHIP $100051 / 8$ |
| R3810 | 0611077A43 | RES CHIP 515 1/8W |
| R3811 | --------- | NOTPLACED |
| R3812 | 0611077A74 | RES CHIP 10005 1/8 |
| R3813 | 0611077A50 | RES CHIP 1005 1/8W |
| R3815 | 0611077A26 | RES CHIP 105 1/8W |
| R3816 | 0611077A80 | RES CHIP 18005 1/8 |
| R3817 | 0611077A74 | RES CHIP $100051 / 8$ |
| R3818 | 0611077A62 | RES CHIP 3305 1/8W |
| R3850 | 0611077 A26 | RES CHIP 105 1/8W |
| R3851 | 0611077 A26 | RES CHIP 105 1/8W |
| R3875 | 1780228N02 | RESISTOR SHUNT |
| R3876 | 0680195M01 | RES CHIP 10 OHMS 5\% .5W |
| R3878 | 0611077A70 | RES CHIP 6805 1/8W |
| R3879 | --------- | NOTPLACED |
| R3880 | 0611077A90 | RES CHIP $470051 / 8$ |
| $\begin{aligned} & \text { R3900 \& } \\ & \text { R3901 } \end{aligned}$ | 0680194M18 | RES 51 OHMS 5\% 1W |
| R3903 | 0680195M25 | RES CHIP 100 OHMS 5\% .5W |
| R3904 | 0611077 B 11 | RES CHIP 33K 5 1/8W |
| $\begin{array}{\|l} \hline \text { R3905 \& } \\ \text { R3906 } \\ \hline \end{array}$ | 0611077A74 | RES CHIP $100051 / 8$ |
| R3907 | 0611077A78 | RES CHIP 15005 1/8 |
| $\begin{aligned} & \text { R3910 } \\ & \text { thru } \\ & \text { R3912 } \end{aligned}$ | 0611077A50 | RES CHIP 1005 1/8W |
| $\begin{array}{\|l} \hline \text { R3920 \& } \\ \text { R3921 } \\ \hline \end{array}$ | 0611077A72 | RES CHIP 8205 1/8W |
| $\begin{array}{\|l} \hline \text { RT3877 \& } \\ \text { RT3905 } \\ \hline \end{array}$ | 0680149M02 | THERMISTOR CHIP 100K OHM |



HLD6064C VHF 100-Watt PA Schematic


HLD6064C VHF 100-Watt PA Component Location Diagram, Side 1


HLD6064C VHF 100-Watt PA Component Location Diagram, Side 2

| HLD6064C VHF R1 100-Watt PA Parts List |  |  | ITEM | MOTOROLA PART NUMBER | DESCRIPTION | ITEM | MOTOROLA PART NUMBER | DESCRIPTION | ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MOTOROLA |  |  |  |  |  |  |  |  |  |  |
| ITEM | PART NUMBER | DESCRIPTION | C3904 \& | $2113740 B 65$ | CAP CHIP REEL CL1 +/-30 470 | L3934 | 2484657R01 | INDUCTOR BEAD CHIP | R3913 \& | 0611077 B47 | RES CHIP 1 MEG 5 1/8W |
| C3801 \& | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 | C3905 |  |  | L3950 \& | 2480090G10 | COIL AIRWOUND | R3914 |  |  |
| C3802 |  |  | C3907 | 2113740865 | CAP CHIP REEL CL1 +/-30 470 | L3951 |  |  | R3916 | 0611077 B07 | RES CHIP 22K 5 1/8W |
| C3804 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 | C3908 | 2111078B59 | CAP CHIP RF 4705 NPO 100V | MP3801 | 4280202R02 | CLIP COAX TIN PLATED | R3920 | 0680195M01 | RES CHIP 10 OHMS 5\% .5W |
| C3807 | 2113740B36 | CAP CHIP REEL CL1 +/-30 30 | C3913 | 2111078B19 | CAP CHIP RF 165 NPO 100V | MP3802 | 2680139P02 | SHLD HAR FLTR TIN PLATED | R4005 \& | 0611077A32 | RES CHIP 185 1/8W |
| C3808 | 2113740B48 | CAP CHIP REEL CL1 +/-30 91 | C3914 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 | MP3803 | 2680201R02 | HEATSINK DIODE TIN PLATED | R4006 |  |  |
| C3809 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 | C3917 | 2113740B36 | CAP CHIP REEL CL1 +/-30 30 | MP3804 | 2680187N02 | HEATSINK, TO39 | R4007 | 0680194M01 | RES 10 OHMS 5\% 1W |
| C3810 | 2113740B76 | CAP CHIP REEL CL1 +/-30 1500 | C3919 | $2113740 B 65$ | CAP CHIP REEL CL1 +/-30 470 | Q3801 | 4880182D50 | TSTR SI SORF 4V 750MW | R4008 | 0680194M18 | RES 51 OHMS 5\% 1W |
| C3812 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 | C3920 | 2113740B36 | CAP CHIP REEL CL1 +/-30 30 |  |  | 960 MHZ | R5001 | 0611077A26 | RES CHIP 105 1/8W |
| C3818 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 | C3933 | 2111078B44 | CAP CHIP RF 1205 NPO 100 V | Q3804 | 4800869859 | TSTR NPN 69859 |  | 0680149M02 | THERMISTOR CHIP 100K OHM |
| C3819 | 2113740B35 | CAP CHIP REEL CL1 +/-30 27 | C3935 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 | Q3805 | 4880225 C 22 | TSTR NPN 174MHZ 3/30W | RT3907 |  |  |
| C3820 | 2111078B49 | CAP CHIP RF 1805 NPO 100V | C3936 | 2111078B59 | CAP CHIP RF 4705 NPO 100 V |  |  | MRF1946 |  | 8480112P05 | BD CKT VHF |
| $\begin{array}{\|l} \hline \text { C3821 \& } \\ \text { C3824 } \\ \hline \end{array}$ | $2113740 B 65$ | CAP CHIP REEL CL1 +/-30 470 | C3937 <br> CR3840 | 2380090M24 | CAP ALU 102050 V SURF MT | Q3806 | 4880141L02 | TSTR NPN SOT23 LO PROFILE TAPE |  | 0180756 T06 | HYBRID BD HAR FLTR VHF SPECTRA |
| C3835 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 | CR3840 | 4880222R01 | SPECTRA | Q3870 \& | 4884411L04 | TSTR M1104 |  |  |  |
| C3836 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 | CR3901 | 4880121R01 | PWR PIN DIODE SMD TAPE \& | Q3871 |  |  |  |  |  |
| C3840 <br> thru | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 | \& CR3902 |  | REEL | $\begin{aligned} & \text { R3802 \& } \\ & \text { R3803 } \end{aligned}$ | 0611077A46 | RES CHIP 685 1/8W |  |  |  |
| C3844 |  |  | CR3903 | 4880142L01 | DIODE PIN SOT 23 MMBV 3401 | R3804 | 0611077 A26 | RES CHIP 105 1/8W |  |  |  |
| C3846 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 | CR3904 | 4813825A05 | DIODE 30 V HOT CARRIER | R3805 | 0611077A82 | RES CHIP $220051 / 8$ |  |  |  |
| thru |  |  |  |  | MMBD301L | R3806 | 0611077A74 | RES CHIP $100051 / 8$ |  |  |  |
| C3849 |  |  | J0001 | 2880102M11 | PLUG VERTICAL 12 POS | R3807 | 0611077 A86 | RES CHIP $330051 / 8$ |  |  |  |
| C3851 | 2111078 B44 | CAP CHIP RF 1205 NPO 100V | J3850 | 4280088P01 | CLIP COAX | R3808 | 0611077 A80 | RES CHIP $180051 / 8$ |  |  |  |
| C3852 | 2111078B40 | CAP CHIP RF 825 NPO 100V | J3851 | 2980119R01 | TERMINAL POWER | R3809 | 0611077 A74 | RES CHIP $100051 / 8$ |  |  |  |
| C3853 | 2111078 B44 | CAP CHIP RF 1205 NPO 100V | J3852 \& | 4280088P01 | CLIP COAX | R3810 | 0611077 A43 | RES CHIP 515 1/8W |  |  |  |
| C3856 | 2111078B59 | CAP CHIP RF 4705 NPO 100V | J3853 |  |  | R3811 | 0611077A74 | RES CHIP 10005 1/8 |  |  |  |
| C3857 | 2111078B47 | CAP CHIP RF 1505 NPO 100V | L3802 | 2480091 G23 | COIL AIRWOUND | R3816 | 0611077 A80 | RES CHIP $180051 / 8$ |  |  |  |
| C3858 | 2111078 B49 | CAP CHIP RF 1805 NPO 100V | L3806 | 2484657R01 | INDUCTOR BEAD CHIP | R3817 | 0611077 A74 | RES CHIP $100051 / 8$ |  |  |  |
| thru |  |  | L3807 | 2480091 G23 | COIL AIRWOUND | R3818 | 0611077 A62 | RES CHIP 3305 1/8W |  |  |  |
| C3861 \& | 2111078851 | CAP CHIP RF 2205 NPO 100V | L3808 | 2480091 G24 | COIL AIRWOUND | R3819 | 0680195M01 | RES CHIP 10 OHMS 5\% .5W |  |  |  |
| C3862 |  |  | L3809 | 2480091 G 23 | COIL AIRWOUND | R3822 | 0611077A26 | RES CHIP $1051 / 8 \mathrm{~W}$ |  |  |  |
| C3863 | 2111078B49 | CAP CHIP RF 1805 NPO 100V | L3810 | 2480090G16 | COIL AIRWOUND RF 000.000 | R3840 | 0611077A50 | RES CHIP $10051 / 8 \mathrm{~W}$ |  |  |  |
| C3870 \& | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 | $\begin{aligned} & \mathrm{L} 3811 \\ & \mathrm{~L} 3812 \end{aligned}$ | 2484657R01 | INDUCTOR BEAD CHIP | R3841 | 0611077A80 | RES CHIP 18005 1/8 |  |  |  |
| C3871 | 2111078849 | CAP CHIP RF 1805 NPO 100V | L3840 | 2484657R01 | INDUCTOR BEAD CHIP | R3849 | 1780165C02 | RES SHUNT |  |  |  |
|  | 2111078 B49 | CAP CHIP RF 1805 NPO 100 V | L3860 | 2480090G19 | COIL AIRWOUND | R3859 | 0680194M01 | RES 10 OHMS 5\% 1W |  |  |  |
| C3875 |  |  | L3870 \& | 2480091G23 | COIL AIRWOUND | R3870 \& | 0680194M18 | RES 51 OHMS 5\% 1W |  |  |  |
| C3878 | 2111078B40 | CAP CHIP RF 825 NPO 100V |  |  |  | R3871 |  |  |  |  |  |
| C3880 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 | L3881 | 2484657R01 |  | R3872 \& | 0680194M13 | RES 33 OHMS 5\% 1W |  |  |  |
| C3881 | 2111078B59 | CAP CHIP RF 4705 NPO 100V | L3900 | 2480140E01 | COIL AIRWOUND RF 000.000 | R3873 <br> R3879 |  |  |  |  |  |
| C3882 | 2111078B40 | CAP CHIP RF 825 NPO 100V | L3901 |  | COIL AIRWOUND RF 000.000 | R3879 | 0680195M01 | RES CHIP 10 OHMS 5\% .5W |  |  |  |
| C3886 \& | 2111078B40 | CAP CHIP RF 825 NPO 100V | L3902 | 2480090G20 | COIL AIRWOUND |  <br> R3901 | 0680194M18 | RES 51 OHMS 5\% 1W |  |  |  |
| C3887 |  |  | L3903 | 2480140E01 | INDUCTOR CHIP 1.2 UH |  |  |  |  |  |  |
| C3888 | 2111078 B 27 | CAP CHIP RF 305 NPO 100V | L3909 | 2480091G21 | COIL AIRWOUND | R3902 \& R3903 | 0611077A54 | RES CHIP 1505 1/8W |  |  |  |
| C3889 | 2111078 B59 | CAP CHIP RF 4705 NPO 100V | L3910 | 2480140 E 11 | INDUCTOR CHIP 360 NH |  |  |  |  |  |  |
| C3900 | $2113740 B 65$ | CAP CHIP REEL CL1 +/-30 470 | L3911 | $2480090 \mathrm{G16}$ | COIL AIRWOUND RF 000.000 | R3904 | 0611077A52 |  |  |  |  |
| C3901 | 2113740B30 | CAP CHIP REEL CL1 +/-30 16 | L3920 | 2480090G13 | COIL AIRWOUND | R3906 | 0611077 A78 |  |  |  |  |
| C3902 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 | L3930 | 2480091 G23 | COIL AIRWOUND | R3909 | 0611077 B15 | RES CHIP 47K $51 / 8 \mathrm{~W}$ |  |  |  |
| C3903 | 2113740B09 | CAP CHIP REEL CL1 +/-30 2.2 | L3931 | 2484657R01 | INDUCTOR BEAD CHIP | R3910 | 0611077 A 66 | RES CHIP 4705 1/8W |  |  |  |



HLD6032B/HLD6066B VHF 25-Watt PA Schematic


HLD6032B/HLD6066B VHF 25-Watt PA Component Location Diagram, Side 1


HLD6032B/HLD6066B VHF 25-Watt PA Component Location Diagram, Side 2

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C3800 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C3801 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C3802 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3803 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C3804 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| $\begin{aligned} & \text { C3805 \& } \\ & \text { C3806 } \end{aligned}$ | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3807 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C3809 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C3810 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C3811 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3812 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C3813 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3815 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C3816 | 2113740B21 | CAP CHIP REEL CL1 + +-30 6.8 |
| C3817 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C3819 | 2113740B44 | CAP CHIP REEL CL1 +/-30 62 |
| C3820 | 2113740B65 | CAP CHIP REEL CL1 + +-30 470 |
| C3821 | 2113740B41 | CAP CHIP REEL CL1 +/-30 47 |
| C3822 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C3823 | 2113740B44 | CAP CHIP REEL CL1 +/-30 62 |
| C3856 | 2380090M24 | CAP ALU 1020 50V SURF MT |
| C3874 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C3875 | 2111078B49 | CAP CHIP RF 1805 NPO 100V |
| C3877 | 2111078 B42 | CAP CHIP RF 1005 NPO 100V |
| C3879 | 2111078B59 | CAP CHIP RF 4705 NPO 100V |
| C3880 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3881 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C3882 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| $\begin{array}{\|l\|} \hline \text { C3883 } \\ \text { thru } \\ \text { C3885 } \end{array}$ | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| $\begin{aligned} & \text { C3889 \& } \\ & \text { C3890 } \end{aligned}$ | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3891 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| $\begin{aligned} & \begin{array}{l} \text { C3892 } \\ \text { thru } \\ \text { C3895 } \end{array} \\ & \hline \end{aligned}$ | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| $\begin{aligned} & \hline \text { C3900 \& } \\ & \text { C3901 } \\ & \hline \end{aligned}$ | $2113740 B 65$ | CAP CHIP REEL CL1 +/-30 470 |
| C3902 | 2113740 B 23 | CAP CHIP REEL CL1 +/-30 8.2 |
| C3903 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3904 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3910 | 2111078B59 | CAP CHIP RF 4705 NPO 100V |
| C3911 | 2111078B19 | CAP CHIP RF 165 NPO 100V |
| C3912 | 2111078B31 | CAP CHIP RF 365 NPO 100V |
| C3913 | 2111078B27 | CAP CHIP RF 305 NPO 100V |
| $\begin{array}{\|l} \hline \text { C3914 \& } \\ \text { C3920 } \\ \hline \end{array}$ | 2111078B22 | CAP CHIP RF 225 NPO 100V |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { C3921 \& } \\ & \text { C3922 } \\ & \hline \end{aligned}$ | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| C3923 | $2113740 B 65$ | CAP CHIP REEL CL1 +/-30 470 |
| C3925 | 2113740B17 | CAP CHIP REEL CL1 + -30 4.7 |
| $\begin{array}{\|l} \hline \text { C3926 } \\ \text { thru } \\ \text { C3928 } \end{array}$ | $2113740 B 65$ | CAP CHIP REEL CL1 +/-30 470 |
| CR3875 | 4880236E07 | DIODE TRANSIENT SUP |
| CR3900 | 4813825A05 | DIODE 30V HOT CARRIER MMBD301L |
| CR3922 | 4805119G18 | DIODE SMBD1023 STEP RECOVERY |
| E3800 | 2680187N02 | HEATSINK, TO39 |
| E3803 | 8480259N33 | CKT BD VHF 25W PANEL |
| E3804 | 8480259N03 | CKT BD VHF 25W PA |
| E3850 | 2980014A03 | CLIP COAX TERMINAL |
| E3851 | 3080152M10 | CBL COAX ASSEMBLY |
| E3852 | 2980014A03 | CLIP COAX TERMINAL |
| E3854 | 3080152M02 | CABLE COAX ASSEMBLY |
| L3800 | 2480091 G24 | COIL AIRWOUND |
| L3801 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L3802 | 2480091 G23 | COIL AIRWOUND |
| L3807 | 2480091 G07 | COIL AIRWOUND |
| L3808 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L3811 | 2480091 G24 | COIL AIRWOUND |
| L3851 | 2480091 G23 | COIL AIRWOUND |
| L3852 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L3875 | 2480090G16 | COIL AIRWOUND RF 000.000 |
| L3877 | 2480090G03 | COIL AIRWOUND |
|  | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L3904 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L3911 | 2480090G15 | COIL AIRWOUND |
| L3912 | 2480090 G 10 | COIL AIRWOUND |
| L3913 | $2480090 \mathrm{G}^{15}$ | COIL AIRWOUND |
| L3914 | 2480090G13 | COIL AIRWOUND |
| L3920 | $2480090 \mathrm{G14}$ | COIL AIRWOUND |
| L3921 | 2480090G03 | COIL AIRWOUND |
| L3922 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| P0853 | 6480262N01 | PLATE LP FEEDTHRU ASSEMBLY |
| Q3801 | 4880182 D 50 | TSTR SI SORF 4 V 750 MW 960 MHZ |
| Q3802 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q3804 | 4800869859 | TSTR NPN 69859 |
| Q3806 | 4880141L02 | TSTR NPN SOT23 LO PROFILE TAPE |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| Q3850 | 4880225C22 | TSTR NPN 174MHZ 3/30W MRF1946 |
| R3800 | 0611077A01 | RES CHIP JUMPER |
| R3801 | 0611077A26 | RES CHIP 105 1/8W |
| R3802 | 0611077A54 | RES CHIP $15051 / 8 \mathrm{~W}$ |
| R3803 | 0611077A36 | RES CHIP 275 1/8W |
| R3804 | 0611077A26 | RES CHIP 105 1/8W |
| R3805 | 0611077 A82 | RES CHIP $220051 / 8$ |
| R3806 | 0611077A54 | RES CHIP 1505 1/8W |
| R3807 | 0611077A10 | RES CHIP 2.25 1/8W |
| R3809 | 0611077A74 | RES CHIP $100051 / 8$ |
| R3810 | 0611077 A43 | RES CHIP 515 1/8W |
| R3811 | 0611077A50 | RES CHIP 1005 1/8W |
| R3812 | 0611077A74 | RES CHIP 10005 1/8 |
| R3813 | 0611077 A86 | RES CHIP $330051 / 8$ |
| R3814 | 0611077 A82 | RES CHIP $220051 / 8$ |
| R3815 | 0611077A26 | RES CHIP 105 1/8W |
| R3816 | 0611077 A80 | RES CHIP 18005 1/8 |
| R3817 | 0611077A74 | RES CHIP $100051 / 8$ |
| R3818 | 0611077A62 | RES CHIP $33051 / 8 \mathrm{~W}$ |
| R3819 | 0611077A32 | RES CHIP 185 1/8W |
| R3820 | 0611077A50 | RES CHIP 1005 1/8W |
| R3821 | 0611077A58 | RES CHIP 2205 1/8W |
| R3875 | 1780228N01 | RESISTOR SHUNT |
| R3876 \& R3877 | 0611077A26 | RES CHIP 105 1/8W |
| R3878 | 0611077A94 | RES CHIP $680051 / 8$ |
| R3879 | 0611077B23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R3881 | 0611077A50 | RES CHIP 1005 1/8W |
|  | 0680194M18 | RES 51 OHMS 5\% 1W |
| R3903 | 0680195M25 | RES CHIP 100 OHMS 5\% .5W |
| R3904 | 0611077 B 11 | RES CHIP 33K 5 1/8W |
| R3905 | 0611077A74 | RES CHIP 10005 1/8 |
| $\begin{aligned} & \text { R3906 \& } \\ & \text { R3907 } \\ & \hline \end{aligned}$ | 0611077A78 | RES CHIP $150051 / 8$ |
| R3908 | 0611077A50 | RES CHIP 1005 1/8W |
| $\begin{aligned} & \hline \text { R3920 \& } \\ & \text { R3921 } \\ & \hline \end{aligned}$ | 0611077A72 | RES CHIP $82051 / 8 \mathrm{~W}$ |
| RM0002 | 0611077A01 | RES CHIP JUMPER |
| $\begin{array}{\|l} \hline \text { RT3876 \& } \\ \text { RT3905 } \\ \hline \end{array}$ | 0680149M02 | THERMISTOR CHIP 100K OHM |
|  | 2484657R01 | INDUCTOR BEAD CHIP |
|  | 4880236E24 | DIODE PIN |
|  | 2111078B32 | CAP CHIP RF 395 NPO 100V |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C3801 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C3802 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3803 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C3804 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| $\begin{aligned} & \text { C3805 \& } \\ & \text { C3806 } \end{aligned}$ | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3807 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C3809 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C3810 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C3811 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3812 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C3813 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3815 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C3816 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| C3817 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C3819 | 2113740B47 | CAP CHIP REEL CL1 +/-30 82 |
| C3820 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| $\begin{aligned} & \text { C3821 \& } \\ & \text { C3822 } \\ & \hline \end{aligned}$ | 2113740B44 | CAP CHIP REEL CL1 +/-30 62 |
| C3823 | $2113740 B 47$ | CAP CHIP REEL CL1 +/-30 82 |
| C3856 | 2380090M24 | CAP ALU 1020 50V SURF MT |
| C3874 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C3875 | 2111078B49 | CAP CHIP RF 1805 NPO 100 V |
| C3877 | 2111078B42 | CAP CHIP RF 1005 NPO 100V |
| C3878 | 2111078B32 | CAP CHIP RF 395 NPO 100V |
| C3879 | 2111078 B59 | CAP CHIP RF 4705 NPO 100V |
| C3880 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| $\begin{aligned} & \text { C3881 \& } \\ & \text { C3882 } \end{aligned}$ | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C3883 <br> thru <br>  <br> C3889 <br> C3890 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3891 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| $\begin{aligned} & \text { C3892 } \\ & \text { thru } \\ & \text { C3895 } \end{aligned}$ | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| $\begin{aligned} & \text { C3900 \& } \\ & \text { C3901 } \end{aligned}$ | $2113740 \mathrm{B65}$ | CAP CHIP REEL CL1 +/-30 470 |
| C3902 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| $\begin{aligned} & \text { C3903 \& } \\ & \text { C3904 } \end{aligned}$ | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3910 | 2111078B59 | CAP CHIP RF 4705 NPO 100V |
| C3911 | 2111078B19 | CAP CHIP RF 165 NPO 100V |
| C3912 | 2111078B31 | CAP CHIP RF 365 NPO 100V |
| C3913 | 2111078 B 27 | CAP CHIP RF 305 NPO 100V |
| $\begin{aligned} & \text { C3914 \& } \\ & \text { C3920 } \end{aligned}$ | 2111078B22 | CAP CHIP RF 225 NPO 100V |
| $\begin{aligned} & \text { C3921 \& } \\ & \text { C3922 } \\ & \hline \end{aligned}$ | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C3923 | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| C3925 | 2113740B17 | CAP CHIP REEL CL1 +/-30 4.7 |
| $\begin{aligned} & \text { C3926 } \\ & \text { thru } \\ & \text { C3928 } \end{aligned}$ | 2113740B65 | CAP CHIP REEL CL1 +/-30 470 |
| CR3875 | 4880236E07 | DIODE TRANSIENT SUP |
| CR3900 | 4813825A05 | DIODE 30V HOT CARRIER MMBD301L |
| $\begin{aligned} & \hline \text { CR3920 } \\ & \text { \& CR3921 } \end{aligned}$ | 4880236E24 | DIODE PIN |
| CR3922 | 4805119G18 | DIODE SMBD1023 STEP RECOVERY |
| E3800 | 2680187N02 | HEATSINK, TO39 |
| E3803 | 8480259N33 | CKT BD VHF 25W PANEL |
| E3804 | 8480259N03 | CKT BD VHF 25W PA |
| $\begin{array}{\|l} \hline \text { E3850 \& } \\ \text { E3852 } \end{array}$ | 2980014A03 | CLIP COAX TERMINAL |
| L3801 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L3802 | 2480091 G23 | COIL AIRWOUND |
| L3806 | 2484657R01 | INDUCTOR BEAD CHIP |
| L3807 | 2480091 G36 | COIL AIRWOUND |
| L3808 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L3811 | 2480091 G24 | COIL AIRWOUND |
| L3851 | 2480091 G23 | COIL AIRWOUND |
| L3852 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28 MZ |
| L3875 | $2480090 \mathrm{G16}$ | COIL AIRWOUND RF 000.000 |
| L3876 | 2484657R01 | INDUCTOR BEAD CHIP |
| L3877 | 2480090G03 | COIL AIRWOUND |
| L3890 | 2484657R01 | INDUCTOR BEAD CHIP |
| $\begin{array}{\|l\|} \hline \text { L3900 \& } \\ \text { L3903 } \end{array}$ | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| L3904 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L3911 | 2480090G15 | COIL AIRWOUND |
| L3912 | 2480090G10 | COIL AIRWOUND |
| L3913 | 2480090 G15 | COIL AIRWOUND |
| L3914 | $2480090 \mathrm{G13}$ | COIL AIRWOUND |
| L3920 | 2480090G14 | COIL AIRWOUND |
| L3921 | 2480090 G04 | COIL AIRWOUND |
| L3922 | 2480140E01 | INDUCTOR CHIP 1.2 UH |
| P0853 | 6480262N01 | PLATE LP FEEDTHRU ASSEMBLY |
| Q3801 | 4880182D50 | TSTR SI SORF 4V 750MW 960MHZ |
| Q3802 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q3804 | 4800869859 | TSTR NPN 69859 |
| Q3806 | 4880141L02 | TSTR NPN SOT23 LO PROFILE TAPE |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| Q3850 | 4880225C22 | TSTR NPN 174MHZ3/30W MRF1946 |
| R3800 | 0611077A01 | RES CHIP JUMPER |
| R3801 | 0611077A26 | RES CHIP 105 1/8W |
| R3802 | 0611077A54 | RES CHIP 1505 1/8W |
| R3803 | 0611077A40 | RES CHIP 395 1/8W |
| R3804 | 0611077A26 | RES CHIP $1051 / 8 \mathrm{~W}$ |
| R3805 | 0611077 A82 | RES CHIP $220051 / 8$ |
| R3806 | 0611077A54 | RES CHIP $15051 / 8 \mathrm{~W}$ |
| R3807 | 0611077A10 | RES CHIP 2.25 1/8W |
| R3809 | 0611077A74 | RES CHIP $100051 / 8$ |
| R3810 | 0611077A43 | RES CHIP 515 1/8W |
| R3811 | 0611077A50 | RES CHIP 1005 1/8W |
| R3812 | 0611077A74 | RES CHIP $100051 / 8$ |
| R3813 | 0611077A86 | RES CHIP $330051 / 8$ |
| R3814 | 0611077A80 | RES CHIP $180051 / 8$ |
| R3815 | 0611077A26 | RES CHIP 105 1/8W |
| R3816 | 0611077 A80 | RES CHIP 18005 1/8 |
| R3817 | 0611077A74 | RES CHIP $100051 / 8$ |
| R3818 | 0611077A62 | RES CHIP 3305 1/8W |
| R3819 | 0611077A32 | RES CHIP 185 1/8W |
| R3820 | 0611077A50 | RES CHIP 1005 1/8W |
| R3821 | 0611077A58 | RES CHIP 2205 1/8W |
| R3875 | 1780228N01 | RESISTOR SHUNT |
| R3876 \& R3877 | 0611077A26 | RES CHIP 105 1/8W |
| R3878 | 0611077A94 | RES CHIP 68005 1/8 |
| R3879 | 0611077 B 23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R3881 | 0611077A50 | RES CHIP 1005 1/8W |
| R3900 \& R3901 | 0680194M18 | RES 51 OHMS 5\% 1W |
| R3903 | 0680195M25 | RES CHIP 100 OHMS 5\% .5W |
| R3904 | 0611077 B 11 | RES CHIP 33K 5 1/8W |
| R3905 thru R3907 | 0611077A78 | RES CHIP $150051 / 8$ |
| R3908 | 0611077A50 | RES CHIP 1005 1/8W |
| $\begin{array}{\|l} \hline \text { R3920 \& } \\ \text { R3921 } \\ \hline \end{array}$ | 0611077A66 | RES CHIP 4705 1/8W |
| RM01 | 0611077 A01 | RES CHIP JUMPER |
|  <br> RT3905 | 0680149M02 | THERMISTOR CHIP 100K OHM |



HLE6062B and HLE6071B UHF 25-Watt PA Schematic

VIEWED FROM SIDE I


HLE6062B UHF 25-Watt PA Component Location Diagram, Side 1

VIEWED FROM SIDE 2


HLE6062B UHF 25-Watt PA Component Location Diagram, Side 2

## HLE6062B VHF R2 25-Watt PA Parts List

| ITEM | MOTOROLA <br> PART NUMBER | DESCRIPTION |
| :--- | :--- | :--- |
|  <br> C5804 | 2113741 N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5805 | 2113740 B21 | CAP CHIP REEL CL1 $+/-306.8$ |
| C5806 | $2113740 B 34$ | CAP CHIP REEL CL1 +/-30 24 |
| C5807 \& | $2113740 B 49$ | CAP CHIP REEL CL1 $+/-30100$ |
| C5808 |  |  |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C5923 | 2111078B01 | CAP CHIP RF 3.3 .25 NPO 100V |
| C5924 | 2113740B13 | CAP CHIP REEL CL1 +/-30 3.3 |
| C5926 thru C5928 \& C5930 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| CR5875 | 4880236E07 | DIODE TRANSIENT SUP |
| CR5900 | 4813825A05 | DIODE 30V HOT CARRIER MMBD301L |
| $\begin{aligned} & \hline \text { CR5920 \& } \\ & \text { CR5921 } \\ & \hline \end{aligned}$ | 4880236E24 | DIODE PIN |
| CR5922 | 4805119G18 | DIODE SMBD1023 STEP RECOVERY |
| E5803 | 2680187N02 | HEATSINK, TO39 |
| E5920 | 2680084N02 | SHLD HAR FLTR TIN PLATED |
| $\begin{array}{\|l} \hline J 5852 \text { \& } \\ J 5853 \end{array}$ | 2980014A03 | CLIP COAX TERMINAL |
| L5801 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L5802 | 2480091G21 | COIL AIRWOUND |
| L5804 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L5805 | 2480091 G23 | COIL AIRWOUND |
| L5806 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| $\begin{aligned} & \hline \begin{array}{l} \text { L5808 \& } \\ L 5809 \end{array} \\ & \hline \end{aligned}$ | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| $\begin{array}{\|l\|} \hline \text { L5820 \& } \\ \text { L5821 } \end{array}$ | 2480091G21 | COIL AIRWOUND |
| L5850 | 2480091 G20 | COIL AIRWOUND |
| L5851 | 2480091G21 | COIL AIRWOUND |
| L5852 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5853 | 2480091 G21 | COIL AIRWOUND |
| L5854 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5875 | 2480091G21 | COIL AIRWOUND |
| L5876 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5878 | 2480090G03 | COIL AIRWOUND |
| L5879 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5882 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L5901 |  | NOTPLACED |
| $\begin{array}{\|l\|} \hline \text { L5902 \& } \\ \hline \text { L5903 } \\ \hline \end{array}$ | 2480091G01 | COIL AIRWOUND |
| L5904 | 2480140E06 | INDUCTOR CHIP 130 NH |
| L5910 | 2480091G36 | COIL AIRWOUND |
| L5920 | 2480140E06 | INDUCTOR CHIP 130 NH |
| L5921 | 2480090G03 | COIL AIRWOUND |
| L5922 | 2480091 G21 | COIL AIRWOUND |
| L5923 | $2480091 \mathrm{G36}$ | COIL AIRWOUND |
| L5930 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| P0853 | 6480262N01 | PLATE LP FEEDTHRU ASSEMBLY |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| Q5800 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q5801 | 4880182 D 50 | TSTR SI SORF 4V 750MW 960MHZ |
| Q5803 | 4880225C09 | TSTR M25C09 |
| Q5806 | 4880141L02 | TSTR NPN SOT23 LO PROFILE |
| Q5850 | 4880225 C 27 | TSTR RF MRF654F IN CS12 PKG |
| R5801 | 0611077 A26 | RES CHIP $1051 / 8 \mathrm{~W}$ |
| R5803 | 0611077 A82 | RES CHIP $220051 / 8$ |
| R5805 | 0611077A26 | RES CHIP 105 1/8W |
| R5806 | 0611077 A43 | RES CHIP 515 1/8W |
| R5807 | 0611077 A82 | RES CHIP 22005 1/8 |
| R5808 | 0611077 A86 | RES CHIP $330051 / 8$ |
| R5809 | 0611077 A74 | RES CHIP $100051 / 8$ |
| R5810 | 0611077A26 | RES CHIP 105 1/8W |
| R5811 | 0611077 A74 | RES CHIP $100051 / 8$ |
| R5812 |  | NOTPLACED |
| R5813 | 0611077 A01 | RES CHIP JUMPER |
| R5816 | 0611077A80 | RES CHIP $180051 / 8$ |
| R5817 | 0611077A74 | RES CHIP 10005 1/8 |
| R5818 | 0611077A62 | RES CHIP $33051 / 8 \mathrm{~W}$ |
| R5850 | 0611077A10 | RES CHIP 2.25 1/8W |
| R5851 | 0611077A26 | RES CHIP 105 1/8W |
| R5875 | 1780228N01 | RESISTOR SHUNT |
| R5876 | $0611077 \mathrm{B11}$ | RES CHIP 33K 5 1/8W |
| R5877 | 0611077A94 | RES CHIP $680051 / 8$ |
| R5880 | 0611077 A 32 | RES CHIP $1851 / 8 \mathrm{~W}$ |
| R5881 | 0611077 A01 | RES CHIP JUMPER |
| R5882 | 0680195M01 | RES CHIP 10 OHMS 5\% .5W |
| R5883 | 0611077A32 | RES CHIP 185 1/8W |
| $\begin{aligned} & \text { R5900 \& } \\ & \text { R5901 } \end{aligned}$ | 0680195M25 | RES CHIP 100 OHMS 5\% .5W |
| R5904 | 0611077 A82 | RES CHIP 22005 1/8 |
| R5905 | 0611077 B 11 | RES CHIP $33 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R5920 \& R5921 | 0680194M18 | RES 51 OHMS 5\% 1W |
| $\begin{aligned} & \hline \text { R5922 \& } \\ & \text { R5923 } \\ & \hline \end{aligned}$ | 0611077A72 | RES CHIP 8205 1/8W |
| R5930 | 0611077 A01 | RES CHIP JUMPER |
| R5931 | --------- | NOTPLACED |
| RT5875 | 0680149M02 | THERMISTOR CHIP 100K OHM |
| RT5876 | --------- | NOTPLACED |
| RT5901 | 0680149M02 | THERMISTOR CHIP 100K OHM |
|  | 8480233N32 | BD CKT 25W PA UHF PNL |
|  | 2280128P01 | PIN POLARIZING |
|  | 8480233N02 | BD CKT 25W PA UHF |



HLE6043C, HLE6044C, and HLE6049B UHF 40-Watt PA Schematic


HLE6043C, HLE6044C, and HLE6049B UHF 40-Watt PA Component Location Diagram, Side 1


HLE6043C, HLE6044C, and HLE6049B UHF 40-Watt PA Component Location Diagram, Side 2

## HLE6043C UHF R3 40-Watt Parts List

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \hline \text { C5801 \& } \\ & \text { C5804 } \end{aligned}$ | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5805 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| C5806 | 2113740B31 | CAP CHIP REEL CL1 +/-30 18 |
| $\begin{aligned} & \hline \text { C5807 \& } \\ & \text { C5808 } \\ & \hline \end{aligned}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5810 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5811 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5813 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5815 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5816 | 2113743G24 | CAP CHIP 2.2 UF 16V +80-20\% |
| $\begin{aligned} & \hline \text { C5820 \& } \\ & \text { C5821 } \\ & \hline \end{aligned}$ | 2113740B25 | CAP CHIP REEL CL1 +/-30 10 |
| C5822 | 2113740B05 | CAP CHIP REEL CL1 +/-30 1.5 |
| C5823 | 2113740B21 | CAP CHIP REEL CL1 + -30 6.8 |
| C5824 | $2113740 B 27$ | CAP CHIP REEL CL1 +/-30 12 |
| C5826 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5850 | 2113740B01 | CAP CHIP REEL CL1 +/-30 1.0 |
| $\begin{aligned} & \hline \text { C5851 \& } \\ & \text { C5852 } \end{aligned}$ | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C5853 |  | NOTPLACED |
| C5854 | 2111078B42 | CAP CHIP RF 1005 NPO 100V |
| C5855 | 2111078B23 | CAP CHIP RF 245 NPO 100V |
| C5856 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5857 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5858 | 2113740B17 | CAP CHIP REEL CL1 +/-30 4.7 |
| $\begin{aligned} & \begin{array}{l} \text { C5860 } \\ \text { thru } \\ \text { C5862 } \end{array} \end{aligned}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5863 | 2113743G24 | CAP CHIP 2.2 UF 16V +80-20\% |
| $\begin{array}{\|l\|} \hline \text { C5864 } \\ \text { thru } \\ \text { C5869 } \end{array}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| $\begin{aligned} & \hline \text { C5875 \& } \\ & \text { C5876 } \end{aligned}$ | 2111078B32 | CAP CHIP RF 395 NPO 100V |
| $\begin{aligned} & \hline \text { C5877 \& } \\ & \text { C5878 } \\ & \hline \end{aligned}$ | 2184366F08 | CAP MICA 405 250V |
| C5879 | --------- | NOTPLACED |
| C5880 | 2111078 B15 | CAP CHIP RF 125 NPO 100V |
| C5881 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5883 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5884 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5885 | 2380090M24 | CAP ALU 1020 50V SURF MT |
| C5886 | 2113741N21 | CAP CHIP CL2 X7R 10\% 1000 |
| C5887 | 2111078B19 | CAP CHIP RF 165 NPO 100V |
| $\begin{array}{\|l} \hline \text { C5888 } \\ \text { thru } \\ \text { C5890 } \end{array}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5891 | 2113740B17 | CAP CHIP REEL CL1 +/-30 4.7 |
| C5892 | 2111078B13 | CAP CHIP RF 10.5 NPO 100 V |
| C5893 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION | $\begin{array}{\|c} \hline \text { ITEM } \\ \hline \begin{array}{l} \text { L5904 \& } \\ \text { L5920 } \end{array} \\ \hline \end{array}$ | PART NUMBER <br> 2480091G36 | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: |
| C5894 | ---- | NOTPLACED | $\begin{aligned} & \hline \hline \text { L5904 \& } \\ & \text { L5920 } \end{aligned}$ | 2480091G36 | COIL AIRWOUND |
| C5895 | 2113741N21 | CAP CHIP CL2 X7R 10\% 1000 |  |  |  |
| C5900 | --------- | NOTPLACED | $\begin{array}{\|l\|} \hline \text { L5921 \& } \\ \hline \text { L5922 } \\ \hline \end{array}$ | 2480090G03 | COIL AIRWOUND |
| C5901 \& | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |  |  |  |
| C5902 |  |  | L5923 | 2480091 G36 | COIL AIRWOUND |
| C5903 | $2113740 \mathrm{B17}$ | CAP CHIP REEL CL1 +/-30 4.7 | P0853 | 0980103M04 | ```RECEPTICAL RIGHT ANGLE 12``` |
| C5904 | $2113740 B 27$ | CAP CHIP REEL CL1 +/-30 12 |  |  |  |
| C5905 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 | Q5800 | 4880141L01 | TSTR PNP SOT23 LO PROFILETAPE |
| C5906 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |  |  |  |
| C5907 |  | NOTPLACED | Q5801 | 4880182D50 | TSTR SI SORF 4V 750MW960MHZ |
| C5920 | 2113743G24 | CAP CHIP 2.2 UF 16V +80-20\% |  |  |  |
| C5922 | 2111078 B42 | CAP CHIP RF 1005 NPO 100 V | Q5803 | 4880225C09 | TSTR M25C09 |
| C5923 | 2111078B05 | CAP CHIP RF 4.7.25 NPO 100 V | Q5803 | 2680187N02 | HEATSINK, TO39 <br> TSTR NPN SOT23 LO PROFILE TAPE |
| C5924 | 2111078B08 | CAP CHIP RF 6.2 . 25 NPO 100V | Q5806 | 4880141L02 |  |
| C5926 \& | 2111078B42 | CAP CHIP RF 1005 NPO 100V | Q5850 | 4880225C27 | TSTR RF MRF654F IN CS12 PKG |
| C5928 | 2113740B34 |  | Q5875 | 4880225C30 | TSTR RF 14W/50W UHF . 5 CQ |
|  | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 | R5801 | 0611077A26 | RES CHIP $1051 / 8 \mathrm{~W}$ |
| C5931 |  |  | R5802 | 0611077A50 | RES CHIP $10051 / 8 \mathrm{~W}$ |
| CR5875 | 4880236E07 | DIODE TRANSIENT SUP | R5803 | 0611077A82 | RES CHIP $220051 / 8$ |
| CR5900 | 4880236E05 | DIODE HOT CAR SOT 23 | R5805 | 0611077A26 | RES CHIP 105 1/8W |
| CR5920 | 4880236E24 | DIODE PIN | R5806 | 0611077A50 | RES CHIP 1005 1/8W |
|  |  |  | R5807 <br> R5808 | 0611077A82 | RES CHIP $220051 / 8$ |
| CR5921 |  |  |  | 0611077A90 | RES CHIP 47005 1/8 |
| CR5922 | 4805119G18 | DIODE SMBD1023 STEP RECOVERY | $\begin{array}{\|l\|} \hline \text { R5809 } \\ \hline \text { R5810 } \\ \hline \end{array}$ | 0611077A78 | RES CHIP 15005 1/8 |
|  |  |  |  | 0611077A26 | RES CHIP 105 1/8W |
| J0853 | 2880102M06 | PLUG VERTICAL 12 POSITION | $\begin{array}{\|l} \hline \text { R5810 } \\ \hline \text { R5811 } \\ \hline \end{array}$ | 0611077A74 | RES CHIP $100051 / 8$ |
| L5801 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ | R5812 | 0611077A01 | RES CHIP JUMPER |
|  |  |  | R5813 |  | NOTPLACED |
| L5802 | 2480091 G21 | COIL AIRWOUND | R5816 | 0611077A80 | RES CHIP $180051 / 8$ |
| L5805 | 2480091G23 | COIL AIRWOUND | R5817 | 0611077A74 | RES CHIP $100051 / 8$ |
| L5806 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR28MZ | $\begin{array}{\|l\|} \hline \text { R5818 } \\ \hline \text { R5850 } \\ \hline \end{array}$ | 0611077A62 | RES CHIP 3305 1/8W |
|  |  |  |  | 0611077A10 | RES CHIP 2.25 1/8W |
| L5809 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ | $\begin{array}{\|l\|} \hline \text { R5872 } \\ \hline \text { R5873 } \end{array}$ | 0611077A50 | RES CHIP 1005 1/8W |
|  |  |  |  | 0611077A50 | RES CHIP 1005 1/8W |
| L5810 | $\begin{array}{\|l\|} \hline 2484657 R 01 \\ \hline 2480091 G 21 \\ \hline \end{array}$ | INDUCTOR BEAD CHIP | $\begin{array}{\|l} \hline \text { R5873 } \\ \hline \text { R5875 } \\ \hline \end{array}$ | 1780228N01 | RESISTOR SHUNT |
| L5820 \& |  | COIL AIRWOUND | $\begin{array}{\|l} \hline \text { R5876 } \\ \text { R5877 } \end{array}$ |  | RES CHIP 33K 5 1/8W |
| L5821 |  |  |  | $\begin{array}{\|l\|} \hline \text { 0611077A94 } \\ \hline \text { 0611077A86 } \\ \hline \end{array}$ | RES CHIP $680051 / 8$ |
| L5850 | 2480091G21 | COIL AIRWOUND | $\begin{array}{\|l\|} \hline \text { R5877 } \\ \hline \text { R5878 } \\ \hline \text { R5879 \& } \\ \text { R5880 } \\ \hline \end{array}$ |  | RES CHIP $330051 / 8$ |
| L5851 | 2480091 G23 | COIL AIRWOUND |  | 0611077A10 | RES CHIP 2.25 1/8W |
| L5852 | $\begin{array}{\|l\|} \hline 2484657 R 01 \\ \hline 2480091 \mathrm{G} 21 \\ \hline \end{array}$ | INDUCTOR BEAD CHIP |  |  |  |
| L5853 |  | COIL AIRWOUND | $\begin{aligned} & \text { R5881 \& } \\ & \text { R5882 } \end{aligned}$ | 0611077A19 | RES CHIP 5.15 1/8W |
| L5854 | 2484657R01 | INDUCTOR BEAD CHIP |  |  |  |
| L5875 | 2480091G21 | COIL AIRWOUND | R5883 \&R5884 | 0611077A10 | RES CHIP 2.25 1/8W |
| L5876 |  | COIL AIRWOUND |  |  |  |
| L5878 | 2480090G03 | COIL AIRWOUND | $\begin{aligned} & \text { R5900 \& } \\ & \text { R5901 } \\ & \hline \end{aligned}$ | 0680194M18 | RES 51 OHMS 5\% 1W |
| L5879 | 2480090G05 |  |  |  |  |
| L5883 | $\begin{array}{\|l\|} \hline 2484657 \mathrm{R01} \\ \hline 2480091 \mathrm{G} 06 \\ \hline \end{array}$ | COIL AIRWOUND INDUCTOR BEAD CHIP | $\begin{aligned} & \text { R5902 \& } \\ & \text { R5903 } \\ & \hline \end{aligned}$ | --------- | NOTPLACED |
| L5900 |  | COIL AIRWOUND |  |  | RES CHIP $180051 / 8$ |
| L5901 | - - -------- | NOTPLACED | R5904 | 0611077A80 |  |
| L5902 \& |  |  | R5905 | $0611077 B 11$ | RES CHIP 33K $51 / 8 \mathrm{~W}$ |
| L5903 |  |  | R5906 | 0611077A50 | RES CHIP 1005 1/8W |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { R5920 \& } \\ & \text { R5921 } \\ & \hline \end{aligned}$ | 0680194M18 | RES 51 OHMS 5\% 1W |
| $\begin{aligned} & \text { R5922 \& } \\ & \text { R5923 } \end{aligned}$ | 0611077A78 | RES CHIP $150051 / 8$ |
| R5931 <br> thru <br> R5933 <br> R593 | 0611077A01 | RES CHIP JUMPER |
| R5934 | --------- | NOTPLACED |
| $\begin{aligned} & \text { RT5875 } \\ & \& \\ & \text { RT5904 } \end{aligned}$ | 0680149M02 | THERMISTOR CHIP 100K OHM |
|  | 8480198N34 | BD CKT 40W UHF PA PNL |

HLE6044C UHF R4 40-Watt PA Parts List

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \hline \text { C5801 \& } \\ & \text { C5804 } \end{aligned}$ | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5805 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| C5806 | 2113740B31 | CAP CHIP REEL CL1 +/-30 18 |
| $\begin{aligned} & \hline \text { C5807 \& } \\ & \text { C5808 } \end{aligned}$ | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C5810 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5811 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5813 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| $\begin{aligned} & \text { C5815 \& } \\ & \text { C5816 } \end{aligned}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| $\begin{aligned} & \hline \text { C5820 \& } \\ & \text { C5821 } \end{aligned}$ | 2113740B24 | CAP CHIP REEL CL1 +/-30 9.1 |
| C5822 | 2113740B05 | CAP CHIP REEL CL1 +/-30 1.5 |
| C5823 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| C5824 | 2113740B27 | CAP CHIP REEL CL1 +/-30 12 |
| C5826 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5850 | --------- | NOTPLACED |
| $\begin{aligned} & \text { C5851 \& } \\ & \text { C5852 } \end{aligned}$ | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C5853 | 2111078 B19 | CAP CHIP RF 165 NPO 100V |
| C5854 | 2111078B42 | CAP CHIP RF 1005 NPO 100V |
| C5855 | 2111078B22 | CAP CHIP RF 225 NPO 100V |
| C5856 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5857 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5858 | 2113740B17 | CAP CHIP REEL CL1 +/-30 4.7 |
| $\begin{aligned} & \hline \text { C5860 } \\ & \text { thru } \\ & \text { C5869 } \end{aligned}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| $\begin{aligned} & \text { C5875 \& } \\ & \text { C5876 } \end{aligned}$ | 2111078B32 | CAP CHIP RF 395 NPO 100V |
| $\begin{array}{\|l} \hline \text { C5877 \& } \\ \text { C5878 } \\ \hline \end{array}$ | 2184366F08 | CAP MICA 405 250V |
| C5879 | --- | NOTPLACED |
| C5880 | 2111078B08 | CAP CHIP RF 6.2 . 25 NPO 100V |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C5881 | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C5883 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5884 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5885 | 2380090M24 | CAP ALU 102050 V SURF MT |
| C5886 | 2113741N21 | CAP CHIP CL2 X7R 10\% 1000 |
| C5887 | --------- | NOTPLACED |
| $\begin{aligned} & \hline \text { C5888 \& } \\ & \text { C5889 } \end{aligned}$ | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C5890 | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C5891 | $2113740 B 17$ | CAP CHIP REEL CL1 +/-30 4.7 |
| C5892 | 2111078B08 | CAP CHIP RF 6.2 25 NPO 100V |
| C5893 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5894 | --------- | NOTPLACED |
| C5895 | 2113741N21 | CAP CHIP CL2 X7R 10\% 1000 |
| C5900 | --------- | NOTPLACED |
| $\begin{aligned} & \hline \text { C5901 \& } \\ & \text { C5902 } \end{aligned}$ | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C5903 | $2113740 B 17$ | CAP CHIP REEL CL1 +/-30 4.7 |
| C5904 | $2113740 B 25$ | CAP CHIP REEL CL1 +/-30 10 |
| C5905 | $2113740 B 21$ | CAP CHIP REEL CL1 +/-30 6.8 |
| C5906 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5907 | --------- | NOTPLACED |
| C5920 | 2113743G24 | CAP CHIP 2.2 UF 16V +80-20\% |
| C5922 | 2111078 B42 | CAP CHIP RF 1005 NPO 100 V |
| C5923 | 2111078B01 | CAP CHIP RF 3.3 .25 NPO 100V |
| C5924 | 2111078B05 | CAP CHIP RF 4.7 . 25 NPO 100V |
| $\begin{aligned} & \hline \text { C5926 \& } \\ & \text { C5927 } \end{aligned}$ | 2111078842 | CAP CHIP RF 1005 NPO 100V |
| C5928 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| $\begin{aligned} & \hline \text { C5930 \& } \\ & \text { C5931 } \end{aligned}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| CR5875 | 4880236E07 | DIODE TRANSIENT SUP |
| CR5900 | 4813825A05 | DIODE 30V HOT CARRIER MMBD301L |
| $\begin{aligned} & \hline \text { CR5920 } \\ & \& \\ & \text { CR5921 } \end{aligned}$ | 4880236E24 | DIODE PIN |
| CR5922 | 4805119G18 | DIODE SMBD1023 STEP RECOVERY |
| J0853 | 2880102M06 | PLUG VERTICAL 12 POSITION |
| L5801 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L5802 | 2480091G21 | COIL AIRWOUND |
| L5805 | 2480091G23 | COIL AIRWOUND |
| $\begin{array}{\|l\|} \hline \text { L5806 \& } \\ \text { L5809 } \end{array}$ | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L5810 | 2484657R01 | INDUCTOR BEAD CHIP |
| $\begin{aligned} & \text { L5820, } \\ & \text { L5821 \& } \\ & \text { L5850 } \end{aligned}$ | 2480091 G21 | COIL AIRWOUND |
| L5851 | 2480091 G23 | COIL AIRWOUND |
| L5852 | 2484657R01 | INDUCTOR BEAD CHIP |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| L5853 | 2480091G21 | COIL AIRWOUND |
| L5854 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5875 | 2480091 G21 | COIL AIRWOUND |
| L5876 | 2480091 G36 | COIL AIRWOUND |
| L5878 | 2480090 G 03 | COIL AIRWOUND |
| L5879 | 2480090G05 | COIL AIRWOUND |
| L5883 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5900 | 2480091 G06 | COIL AIRWOUND |
| L5901 |  | NOTPLACED |
| $\begin{aligned} & \hline \text { L5902 \& } \\ & \text { L5903 } \end{aligned}$ | 2480091 G24 | COIL AIRWOUND |
| $\begin{array}{\|l\|} \hline \text { L5904 \& } \\ \hline \text { L5920 } \\ \hline \end{array}$ | 2480091 G36 | COIL AIRWOUND |
| $\begin{array}{\|l\|} \hline \text { L5921 \& } \\ \text { L5922 } \end{array}$ | 2480090G03 | COIL AIRWOUND |
| L5923 | 2480091 G36 | COIL AIRWOUND |
| $\begin{array}{\|l\|} \hline \begin{array}{l} \text { MP5801 } \\ \text { thru } \\ \text { MP5803 } \end{array} \\ \hline \end{array}$ | 2980014A03 | CLIP COAX TERMINAL |
| P0853 | 0980103M04 | RECEPTICAL RIGHT ANGLE 12 POS |
| Q5800 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q5801 | 4880182 D 50 | TSTR SI SORF 4V 750MW 960MHZ |
| Q5803 | 4880225C09 | TSTR M25C09 |
| Q5806 | 4880141L02 | TSTR NPN SOT23 LO PROFILE TAPE |
| Q5850 | 4880225 C 27 | TSTR RF MRF654F IN CS12 PKG |
| Q5875 | 4880225 C 30 | TSTR RF 14W/50W UHF . 5 CQ |
| R5801 | 0611077A30 | RES CHIP 155 1/8W |
| R5802 | 0611077A50 | RES CHIP 1005 1/8W |
| R5803 | 0611077A82 | RES CHIP $220051 / 8$ |
| R5805 | 0611077A26 | RES CHIP 105 1/8W |
| R5806 | 0611077A50 | RES CHIP 1005 1/8W |
| R5807 | 0611077A82 | RES CHIP $220051 / 8$ |
| R5808 | 0611077A90 | RES CHIP $470051 / 8$ |
| R5809 | 0611077A78 | RES CHIP $150051 / 8$ |
| R5810 | 0611077A26 | RES CHIP $1051 / 8 \mathrm{~W}$ |
| R5811 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5812 | 0611077A01 | RES CHIP JUMPER |
| R5813 | --------- | NOTPLACED |
| R5816 | 0611077A80 | RES CHIP $180051 / 8$ |
| R5817 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5818 | 0611077A62 | RES CHIP 3305 1/8W |
| R5850 | 0611077A10 | RES CHIP 2.25 1/8W |
| $\begin{array}{\|l} \hline \text { R5872 \& } \\ \text { R5873 } \\ \hline \end{array}$ | 0611077A50 | RES CHIP 1005 1/8W |
| R5875 | 1780228N01 | RESISTOR SHUNT |
| R5876 | $0611077 \mathrm{B11}$ | RES CHIP 33K 5 1/8W |
| R5877 | 0611077A94 | RES CHIP $680051 / 8$ |
| R5878 | 0611077A86 | RES CHIP $330051 / 8$ |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R5879 | 0611077A10 | RES CHIP 2.25 1/8W |
| R5880 | 0611077A10 | RES CHIP 2.25 1/8W |
| R5881 | 0611077A19 | RES CHIP 5.15 1/8W |
| R5882 | 0611077A19 | RES CHIP 5.15 1/8W |
| R5883 \& R5884 | 0611077A10 | RES CHIP 2.25 1/8W |
| $\begin{aligned} & \text { R5900 \& } \\ & \text { R5901 } \end{aligned}$ | 0680194M18 | RES 51 OHMS 5\% 1W |
| $\begin{aligned} & \text { R5902 \& } \\ & \text { R5903 } \\ & \hline \end{aligned}$ |  | NOTPLACED |
| R5904 | 0611077A80 | RES CHIP $180051 / 8$ |
| R5905 | 0611077 B 11 | RES CHIP 33K $51 / 8 \mathrm{~W}$ |
| R5906 | 0611077A50 | RES CHIP 1005 1/8W |
| $\begin{array}{\|l} \hline \text { R5920 \& } \\ \text { R5921 } \\ \hline \end{array}$ | 0680194M18 | RES 51 OHMS 5\% 1W |
| $\begin{array}{\|l} \hline \text { R5922 \& } \\ \text { R5923 } \\ \hline \end{array}$ | 0611077A78 | RES CHIP $150051 / 8$ |
| $\begin{aligned} & \text { R5931 \& } \\ & \text { R5932 } \end{aligned}$ | 0611077A01 | RES CHIP JUMPER |
| R5933 | --------- | NOTPLACED |
| R5934 | 0611077A01 | RES CHIP JUMPER |
| $\begin{aligned} & \hline \text { RT5875 } \\ & \& \\ & \text { RT5904 } \end{aligned}$ | 0680149M02 | THERMISTOR CHIP 100K OHM |
|  | 8480198N34 | BD CKT 40W UHF PA PNL |

HLE6049B UHF R1 40-Watt PA Parts List

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C5801 \& | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5805 | 2113740B24 | CAP CHIP REEL CL1 +/-30 9.1 |
| C5806 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| $\begin{array}{\|l} \hline \text { C5807 \& } \\ \text { C5808 } \\ \hline \end{array}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5810 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5811 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5813 | 2113741 N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5815 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5816 | 2113743 G 24 | CAP CHIP 2.2 UF 16V +80-20\% |
| C5820 | 2113740B27 | CAP CHIP REEL CL1 +/-30 12 |
| C5821 | 2113740B28 | CAP CHIP REEL CL1 +/-30 13 |
| C5822 | 2113740B07 | CAP CHIP REEL CL1 +/-30 1.8 |
| C5823 | 2113740B24 | CAP CHIP REEL CL1 +/-30 9.1 |
| C5824 | 2113740B28 | CAP CHIP REEL CL1 +/-30 13 |
| C5826 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5850 | 2113740B11 | CAP CHIP REEL CL1 +/-30 2.7 |
| C5851 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C5852 | 2113740B35 | CAP CHIP REEL CL1 +/-30 27 |
| C5853 | --------- | NOTPLACED |
| C5854 | 2111078B42 | CAP CHIP RF 1005 NPO 100V |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C5855 | 2111078 B27 | CAP CHIP RF 305 NPO 100V |
| C5856 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5857 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5858 | 2113740B17 | CAP CHIP REEL CL1 +/-30 4.7 |
| C5860 \& | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| 5862 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5863 | 2113743G24 | CAP CHIP 2.2 UF 16V +80-20\% |
| C5864 <br> thru C5869 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| $\begin{aligned} & \text { C5875 \& } \\ & \text { C5876 } \end{aligned}$ | 2111078B32 | CAP CHIP RF 395 NPO 100V |
| $\begin{aligned} & \hline \text { C5877 \& } \\ & \text { C5878 } \\ & \hline \end{aligned}$ | 2184366F08 | CAP MICA 405 250V |
| C5879 |  | NOTPLACED |
| C5880 | 2111078 B22 | CAP CHIP RF 225 NPO 100V |
| C5881 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5883 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5884 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5885 | 2380090M24 | CAP ALU 1020 50V SURF MT |
| C5886 | 2113741N21 | CAP CHIP CL2 X7R 10\% 1000 |
| C5887 | 2111078 B22 | CAP CHIP RF 225 NPO 100V |
| $\begin{aligned} & \text { C5888 \& } \\ & \text { C5889 } \end{aligned}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5890 | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C5891 | $2113740 B 24$ | CAP CHIP REEL CL1 +/-30 9.1 |
| C5892 | 2111078 B15 | CAP CHIP RF 125 NPO 100V |
| C5893 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5894 |  | NOTPLACED |
| C5895 | 2113741N21 | CAP CHIP CL2 X7R 10\% 1000 |
| C5900 |  | NOTPLACED |
| C5901 | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C5902 | --------- | NOTPLACED |
| C5903 | $2113740 B 17$ | CAP CHIP REEL CL1 +/-30 4.7 |
| C5904 | 2113740 B 28 | CAP CHIP REEL CL1 +/-30 13 |
| C5905 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| C5906 | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C5920 | 2113743G24 | CAP CHIP 2.2 UF 16V +80-20\% |
| C5922 | 2111078B42 | CAP CHIP RF 1005 NPO 100 V |
| C5923 | 2111078B01 | CAP CHIP RF 3.3 .25 NPO 100V |
| C5924 | 2111078B08 | CAP CHIP RF 6.2 .25 NPO 100 V |
| $\begin{aligned} & \text { C5926 \& } \\ & \text { C5927 } \end{aligned}$ | 2111078B42 | CAP CHIP RF 1005 NPO 100V |
| C5928 | $2113740 B 34$ | CAP CHIP REEL CL1 +/-30 24 |
| $\begin{aligned} & \hline \text { C5930 \& \& } \\ & \hline \end{aligned}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| CR5875 | 4880236E07 | DIODE TRANSIENT SUP |
| CR5900 | 4813825A05 | DIODE 30V HOT CARRIER MMBD301L |
| $\begin{array}{\|l} \hline \text { CR5920 \& } \\ \text { CR5921 } \\ \hline \end{array}$ | 4880236E24 | DIODE PIN |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| CR5922 | 4805119G18 | DIODE SMBD1023 STEP RECOVERY |
| E5803 | 2680187N02 | HEATSINK, TO39 |
| E5920 | 2680188N02 | SHLD HAR FILTER FENCE TIN PLAT |
| E5922 | 2880102M06 | PLUG VERTICAL 12 POSITION |
| L5801 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L5802 | 2480091 G21 | COIL AIRWOUND |
| L5805 | 2480091 G23 | COIL AIRWOUND |
| $\begin{array}{\|l\|l} \hline \text { L5806 \& } \\ \text { L5809 } \end{array}$ | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L5810 | 2484657R01 | INDUCTOR BEAD CHIP |
| $\begin{array}{\|l\|} \hline \text { L5820, } \\ \text { L5821 \& } \\ \text { L5850 } \end{array}$ | 2480091 G21 | COIL AIRWOUND |
| L5851 | 2480091 G23 | COIL AIRWOUND |
| L5852 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5853 | 2480091 G21 | COIL AIRWOUND |
| L5854 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5875 | 2480091 G21 | COIL AIRWOUND |
| L5876 | 2480091 G36 | COIL AIRWOUND |
| L5878 | 2480090G03 | COIL AIRWOUND |
| L5879 | 2480090G05 | COIL AIRWOUND |
| L5883 | 2484657R01 | INDUCTOR BEAD CHIP |
| $\begin{array}{\|l\|l\|} \hline L 5900 \text { \& } \\ \hline \text { L5901 } \\ \hline \end{array}$ |  | NOTPLACED |
| $\begin{aligned} & \hline \text { L5902 \& } \\ & \text { L5903 } \\ & \hline \end{aligned}$ | 2480091G24 | COIL AIRWOUND |
| $\begin{aligned} & \hline \text { L5904 \& } \\ & \hline \text { L5920 } \\ & \hline \end{aligned}$ | 2480091 G36 | COIL AIRWOUND |
| L5921 | 2480090G04 | COIL AIRWOUND |
| L5922 | 2480090 G 18 | COIL AIRWOUND |
| L5923 | 2480091 G36 | COIL AIRWOUND |
| $\begin{array}{\|l\|l} \hline \begin{array}{l} \text { MP5801 } \\ \text { thru } \\ \text { MP5803 } \end{array} \end{array}$ | 2980014A03 | CLIP COAX TERMINAL |
| P0853 | 0980103M04 | RECEPTICAL RIGHT ANGLE 12 POS |
| Q5800 | 4880141L01 | TSTR PNP SOT23 LO PROFILE |
| Q5801 | 4880182D50 | TSTR SI SORF 4V 750MW 960MHZ |
| Q5803 | 4880225C09 | TSTR M25C09 |
| Q5806 | 4880141L02 | TSTR NPN SOT23 LO PROFILE TAPE |
| Q5850 | 4880225C27 | TSTR RF MRF654F IN CS12 PKG |
| Q5875 | 4880225 C 30 | TSTR RF 14W/50W UHF . 5 CQ |
| R5801 | 0611077A30 | RES CHIP 155 1/8W |
| R5802 | 0611077A50 | RES CHIP 1005 1/8W |
| R5803 | 0611077A82 | RES CHIP $220051 / 8$ |
| R5805 | 0611077A26 | RES CHIP 105 1/8W |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R5806 | 0611077A50 | RES CHIP 1005 1/8W |
| R5807 | 0611077A82 | RES CHIP $220051 / 8$ |
| R5808 | 0611077A90 | RES CHIP $470051 / 8$ |
| R5809 | 0611077A78 | RES CHIP $150051 / 8$ |
| R5810 | 0611077A26 | RES CHIP 105 1/8W |
| R5811 | 0611077 A74 | RES CHIP $100051 / 8$ |
| R5812 | 0611077A01 | RES CHIP JUMPER |
| R5813 | --------- | NOTPLACED |
| R5816 | 0611077 A80 | RES CHIP $180051 / 8$ |
| R5817 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5818 | 0611077A62 | RES CHIP 3305 1/8W |
| R5850 | 0611077A10 | RES CHIP 2.25 1/8W |
| $\begin{array}{\|l} \hline \text { R5872 \& } \\ \text { R5873 } \\ \hline \end{array}$ | 0611077A50 | RES CHIP 1005 1/8W |
| R5875 | 1780228N01 | RESISTOR SHUNT |
| R5876 | $0611077 \mathrm{B11}$ | RES CHIP 33K 5 1/8W |
| R5877 | 0611077A94 | RES CHIP $680051 / 8$ |
| R5878 | 0611077A86 | RES CHIP $330051 / 8$ |
| $\begin{array}{\|l\|l} \hline \text { R5879 \& } \\ \text { R5880 } \end{array}$ | 0611077A10 | RES CHIP 2.25 1/8W |
| $\begin{aligned} & \text { R5881 \& } \\ & \text { R5882 } \end{aligned}$ | 0611077A19 | RES CHIP 5.15 1/8W |
| $\begin{array}{\|l} \hline \text { R5883 \& } \\ \text { R5884 } \\ \hline \end{array}$ | 0611077A10 | RES CHIP 2.25 1/8W |
| $\begin{array}{\|l} \hline \text { R5900 \& } \\ \text { R5901 } \\ \hline \end{array}$ | 0680194M18 | RES 51 OHMS 5\% 1W |
| $\begin{array}{\|l} \hline \text { R5902 \& } \\ \text { R5903 } \\ \hline \end{array}$ |  | NOTPLACED |
| R5904 | 0611077 A80 | RES CHIP $180051 / 8$ |
| R5905 | 0611077 B 11 | RES CHIP 33K 5 1/8W |
| R5906 | 0611077A50 | RES CHIP 1005 1/8W |
| R5907 | 0611077A36 | RES CHIP 275 1/8W |
| $\begin{aligned} & \text { R5920 \& } \\ & \text { R5921 } \\ & \hline \end{aligned}$ | 0680194M18 | RES 51 OHMS 5\% 1W |
| $\begin{array}{\|l} \hline \text { R5922 \& } \\ \text { R5923 } \\ \hline \end{array}$ | 0611077A78 | RES CHIP $150051 / 8$ |
| R5931 | 0611077 A01 | RES CHIP JUMPER |
| R5932 |  | NOTPLACED |
| $\begin{aligned} & \text { R5933 \& } \\ & \text { R5934 } \end{aligned}$ | 0611077A01 | RES CHIP JUMPER |
| RT5875 | 0680149M02 | THERMISTOR CHIP 100K OHM |
| RT5904 | 0680149M02 | THERMISTOR CHIP 100K OHM |
|  | 4280120P01 | CLIP RECEIVE GROUND |
|  | 8480198N34 | BD CKT 40W UHF PA PNL |



HLE6039C, HLE6040C, and HLE6051C UHF 100-Watt PA Schematic

VIEWED FROM SIDE I


HLE6039C, HLE6040C, and HLE6051C UHF 100-Watt PA Component Location Diagram, Side 1


HLE6039C, HLE6040C, and HLE6051C UHF 100-Watt PA Component Location Diagram, Side 2

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C5801 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5804 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5805 | 2113740B25 | CAP CHIP REEL CL1 +/-30 10 |
| C5806 | 2113740B30 | CAP CHIP REEL CL1 +/-30 16 |
| C5807 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5808 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5810 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5811 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5813 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| $\begin{aligned} & \text { C5815 } \\ & \text { thru } \\ & \text { C5818 } \end{aligned}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5820 | 2113740 B 27 | CAP CHIP REEL CL1 +/-30 12 |
| C5821 | 2113740 B23 | CAP CHIP REEL CL1 +/-30 8.2 |
| C5822 | 2113740B05 | CAP CHIP REEL CL1 +/-30 1.5 |
| C5823 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| C5824 | 2113740B27 | CAP CHIP REEL CL1 +/-30 12 |
| C5826 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5830 \& | 2380090M32 | CAP ALU 2202035 V |
| C5838 | 2113740B73 | CAP CHIP REEL CL1 +/-30 1000 |
| C5839 | $2113740 B 25$ | CAP CHIP REEL CL1 +/-30 10 |
| $\begin{aligned} & \text { C5840 } \\ & \text { thru } \\ & \text { C5844 } \end{aligned}$ | $2113740 B 73$ | CAP CHIP REEL CL1 +/-30 1000 |
| C5846 | 2113740B73 | CAP CHIP REEL CL1 +/-30 1000 |
| $\begin{aligned} & \text { C5848 \& } \\ & \text { C5849 } \end{aligned}$ | 2113740B73 | CAP CHIP REEL CL1 +/-30 1000 |
| $\begin{aligned} & \text { C5851 \& } \\ & \text { C5852 } \end{aligned}$ | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C5853 | 2111078B19 | CAP CHIP RF 165 NPO 100V |
| C5854 | 2111078B40 | CAP CHIP RF 825 NPO 100V |
| C5855 | 2111078B19 | CAP CHIP RF 165 NPO 100V |
| C5856 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5858 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| C5859 | 2113740B17 | CAP CHIP REEL CL1 $+/-304.7$ |
| C5860 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| $\begin{aligned} & \text { C5861 \& } \\ & \text { C5862 } \end{aligned}$ | 2111078B29 | CAP CHIP RF 335 NPO 100V |
| $\begin{aligned} & \text { C5863 \& } \\ & \text { C5864 } \end{aligned}$ | $2184366 F 43$ | CAP MICA 305250 V |
| C5865 | 2111078B07 | CAP CHIP RF 5.6 .25 NPO 100V |
| C5867 | 2111078B42 | CAP CHIP RF 1005 NPO 100V |
| C5868 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5870 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5872 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5873 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5874 | 2380090M24 | CAP ALU 1020 50V SURF MT |
| C5875 | 2111078B08 | CAP CHIP RF 6.2 .25 NPO 100V |
| C5876 | 2111078 B22 | CAP CHIP RF 225 NPO 100V |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { C5877 \& } \\ & \text { C5878 } \end{aligned}$ | 2111078 B 13 | CAP CHIP RF 10.5 NPO 100V |
| $\begin{aligned} & \text { C5879 \& } \\ & \text { C5880 } \end{aligned}$ | 2111078 B27 | CAP CHIP RF 305 NPO 100V |
| $\begin{aligned} & \hline \text { C5883 \& } \\ & \text { C5884 } \\ & \hline \end{aligned}$ | $2113740 B 73$ | CAP CHIP REEL CL1 +/-30 1000 |
| $\begin{aligned} & \text { C5885 } \\ & \text { thru } \\ & \text { C5888 } \end{aligned}$ | $2184366 F 43$ | CAP MICA 305250 V |
| $\begin{aligned} & \hline \text { C5889 \& } \\ & \text { C5890 } \end{aligned}$ | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| $\begin{array}{\|l} \hline \begin{array}{l} \text { C5891 } \\ \text { thru } \\ \text { C5899 } \end{array} \\ \hline \end{array}$ | $2184366 F 43$ | CAP MICA 305250 V |
| C5897 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5898 |  | NOTPLACED |
| C5899 | 2113742B18 | CAP CER 4.7 PF +- . 25PF |
| C5900 | 2111078 B27 | CAP CHIP RF 305 NPO 100 V |
| C5901 | 2111078B42 | CAP CHIP RF 1005 NPO 100V |
| C5904 | 2113740B01 | CAP CHIP REEL CL1 +/-30 1.0 |
| C5905 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5906 | 2113740B29 | CAP CHIP REEL CL1 +/-30 15 |
| C5908 | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C5909 | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C5910 | 2113740B13 | CAP CHIP REEL CL1 +/-30 3.3 |
| C5912 | 2111078B42 | CAP CHIP RF 1005 NPO 100V |
| C5915 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5917 | 2111078B01 | CAP CHIP RF 3.3 .25 NPO 100V |
| C5918 | $2113740 B 13$ | CAP CHIP REEL CL1 +/-30 3.3 |
| $\begin{aligned} & \hline \text { C5919 \& } \\ & \text { C5921 } \end{aligned}$ | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C5923 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5924 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5926 | 2111078B09 | CAP CHIP RF 6.8 .25 NPO 100V |
| C5928 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5965 | 2111078B09 | CAP CHIP RF 6.8 .25 NPO 100 V |
| C5997 | 2113742B22 | CAP CER 10.0 PF . 50 PF |
| CR5875 | 4880222R01 | DIODE REVERSE POLARITY SPECTRA |
| $\begin{array}{\|l} \hline \text { CR5900 } \\ \text { \& } \\ \text { CR5902 } \\ \hline \end{array}$ | 4880121R01 | PWR PIN DIODE SMD TAPE \& REEL |
| $\begin{aligned} & \text { CR5904 } \\ & \& \\ & \text { CR5905 } \\ & \hline \end{aligned}$ | 4805656W04 | DIODE SOT 23 PIN |
| CR5906 | 4813825A05 | DIODE 30V HOT CARRIER MMBD301L |
| $\begin{aligned} & \hline \text { E5800 \& } \\ & \text { E5802 } \\ & \hline \end{aligned}$ | 4280088P01 | CLIP COAX |
| E5803 | 2680187N02 | HEATSINK, TO39 |
| E5804 | 2680201R02 | HEATSINK DIODE TIN PLATED |
| E5806 | 4280088P01 | CLIP COAX |
| E5807 | 4280088P01 | CLIP COAX |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| E5808 | 4280202R02 | CLIP COAX TIN PLATED |
| J0001 | 2880102M11 | PLUG VERTICAL 12 POS |
| J5901 | 4280088P01 | CLIP COAX |
| L5801 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L5802 | 2480091 G21 | COIL AIRWOUND |
| L5805 | 2480091 G23 | COIL AIRWOUND |
| L5806 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| $\begin{aligned} & \hline \text { L5810 \& } \\ & \text { L5811 } \\ & \hline \end{aligned}$ | 2484657R01 | INDUCTOR BEAD CHIP |
| $\begin{array}{\|l\|} \hline \text { L5820, } \\ \text { L5821 \& } \\ \text { L5850 } \end{array}$ | 2480091 G21 | COIL AIRWOUND |
| L5851 | 2480091 G23 | COIL AIRWOUND |
| L5852 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5853 | 2480090G03 | COIL AIRWOUND |
| L5854 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5855 | 2480091G21 | COIL AIRWOUND |
| L5857 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5858 | 2480090G03 | COIL AIRWOUND |
| L5859 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5875 | 2480090G03 | COIL AIRWOUND |
| $\begin{array}{\|l} \hline \text { L5876 \& } \\ \text { L5877 } \end{array}$ | 2480091 G21 | COIL AIRWOUND |
| $\begin{array}{\|l\|l\|} \hline L 5878 \text { \& } \\ \hline \text { L5879 } \\ \hline \end{array}$ | 2484657R01 | INDUCTOR BEAD CHIP |
| $\begin{aligned} & \hline L 5880 \text { \& } \\ & \hline \text { L5881 } \\ & \hline \end{aligned}$ | 2480091 G36 | COIL AIRWOUND |
| L5882 | 2480090G10 | COIL AIRWOUND |
| L5884 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5900 | $2480090 \mathrm{G13}$ | COIL AIRWOUND |
| L5901 | $2480090 \mathrm{G16}$ | COIL AIRWOUND RF 000.000 |
| L5902 | $2480090 \mathrm{G19}$ | COIL AIRWOUND |
| L5903 | 2480090G03 | COIL AIRWOUND |
| L5904 | 2480140E11 | INDUCTOR CHIP 360 NH |
| L5905 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| $\begin{aligned} & \hline \text { L5906 } \\ & \text { thru } \\ & \text { L5910 } \end{aligned}$ | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28 MZ |
| L5927 | 2480091 G24 | COIL AIRWOUND |
| MP0002 | 2680139P02 | SHLD HAR FLTR TIN PLATED |
| Q5800 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q5801 | 4880182D50 | TSTR SI SORF 4V 750MW 960MHZ |
| Q5802 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q5803 | 4880225C09 | TSTR M25C09 |
| Q5804 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { Q5805 \& } \\ & \text { Q5806 } \end{aligned}$ | 4880141L02 | TSTR NPN SOT23 LO PROFILE TAPE |
| Q5850 | 4880225 C 27 | TSTR RF MRF654F IN CS12 PKG |
| Q5851 | 4880225C30 | TSTR RF 14W/50W UHF . 5 CQ |
| $\begin{aligned} & \text { Q5875 \& } \\ & \text { Q5876 } \end{aligned}$ | 4880225C29 | TSTR RF MRF658 IN FLANGE PKG |
| R5801 | 0611077A30 | RES CHIP 155 1/8W |
| R5802 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5803 | 0611077A82 | RES CHIP $220051 / 8$ |
| R5805 | 0611077A26 | RES CHIP 105 1/8W |
| R5806 | 0611077A50 | RES CHIP 1005 1/8W |
| R5807 | 0611077A82 | RES CHIP $220051 / 8$ |
| R5808 | 0611077A90 | RES CHIP $470051 / 8$ |
| R5809 | 0611077A78 | RES CHIP $150051 / 8$ |
| R5810 | 0611077A26 | RES CHIP 105 1/8W |
| R5811 | 0611077A74 | RES CHIP 10005 1/8 |
| R5812 | 0611077A01 | RES CHIP JUMPER |
| R5816 | 0611077A80 | RES CHIP 18005 1/8 |
| R5817 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5818 | 0611077A62 | RES CHIP $33051 / 8 \mathrm{~W}$ |
| R5819 | 0611077A86 | RES CHIP $330051 / 8$ |
| R5820 | 0611077B01 | RES CHIP $12 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R5821 | 0611077A90 | RES CHIP $470051 / 8$ |
| R5822 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5823 | 0611077A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R5824 | 0611077A90 | RES CHIP $470051 / 8$ |
| R5825 | 0611077B05 | RES CHIP 18K $51 / 8 \mathrm{~W}$ |
| R5826 | 0611077A68 | RES CHIP 5605 1/8W |
| R5827 | 0611077A50 | RES CHIP 1005 1/8W |
| R5828 | 0611077 B 15 | RES CHIP 47K $51 / 8 \mathrm{~W}$ |
| R5843 | 0611077 B07 | RES CHIP $22 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R5850 \& R585 | 0611077A10 | RES CHIP 2.25 1/8W |
| $\begin{aligned} & \text { R5853 \& } \\ & \text { R5854 } \end{aligned}$ | 0611077A10 | RES CHIP 2.25 1/8W |
| R5855 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5857 | 0611077A82 | RES CHIP 22005 1/8 |
| R5858 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5859 | 0611077A50 | RES CHIP 1005 1/8W |
| R5875 | 1780165C02 | RES SHUNT |
| $\begin{aligned} & \text { R5876 \& } \end{aligned}$ R5877 | 0611077A10 | RES CHIP 2.25 1/8W |
| R5878 | 0680194M01 | RES 10 OHMS 5\% 1W |
| $\begin{aligned} & \text { R5879 \& } \\ & \text { D5000 } \end{aligned}$ | 0680194M13 | RES 33 OHMS 5\% 1W |
| $\begin{aligned} & \text { R5881 \& } \\ & \text { R5882 } \end{aligned}$ | 0680194M01 | RES 10 OHMS 5\% 1W |
| R5900 | 0680194M18 | RES 51 OHMS 5\% 1W |
| R5901 | 0680194M01 | RES 10 OHMS 5\% 1W |
| R5904 | 0611077A50 | RES CHIP 1005 1/8W |
| R5905 | 0611077A60 | RES CHIP $27051 / 8 \mathrm{~W}$ |
| R5906 | 0611077A01 | RES CHIP JUMPER |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R5907 | 0611077 B11 | RES CHIP 33K $51 / 8 \mathrm{~W}$ |
| R5908 \& R5909 | 0611077A54 | RES CHIP 1505 1/8W |
| R5910 | 0611077 B23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R5911 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5912 \& R5913 | $0611077 \mathrm{B47}$ | RES CHIP 1 MEG 5 1/8W |
| R5914 | 0611077A90 | RES CHIP $470051 / 8$ |
| R5915 | 0611077A26 | RES CHIP 105 1/8W |
| R5916 | 0611077A66 | RES CHIP 4705 1/8W |
| $\begin{array}{\|l\|} \hline \text { RT5875 } \\ \text { \& RT5904 } \\ \hline \end{array}$ | 0680149M02 | THERMISTOR CHIP 100K OHM |
| Z1 | 4813830A14 | DIODE 5.1V 5\% 225MW MMBZ5231B |

HLE6040C UHF R4 100-Watt PA Parts List

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C5801 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5804 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5805 | 2113740B25 | CAP CHIP REEL CL1 +/-30 10 |
| C5806 | $2113740 B 27$ | CAP CHIP REEL CL1 +/-30 12 |
| C5807 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5808 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5810 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5811 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5813 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5815 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| thru C5818 |  |  |
| C5820 | 2113740B24 | CAP CHIP REEL CL1 +/-30 9.1 |
| C5820 | 2113740B21 | CAP CHIP REEL CL1 + +-30 6.8 |
| C5821 | 2113740B23 | CAP CHIP REEL CL1 +/-30 8.2 |
| C5822 | $2113740 B 17$ | CAP CHIP REEL CL1 +/-30 4.7 |
| C5823 | 2113740B23 | CAP CHIP REEL CL1 +/-30 8.2 |
| C5824 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| C5826 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5830 \& | 2380090M32 | CAP ALU 2202035 V |
| C5838 | 2113740B73 | CAP CHIP REEL CL1 +/-30 1000 |
| C5839 | 2113740B24 | CAP CHIP REEL CL1 +/-30 9.1 |
| C5840 | $2113740 B 73$ | CAP CHIP REEL CL1 +/-30 1000 |
| thru |  |  |
| C5844 |  |  |
| C5846 | $2113740 B 73$ | CAP CHIP REEL CL1 +/-30 1000 |
| C5848 \& | 2113740B73 | CAP CHIP REEL CL1 +/-30 1000 |
| C5849 |  |  |
| C5851 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C5852 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C5853 | 2111078B21 | CAP CHIP RF 205 NPO 100V |
| C5854 | 2111078B42 | CAP CHIP RF 1005 NPO 100V |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C5855 | 2111078B22 | CAP CHIP RF 225 NPO 100V |
| C5856 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5858 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| C5859 | 2113740B17 | CAP CHIP REEL CL1 +/-30 4.7 |
| C5860 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| $\begin{aligned} & \text { C5861 \& } \\ & \text { C5862 } \end{aligned}$ | 2111078B23 | CAP CHIP RF 245 NPO 100V |
| $\begin{aligned} & \text { C5863 \& } \\ & \text { C5864 } \end{aligned}$ | $2113742 B 27$ | CAP CER 27.0 PF 5\% PF |
| C5865 | 2111078B05 | CAP CHIP RF 4.7 . 25 NPO 100 V |
| C5866 | 2111078B15 | CAP CHIP RF 125 NPO 100 V |
| C5867 | 2111078B42 | CAP CHIP RF 1005 NPO 100V |
| C5868 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5870 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5872 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5873 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5874 | 2380090M24 | CAP ALU 1020 50V SURF MT |
| C5875 | 2111078B08 | CAP CHIP RF 6.2 . 25 NPO 100V |
| C5876 | 2111078B22 | CAP CHIP RF 225 NPO 100V |
| C5877 | 2111078B13 | CAP CHIP RF 10.5 NPO 100V |
| C5878 | 2111078B15 | CAP CHIP RF 125 NPO 100V |
| $\begin{aligned} & \hline \text { C5879 \& } \\ & \text { C5880 } \\ & \hline \end{aligned}$ | 2111078 B 27 | CAP CHIP RF 305 NPO 100V |
| $\begin{aligned} & \hline \text { C5883 \& } \\ & \text { C5884 } \end{aligned}$ | $2113740 B 73$ | CAP CHIP REEL CL1 +/-30 1000 |
| $\begin{array}{\|l} \hline \text { C5885 } \\ \text { thru } \\ \text { C5888 } \\ \hline \end{array}$ | $2113742 B 27$ | CAP CER 27.0 PF 5\% PF |
| C5889 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5890 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| $\begin{aligned} & \begin{array}{l} \text { C5891 } \\ \text { thru } \\ \text { C5894 } \end{array} \end{aligned}$ | 2113742B27 | CAP CER 27.0 PF 5\% PF |
| C5897 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5898 | $2113742 B 17$ | CAP CER 3.9 PF +-. 25PF |
| C5898 | 2113742B18 | CAP CER 4.7 PF +- .25PF |
| C5900 | 2111078B27 | CAP CHIP RF 305 NPO 100V |
| C5901 | 2111078B42 | CAP CHIP RF 1005 NPO 100V |
| C5904 | 2113740B01 | CAP CHIP REEL CL1 +/-30 1.0 |
| C5905 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5906 | 2113740B29 | CAP CHIP REEL CL1 +/-30 15 |
| $\begin{aligned} & \text { C5908 \& } \\ & \text { C5909 } \\ & \hline \end{aligned}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5910 | 2113740B11 | CAP CHIP REEL CL1 +/-30 2.7 |
| C5912 | 2111078 B42 | CAP CHIP RF 1005 NPO 100V |
| C5915 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5917 | 2111078B01 | CAP CHIP RF 3.3 .25 NPO 100 V |
| C5918 | $2113740 B 11$ | CAP CHIP REEL CL1 +/-30 2.7 |
| $\begin{aligned} & \text { C5919 \& } \\ & \text { C5921 } \end{aligned}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5923 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5924 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C5925 | 2113740B73 | CAP CHIP REEL CL1 +/-30 1000 |
| C5926 | 2111078B08 | CAP CHIP RF 6.2 .25 NPO 100 V |
| C5928 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5965 | 2111078B19 | CAP CHIP RF 165 NPO 100V |
| C5997 | 2113742B17 | CAP CER 3.9 PF +- . 25 PF |
| C5998 | $2113740 B 47$ | CAP CHIP REEL CL1 +/-30 82 |
| C5999 | $2113742 B 20$ | CAP CER 6.8 PF . 25 PF |
| CR5875 | 4880222R01 | DIODE REVERSE POLARITY SPECTRA |
| $\begin{array}{\|l} \hline \text { CR5900 } \\ \& \\ \text { CR5902 } \end{array}$ | 4880121R01 | PWR PIN DIODE SMD TAPE \& REEL |
| $\begin{array}{\|l\|l\|} \hline \text { CR5904 } \\ \& \\ \text { CR5905 } \end{array}$ | 4880142L01 | DIODE PIN SOT 23 MMBV 3401 |
| CR5906 | 4813825A05 | DIODE 30V HOT CARRIER MMBD301L |
| $\begin{aligned} & \hline \text { E5800 \& } \\ & \text { E5802 } \\ & \hline \end{aligned}$ | 4280088P01 | CLIP COAX |
| E5803 | 2680187N02 | HEATSINK, TO39 |
| E5804 | 8480110P05 | BD CKT PA UHF |
| E5804 | 2680201R02 | HEATSINK DIODE TIN PLATED |
| $\begin{aligned} & \begin{array}{l} \text { E5806 } \\ \text { thru } \\ \text { E5808 } \end{array} \end{aligned}$ | 4280088P01 | CLIP COAX |
| E5808 | 4280202R02 | CLIP COAX TIN PLATED |
| J0001 | 2880102M11 | PLUG VERTICAL 12 POS |
| L5801 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L5802 | 2480091 G21 | COIL AIRWOUND |
| L5805 | 2480091 G23 | COIL AIRWOUND |
| L5806 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| $\begin{aligned} & \hline \text { L5820 \& } \\ & \text { L5821 } \end{aligned}$ | 2480091G21 | COIL AIRWOUND |
| L5850 | 2480091 G21 | COIL AIRWOUND |
| L5851 | 2480090G16 | COIL AIRWOUND RF 000.000 |
| L5853 | 2480090G03 | COIL AIRWOUND |
| L5855 | 2480091G21 | COIL AIRWOUND |
| $\begin{array}{\|l\|l\|} \hline \text { L5858 \& } \\ \text { L5875 } \end{array}$ | 2480090G03 | COIL AIRWOUND |
| $\begin{aligned} & \hline \text { L5876 \& } \\ & \hline \text { L5877 } \\ & \hline \end{aligned}$ | 2480091 G20 | COIL AIRWOUND |
| $\begin{array}{\|l} \hline \text { L5880 \& } \\ \text { L5881 } \\ \hline \end{array}$ | 2480091 G36 | COIL AIRWOUND |
| L5882 | 2480090G19 | COIL AIRWOUND |
| L5882 | 2480090G10 | COIL AIRWOUND |
| L5900 | 2480090G13 | COIL AIRWOUND |
| L5901 | $2480090 \mathrm{G16}$ | COIL AIRWOUND RF 000.000 |
| L5902 | $2480090 \mathrm{G19}$ | COIL AIRWOUND |
| L5903 | 2480090G03 | COIL AIRWOUND |
| L5904 | 2480140E11 | INDUCTOR CHIP 360 NH |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \begin{array}{l} \text { Lh505 } \\ \text { thru } \\ \text { L5910 } \end{array} \\ & \hline \end{aligned}$ | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L5927 | 2480091 G24 | COIL AIRWOUND |
| MP0002 | 2680139P02 | SHLD HAR FLTR TIN PLATED |
| Q5800 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q5801 | 4880182D50 | TSTR SI SORF 4V 750MW 960MHZ |
| Q5802 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q5803 | 4880225C09 | TSTR M25C09 |
| Q5804 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| $\begin{array}{\|l\|l} \hline \text { Q5805 \& } \\ \text { Q5806 } \end{array}$ | 4880141L02 | TSTR NPN SOT23 LO PROFILE TAPE |
| Q5850 | 4880225C27 | TSTR RF MRF654F IN CS12 PKG |
| Q5851 | 4880225C30 | TSTR RF 14W/50W UHF . 5 CQ |
| Q5875 | 4880225C29 | TSTR RF MRF658 IN FLANGE PKG |
| Q5876 | 4880225C29 | TSTR RF MRF658 IN FLANGE PKG |
| R5801 | 0611077A30 | RES CHIP 155 1/8W |
| R5802 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5803 | 0611077 A82 | RES CHIP $220051 / 8$ |
| R5805 | 0611077A26 | RES CHIP 105 1/8W |
| R5806 | 0611077A50 | RES CHIP 1005 1/8W |
| R5807 | 0611077A82 | RES CHIP 22005 1/8 |
| R5808 | 0611077A90 | RES CHIP $470051 / 8$ |
| R5809 | 0611077A78 | RES CHIP $150051 / 8$ |
| R5810 | 0611077A26 | RES CHIP 105 1/8W |
| R5811 | 0611077 A74 | RES CHIP $100051 / 8$ |
| R5812 | 0611077 A01 | RES CHIP JUMPER |
| R5816 | 0611077A80 | RES CHIP $180051 / 8$ |
| R5817 | 0611077 A74 | RES CHIP $100051 / 8$ |
| R5818 | 0611077A62 | RES CHIP $33051 / 8 \mathrm{~W}$ |
| R5819 | 0611077 A86 | RES CHIP $330051 / 8$ |
| R5820 | $0611077 \mathrm{B01}$ | RES CHIP 12 K 5 1/8W |
| R5821 | 0611077A90 | RES CHIP $470051 / 8$ |
| R5822 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5823 | 0611077 A98 | RES CHIP 10K 5 1/8W |
| R5824 | 0611077 A90 | RES CHIP $470051 / 8$ |
| R5825 | 0611077 B 05 | RES CHIP 18K 5 1/8W |
| R5826 | 0611077A68 | RES CHIP $56051 / 8 \mathrm{~W}$ |
| R5827 | 0611077A50 | RES CHIP $10051 / 8 \mathrm{~W}$ |
| R5828 | 0611077 B 15 | RES CHIP 47K 5 1/8W |
| R5843 | 0611077 B 07 | RES CHIP 22K 5 1/8W |
| $\begin{aligned} & \text { R5850 \& } \\ & \text { R5851 } \end{aligned}$ | 0611077A10 | RES CHIP 2.25 1/8W |
| $\begin{array}{\|l} \hline \text { R5853 \& } \\ \text { R5854 } \\ \hline \end{array}$ | 0611077A10 | RES CHIP 2.25 1/8W |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R5855 | 0611077A50 | RES CHIP 1005 1/8W |
| R5857 | 0611077A82 | RES CHIP $220051 / 8$ |
| R5858 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5859 | 0611077A50 | RES CHIP $10051 / 8 \mathrm{~W}$ |
| R5875 | 1780165C02 | RES SHUNT |
| $\begin{aligned} & \text { R5876 \& } \\ & \text { R5877 } \end{aligned}$ | 0611077A10 | RES CHIP 2.25 1/8W |
| R5878 | 0680194M01 | RES 10 OHMS 5\% 1W |
| $\begin{aligned} & \text { R5879 \& } \\ & \text { R5880 } \end{aligned}$ | 0680194M13 | RES 33 OHMS 5\% 1W |
| R5881 | 0680194M01 | RES 10 OHMS 5\% 1W |
| R5882 | 0680194M01 | RES 10 OHMS 5\% 1W |
| R5900 | 0680194M18 | RES 51 OHMS 5\% 1W |
| R5901 | 0680194M01 | RES 10 OHMS 5\% 1W |
| R5904 | 0611077A52 | RES CHIP 1205 1/8W |
| R5905 | 0611077A60 | RES CHIP 2705 1/8W |
| R5906 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5907 | $0611077 B 11$ | RES CHIP 33K $51 / 8 \mathrm{~W}$ |
| $\begin{aligned} & \text { R5908 \& } \\ & \text { R5909 } \end{aligned}$ | 0611077A54 | RES CHIP 1505 1/8W |
| R5910 | $0611077 B 23$ | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R5911 | 0611077A01 | RES CHIP JUMPER |
| $\begin{aligned} & \text { R5912 \& } \\ & \text { R5913 } \\ & \hline \end{aligned}$ | $0611077 B 47$ | RES CHIP 1 MEG 5 1/8W |
| R5915 | 0611077A26 | RES CHIP 105 1/8W |
| R5916 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5917 | 0680194M01 | RES 10 OHMS 5\% 1W |
| $\begin{array}{\|l} \text { RT5875 } \\ \text { \& } \\ \text { RT5904 } \end{array}$ | 0680149M02 | THERMISTOR CHIP 100K OHM |

HLE6051 UHF R1 100-Watt PA Parts List

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C5801 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| $\begin{aligned} & \hline \text { C5802 \& } \\ & \text { C5803 } \end{aligned}$ |  | NOTPLACED |
| C5804 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5805 | 2113740B30 | CAP CHIP REEL CL1 +/-30 16 |
| C5806 | 2113740B34 | CAP CHIP REEL CL1 +/-30 24 |
| C5807 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5808 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5810 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5811 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5813 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| $\begin{aligned} & \text { C5815 } \\ & \text { thru } \\ & \text { C5818 } \end{aligned}$ | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C5820 | 2113740B25 | CAP CHIP REEL CL1 +/-30 10 |
| C5821 | 2113740B29 | CAP CHIP REEL CL1 +/-30 15 |
| C5822 | 2113740B09 | CAP CHIP REEL CL1 +/-30 2.2 |



| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { C5889 \& } \\ & \text { C5890 } \end{aligned}$ | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| $\begin{aligned} & \begin{array}{l} \text { C5891 } \\ \text { thru } \\ \text { C5894 } \end{array} \end{aligned}$ | 2184366 F44 | CAP MICA 405 250V |
| $\begin{aligned} & \text { C5895 \& } \\ & \text { C5896 } \end{aligned}$ |  | NOTPLACED |
| C5897 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5898 | 2113742B21 | CAP CER 8.2 PF . 50 PF |
| C5899 | --------- | NOTPLACED |
| C5900 | 2111078B19 | CAP CHIP RF 165 NPO 100V |
| C5901 | 2111078B42 | CAP CHIP RF 1005 NPO 100V |
| C5904 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| C5905 | $2113740 B 49$ | CAP CHIP REEL CL1 +/-30 100 |
| C5906 | 2113740 B 23 | CAP CHIP REEL CL1 $+/-308.2$ |
| $\begin{aligned} & \text { C5908 \& } \\ & \text { C5909 } \end{aligned}$ | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5910 | 2113740B01 | CAP CHIP REEL CL1 +/-30 1.0 |
| C5912 | 2111078B42 | CAP CHIP RF 1005 NPO 100 V |
| C5915 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5917 | 2111078B01 | CAP CHIP RF 3.3 .25 NPO 100V |
| C5918 | 2113740B09 | CAP CHIP REEL CL1 +/-30 2.2 |
| C5919 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5920 |  | NOTPLACED |
| C5921 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5922 | ------- | NOTPLACED |
| C5923 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C5924 | 2113740B49 | CAP CHIP REEL CL1 +/-30 100 |
| C5925 | --------- | NOTPLACED |
| C5926 | 2111078B13 | CAP CHIP RF 10.5 NPO 100 V |
| C5927 | 2113742B31 | CAP CER 6.2PF +-.25PF |
| C5928 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C5965 | 2111078B22 | CAP CHIP RF 225 NPO 100V |
| C5997 | $2113742 B 23$ | CAP CHIP 12PF |
| $\begin{aligned} & \begin{array}{l} \text { C5998 } \\ \text { thru } \\ \text { C6000 } \end{array} \end{aligned}$ |  | NOTPLACED |
| C6004 | 2111078B03 | CAP CHIP RF 3.9 .25 NPO 100V |
| C6005 | 0780255R01 | BRKT FLTR |
| CR5875 | 4880222R01 | DIODE REVERSE POLARITY SPECTRA |
| $\begin{array}{\|l} \hline \text { CR5900 } \\ \& \\ \text { CR5902 } \end{array}$ | 4880121R01 | PWR PIN DIODE SMD TAPE \& REEL |
| $\begin{aligned} & \text { CR5904 } \\ & \& \\ & \text { CR5905 } \end{aligned}$ | 4880142L01 | DIODE PIN SOT 23 MMBV 3401 |
| CR5906 | 4813825A05 | DIODE 30V HOT CARRIER MMBD301L |
| E5800 | 4280088P01 | CLIP COAX |
| E5801 | 2680201R02 | HEATSINK DIODE TIN PLATED |
| E5802 | 4280088P01 | CLIP COAX |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| E5803 | 2680187N02 | HEATSINK, TO39 |
| $\begin{array}{\|l\|l\|} \hline E 5806 \text { \& } \\ \text { F5807 } \end{array}$ | 4280088P01 | CLIP COAX |
| E5808 | 4280202R02 | CLIP COAX TIN PLATED |
| J1103 | 2880102M11 | PLUG VERTICAL 12 POS |
| J5901 | 4280088P01 | CLIP COAX |
| L5801 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L5802 | 2480091G21 | COIL AIRWOUND |
| L5805 | 2480091 G23 | COIL AIRWOUND |
| L5806 | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| $\begin{array}{\|l} \hline L 5810 \& \\ L 5811 \\ \hline \end{array}$ | 2484657R01 | INDUCTOR BEAD CHIP |
| $\begin{aligned} & \hline \text { L5820 \& } \\ & \text { L5821 } \end{aligned}$ | 2480091G21 | COIL AIRWOUND |
| L5850 | 2480091G21 | COIL AIRWOUND |
| L5851 | 2480091G23 | COIL AIRWOUND |
| L5852 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5853 | 2480090G03 | COIL AIRWOUND |
| L5854 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5855 | 2480091G21 | COIL AIRWOUND |
| L5857 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5858 | 2480090G03 | COIL AIRWOUND |
| L5859 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5875 | 2480090G03 | COIL AIRWOUND |
| $\begin{array}{\|l\|} \hline \text { L5876 \& } \\ \text { L5877 } \end{array}$ | 2480091 G21 | COIL AIRWOUND |
| $\begin{array}{\|l\|} \hline \text { L5878 \& } \\ \text { L5879 } \end{array}$ | 2484657R01 | INDUCTOR BEAD CHIP |
| $\begin{aligned} & \hline \text { L5880 \& } \\ & \text { L5881 } \\ & \hline \end{aligned}$ | 2480091 G36 | COIL AIRWOUND |
| L5882 | 2480090G16 | COIL AIRWOUND RF 000.000 |
| L5884 | 2484657R01 | INDUCTOR BEAD CHIP |
| L5900 | $2480090 \mathrm{G13}$ | COIL AIRWOUND |
| L5901 | 2480090G16 | COIL AIRWOUND RF 000.000 |
| L5902 | $2480090 \mathrm{G19}$ | COIL AIRWOUND |
| L5903 | 2480090G03 | COIL AIRWOUND |
| L5904 | 2480140 E 11 | INDUCTOR CHIP 360 NH |
| $\begin{array}{\|l\|l} \hline \text { Lhru } \\ \text { thru } \\ \text { L59910 } \end{array}$ | 2480067M01 | CHK RF CHIP BEAD INDUCTOR 28MZ |
| L5927 | 2480091G24 | COIL AIRWOUND |
| MP0002 | 2680139P02 | SHLD HAR FLTR TIN PLATED |
| Q5800 | 4880141L01 | TSTR PNP SOT23 LO PROFILE |
| Q5801 | 4880182D50 | TSTR SI SORF 4 V 750 MW 960 MHZ |
| Q5802 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q5803 | 4880225C09 | TSTR M25C09 |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| Q5804 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| $\begin{aligned} & \hline \text { Q5805 \& } \\ & \text { Q5806 } \end{aligned}$ | 4880141L02 | TSTR NPN SOT23 LO PROFILE TAPE |
| Q5850 | 4880225C27 | TSTR RF MRF654F IN CS12 PKG |
| Q5851 | 4880225C30 | TSTR RF 14W/50W UHF . 5 CQ |
| Q5875 | 4880225C29 | TSTR RF MRF658 IN FLANGE PKG |
| Q5876 | 4880225C29 | TSTR RF MRF658 IN FLANGE PKG |
| R5801 | 0611077A30 | RES CHIP $1551 / 8 \mathrm{~W}$ |
| R5802 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5803 | 0611077A82 | RES CHIP $220051 / 8$ |
| R5805 | 0611077A26 | RES CHIP $1051 / 8 \mathrm{~W}$ |
| R5806 | 0611077A50 | RES CHIP 1005 1/8W |
| R5807 | 0611077A82 | RES CHIP $220051 / 8$ |
| R5808 | 0611077A90 | RES CHIP $470051 / 8$ |
| R5809 | 0611077A78 | RES CHIP $150051 / 8$ |
| R5810 | 0611077A26 | RES CHIP 105 1/8W |
| R5811 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5812 | 0611077A01 | RES CHIP JUMPER |
|  <br> R5814 | --------- | NOTPLACED |
| R5816 | 0611077A80 | RES CHIP 18005 1/8 |
| R5817 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5818 | 0611077A62 | RES CHIP 3305 1/8W |
| R5819 | 0611077A86 | RES CHIP $330051 / 8$ |
| R5820 | 0611077B01 | RES CHIP $12 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R5821 | 0611077A90 | RES CHIP $470051 / 8$ |
| R5822 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5823 | 0611077A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R5824 | 0611077A90 | RES CHIP $470051 / 8$ |
| R5825 | 0611077B05 | RES CHIP 18K 5 1/8W |
| R5826 | 0611077A68 | RES CHIP 5605 1/8W |
| R5827 | 0611077A50 | RES CHIP 1005 1/8W |
| R5828 | $0611077 B 15$ | RES CHIP 47K $51 / 8 \mathrm{~W}$ |
| R5843 | $0611077 B 07$ | RES CHIP $22 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| $\begin{aligned} & \text { R5850 \& } \\ & \text { R5851 } \\ & \hline \end{aligned}$ | 0611077A10 | RES CHIP 2.25 1/8W |
| $\begin{aligned} & \text { R5853 \& } \\ & \text { R5854 } \end{aligned}$ | 0611077A10 | RES CHIP 2.25 1/8W |
| R5855 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5857 | 0611077A82 | RES CHIP 22005 1/8 |
| R5858 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5859 | 0611077A50 | RES CHIP 1005 1/8W |
| R5875 | 1780165C02 | RES SHUNT |
| $\begin{array}{\|l} \hline \text { R5876 \& } \\ \text { R5877 } \\ \hline \end{array}$ | 0611077A10 | RES CHIP 2.25 1/8W |
| R5878 | 0680194M01 | RES 10 OHMS 5\% 1W |
| $\begin{array}{\|l} \hline \text { R5879 \& } \\ \text { R5880 } \\ \hline \end{array}$ | 0680194M13 | RES 33 OHMS 5\% 1W |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \hline \text { R5881 \& } \\ & \text { R5882 } \end{aligned}$ | 0680194M01 | RES 10 OHMS 5\% 1W |
| R5900 | 0680194M18 | RES 51 OHMS 5\% 1W |
| R5901 | 0680194M01 | RES 10 OHMS 5\% 1W |
| R5904 | 0611077A50 | RES CHIP 1005 1/8W |
| R5905 | 0611077A60 | RES CHIP 2705 1/8W |
| R5906 | 0611077A74 | RES CHIP $100051 / 8$ |
| R5907 | 0611077B11 | RES CHIP 33K $51 / 8 \mathrm{~W}$ |
| $\begin{aligned} & \text { R5908 \& } \\ & \text { R5909 } \end{aligned}$ | 0611077A54 | RES CHIP 1505 1/8W |
| R5910 | 0611077 B23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R5911 | 0611077A74 | RES CHIP $100051 / 8$ |
| $\begin{aligned} & \text { R5912 \& } \\ & \text { R5913 } \end{aligned}$ | $0611077 B 47$ | RES CHIP 1 MEG 5 1/8W |
| R5914 | 0611077 A90 | RES CHIP $470051 / 8$ |
| R5915 | 0611077A26 | RES CHIP 105 1/8W |
| R5916 | 0611077 A66 | RES CHIP 4705 1/8W |
| R5917 | 0680194M01 | RES 10 OHMS 5\% 1W |
| $\begin{aligned} & \text { RT5875 } \\ & \& \\ & \text { RT5904 } \\ & \hline \end{aligned}$ | 0680149M02 | THERMISTOR CHIP 100K OHM |
| Z1 | 4813830A14 | DIODE 5.1V 5\% 225MW MMBZ5231B |
| Z2 | --------- | NOTPLACED |
|  | 2280128P01 | PIN POLARIZING |
|  | 8480110P05 | BD CKT PA UHF |



HLF6078B 800 MHz 15-Watt PA Schematic


HLF6078B 800 MHz 15-Watt PA Component Location Diagram, Side 1

VIEWED FROM SIDE 2


| ITEM | MOTOROLA PARTS NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C9500 | 2311049A37 | CAP TANT CHIP 12020 |
| $\begin{aligned} & \text { C9800 \& } \\ & \text { C9801 } \end{aligned}$ | 2113740B13 | CAP CHIP REEL CL1 +/-30 3.3 |
| C9802 | 2113740 B 25 | CAP CHIP REEL CL1 +/-30 10 |
| C9803 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9804 | 2113740B11 | CAP CHIP REEL CL1 +/-30 2.7 |
| C9805 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| $\begin{aligned} & \begin{array}{l} \text { C9806 } \\ \text { thru } \\ \text { C9800 } \end{array} \end{aligned}$ | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9851 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9852 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R } 10 \% \\ & 100000 \end{aligned}$ |
| C9854 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R 10\% } \\ & 100000 \end{aligned}$ |
| C9855 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9857 | 2380090M24 | CAP ALU 1020 50V SURF MT |
| C9858 | 2111078B29 | CAP CHIP RF 335 NPO 100V |
| C9859 | 2113741N69 | $\begin{aligned} & \text { CAP CHIP CL2 X7R } 10 \% \\ & 100000 \end{aligned}$ |
| C9879 | 2111078B32 | CAP CHIP RF 395 NPO 100V |
| C9900 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9902 | 2113740 B 25 | CAP CHIP REEL CL1 +/-30 10 |
| C9920 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9921 | 2111078B32 | CAP CHIP RF 395 NPO 100V |
| C9922 | 2113740B01 | CAP CHIP REEL CL1 +/-30 1.0 |
| C9923 | 2113740B07 | CAP CHIP REEL CL1 +/-30 1.8 |
| $\begin{aligned} & \begin{array}{l} \text { C9924 } \\ \text { thru } \\ \text { C9928 } \end{array} \end{aligned}$ | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9932 | 2311049A37 | CAP TANT CHIP 12020 |
| $\begin{aligned} & \hline \text { C9933 \& } \\ & \text { C9934 } \end{aligned}$ | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| $\begin{aligned} & \hline \begin{array}{l} \text { C9990 } \\ \text { thru } \\ \text { C9944 } \end{array} \\ & \hline \end{aligned}$ | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| CR9800 | 4811058B11 | DIODE 48S11058A11 A/P |
| CR9850 | 4880236E07 | DIODE TRANSIENT SUP |
| CR9900 | 4813825A05 | DIODE 30V HOT CARRIER MMBD301L |
| CR9920 thru CR9922 | 4880236E24 | DIODE PIN |
| CR9923 | 4811058B11 | DIODE 48S11058A11 A/P |
| J9860 | 2980014A03 | CLIP COAX TERMINAL |
| L9800 | 2480091 G 23 | COIL AIRWOUND |
| L9801 | 2480091 G24 | COIL AIRWOUND |
| L9803 | 2480091 G23 | COIL AIRWOUND |
| L9804 | 2480091 G06 | COIL AIRWOUND |
| L9805 | 2480091 G 23 | COIL AIRWOUND |


| ITEM | MOTOROLA PARTS NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| $\begin{aligned} & \text { L9900 \& } \\ & \text { L9920 } \end{aligned}$ | 2480091G32 | COIL AIRWOUND |
| L9921 | 2480091 G40 | COIL AIRWOUND |
| L9922 | 2480091 G32 | COIL AIRWOUND |
| L9923 | 2480091 G23 | COIL AIRWOUND |
| L9924 | 2480091 G01 | COIL AIRWOUND |
| $\begin{array}{\|l\|} \hline \text { L9925 \& } \\ \text { L9926 } \end{array}$ | 2480091G24 | COIL AIRWOUND |
| L9927 | 2480091G06 | COIL AIRWOUND |
| P9853 | 6480262N01 | PLATE LP FEEDTHRU ASSEMBLY |
| $\begin{array}{\|l\|l\|} \hline \text { P9950 \& \& } \\ \hline \text { P9951 } \end{array}$ | 2980014A03 | CLIP COAX TERMINAL |
| Q9800 | 4880182D50 | TSTR SI SORF 4V 750MW 960MHZ |
| Q9500 | 4813822D56 | TSTR NPN 100V 6A |
| Q9920 | 4880048M01 | TSTR NPN DIG 47K/47K |
| R9500 | 0611077A74 | RES CHIP $100051 / 8$ |
| R9501 | 0611077A28 | RES CHIP 125 1/8W |
| R9560 | 0611077 B 23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| R9561 | 0611077A94 | RES CHIP $680051 / 8$ |
| R9562 | 0611077A86 | RES CHIP $330051 / 8$ |
| R9563 | 0611077A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R9800 | 0611077A54 | RES CHIP 1505 1/8W |
| R9801 | 0611077A74 | RES CHIP $100051 / 8$ |
| $\begin{array}{\|l} \text { R9802 \& } \\ \text { R9805 } \end{array}$ | 0611077A50 | RES CHIP 1005 1/8W |
| R9806 | 0611077A50 | RES CHIP 1005 1/8W |
| R9807 | 0611077A26 | RES CHIP 105 1/8W |
| R9875 | 1780228N03 | RESISTOR SHUNT |
| R9900 | 0611077A43 | RES CHIP 515 1/8W |
| $\begin{array}{\|l} \hline \text { R9901 \& } \\ \text { R9902 } \\ \hline \end{array}$ | 0680194M18 | RES 51 OHMS 5\% 1W |
| R9905 | 0611077B23 | RES CHIP 100K $51 / 8 \mathrm{~W}$ |
| $\begin{array}{\|l} \text { R9920 \& \& } \\ \text { R9921 } \end{array}$ | 0680194M18 | RES 51 OHMS 5\% 1W |
| $\begin{array}{\|l} \hline \begin{array}{l} \text { R99222 } \\ \text { thru } \\ \text { R9997 } \end{array} \\ \hline \end{array}$ | 0611077A74 | RES CHIP 10005 1/8 |
| $\begin{array}{\|l} \hline \begin{array}{l} \text { R990 } \\ \text { thru } \\ \text { R9995 } \\ \hline \end{array} \\ \hline \end{array}$ | 0611077A01 | RES CHIP JUMPER |
| RT9560 | 0680149M02 | THERMISTOR CHIP 100K OHM |
| U9850 | 5180110E01 | MODE RF PWR 800MH 20W |
|  | 2280128P01 | PIN POLARIZING |
|  | 8480130P05 | BD CKT 1.5/15W PA |




Spectra
8480156p04.A

HLF6077D 800 MHz 35-Watt PA Component Location Diagram, Side 1


HLF6077D 800 MHz 35-Watt PA Component Location Diagram, Side 2

| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| C9500 | 2311049A37 | CAP TANT CHIP 12020 |
| C9560 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9800 \& C9801 | $2113740 B 13$ | CAP CHIP REEL CL1 +/-30 3.3 |
| C9802 | $2113740 B 15$ | CAP CHIP REEL CL1 +/-30 3.9 |
| C9803 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9804 | 2113740B21 | CAP CHIP REEL CL1 +/-30 6.8 |
| $\begin{array}{\|l} \hline \text { C9806 } \\ \text { thru } \\ \text { C9808 } \end{array}$ | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9809 |  | NOTPLACED |
| C9851 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9852 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C9853 | 2311049A37 | CAP TANT CHIP 12020 |
| C9854 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C9855 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9856 | 2111078 B42 | CAP CHIP RF 1005 NPO 100V |
| C9857 | 2380090M24 | CAP ALU 1020 50V SURF MT |
| C9858 | 2111078829 | CAP CHIP RF 335 NPO 100V |
| C9859 | ---- | NOTPLACED |
| C9879 | 2180240G69 | CAP MTL CLAD MICA PF |
| C9880 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9881 | 2113741N69 | CAP CHIP CL2 X7R 10\% 100000 |
| C9882 | 2113741N45 | CAP CHIP CL2 X7R 10\% 10000 |
| C9883 | 2111078B32 | CAP CHIP RF 395 NPO 100V |
| C9884 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| $\begin{aligned} & \text { C9885 \& } \\ & \text { C9890 } \end{aligned}$ |  | NOTPLACED |
| C9900 | 2113740 B 39 | CAP CHIP REEL CL1 +/-30 39 |
| C9901 | $2113740 B 13$ | CAP CHIP REEL CL1 +/-30 3.3 |
| C9902 | $2113740 B 25$ | CAP CHIP REEL CL1 +/-30 10 |
| $\begin{aligned} & \text { C9903 \& } \\ & \text { C9920 } \\ & \hline \end{aligned}$ | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9921 | 2111078B32 | CAP CHIP RF 395 NPO 100V |
| C9922 | 2113740 001 | CAP CHIP REEL CL1 +/-30 1.0 |
| C9923 | 2113740B05 | CAP CHIP REEL CL1 +/-30 1.5 |
| $\begin{aligned} & \hline \text { C9924 } \\ & \text { thru } \\ & \text { C9929 } \\ & \hline \end{aligned}$ | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9930 | $2113740 B 25$ | CAP CHIP REEL CL1 +/-30 10 |
| C9931 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9932 | 2311049A37 | CAP TANT CHIP 12020 |
| $\begin{aligned} & \hline \begin{array}{l} \text { C9933 } \\ \text { thru } \\ \text { C9941 } \end{array} \\ & \hline \end{aligned}$ | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| $\begin{aligned} & \text { C9942 \& } \\ & \text { C9943 } \end{aligned}$ |  | NOTPLACED |
| C9945 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |
| C9946 | --------- | NOTPLACED |
| C9947 | 2113740B39 | CAP CHIP REEL CL1 +/-30 39 |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| CR9800 | 4880066M01 | DIODE SIGNAL RLS4148 |
| CR9850 | 4880236E07 | DIODE TRANSIENT SUP |
| CR9900 | 4813825A05 | DIODE 30V HOT CARRIER MMBD301L |
| $\begin{array}{\|l\|} \hline \text { CR9920 } \\ \text { thru } \\ \text { CR9922 } \end{array}$ | 4880236E24 | DIODE PIN |
| CR9923 | 4880066M01 | DIODE SIGNAL RLS4148 |
| CR9930 | 4813825A05 | DIODE 30V HOT CARRIER MMBD301L |
| J9001 | 2980014A03 | CLIP COAX TERMINAL |
| L9800 | 2480091 G23 | COIL AIRWOUND |
| L9801 | 2480091 G24 | COIL AIRWOUND |
| L9802 | 2480091 G13 | COIL AIRWOUND |
| L9803 | 2480091G24 | COIL AIRWOUND |
| L9804 | 2480091 G06 | COIL AIRWOUND |
| L9805 | 2480091 G23 | COIL AIRWOUND |
| L9806 | 2480091 G06 | COIL AIRWOUND |
| L9876 | 2480091G21 | COIL AIRWOUND |
| L9877 | 2480090G05 | COIL AIRWOUND |
| $\begin{aligned} & \text { L9910 \& } \\ & \text { L9920 } \end{aligned}$ | 2480091G32 | COIL AIRWOUND |
| L9921 | 2480091 G40 | COIL AIRWOUND |
| L9922 | 2480091 G06 | COIL AIRWOUND |
| $\begin{array}{\|l\|} \hline \text { L9923 } \\ \text { thru } \\ \hline \text { L9926 } \\ \hline \end{array}$ | 2480091 G24 | COIL AIRWOUND |
| L9930 | 2480091G21 | COIL AIRWOUND |
| MP0001 | 2680090N02 | SHLD MID PWR FENCE TIN PLATED |
| P0853 | 0980103M04 | RECEPTICAL RIGHT ANGLE 12 POS |
| $\begin{array}{\|l\|} \hline \text { P9227 \& } \\ \text { P9741 } \\ \hline \end{array}$ | 2980014A03 | CLIP COAX TERMINAL |
| Q9500 | 4813822D56 | TSTR NPN 100V 6A |
| Q9510 | 4880048M01 | TSTR NPN DIG 47K/47K |
| Q9515 | 4880141L01 | TSTR PNP SOT23 LO PROFILE TAPE |
| Q9800 | 4880182 D 50 | TSTR SI SORF 4V 750MW 960MHZ |
| Q9920 | 4880048M01 | TSTR NPN DIG 47K/47K |
| R9500 | 0611077A74 | RES CHIP $100051 / 8$ |
| R9501 | 0611077A28 | RES CHIP 125 1/8W |
| R9510 | 0611077 B 11 | RES CHIP 33K $51 / 8 \mathrm{~W}$ |
| R9511 | 0611077A98 | RES CHIP $10 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R9512 | 0611077A01 | RES CHIP JUMPER |
| R9513 | 0611077 B07 | RES CHIP $22 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R9560 | --------- | NOTPLACED |
| R9561 | 0611077A90 | RES CHIP $470051 / 8$ |
| R9562 | 0611077 B 11 | RES CHIP 33K $51 / 8 \mathrm{~W}$ |
| R9563 | 0611077 B 21 | RES CHIP $82 \mathrm{~K} 51 / 8 \mathrm{~W}$ |
| R9800 | 0611077A54 | RES CHIP $15051 / 8 \mathrm{~W}$ |


| ITEM | MOTOROLA PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: |
| R9801 | 0611077A74 | RES CHIP 10005 1/8 |
| R9802 | 0611077A50 | RES CHIP 1005 1/8W |
| R9803 |  | NOTPLACED |
| $\begin{array}{\|l\|} \hline \text { R9805 \& } \\ \text { R9806 } \\ \hline \end{array}$ | 0611077A50 | RES CHIP 1005 1/8W |
| $\begin{array}{\|l\|} \hline \text { R9807 \& } \\ \text { R9809 } \\ \hline \end{array}$ |  | NOTPLACED |
| $\begin{array}{\|l} \hline \text { R9870 \& } \\ \text { R9871 } \end{array}$ | 0611077A01 | RES CHIP JUMPER |
| $\begin{aligned} & \text { R9872 \& } \\ & \text { R9873 } \end{aligned}$ |  | NOTPLACED |
| R9874 | 0611077A01 | RES CHIP JUMPER |
| R9875 | 1780228N01 | RESISTOR SHUNT |
| R9876 | --------- | NOTPLACED |
| $\begin{array}{\|l\|l\|} \hline \text { R9877 \& } \\ \text { R9878 } \end{array}$ | 0611077A26 | RES CHIP 105 1/8W |
| $\begin{aligned} & \text { R9901 \& } \\ & \text { R9902 } \end{aligned}$ | 0680194M18 | RES 51 OHMS 5\% 1W |
| R9904 | $0611077 \mathrm{B07}$ | RES CHIP 22K 5 1/8W |
| R9905 | 0611077 B 11 | RES CHIP 33K 5 1/8W |
| R9906 | --------- | NOTPLACED |
| $\begin{array}{\|l} \hline \text { R9920 \& } \\ \text { R9921 } \\ \hline \end{array}$ | 0680194M18 | RES 51 OHMS 5\% 1W |
| $\begin{array}{\|l} \hline \begin{array}{l} \text { R9922 } \\ \text { thru } \\ \text { R99927 } \end{array} \\ \hline \end{array}$ | 0611077A74 | RES CHIP $100051 / 8$ |
| $\begin{aligned} & \text { R9928 \& } \\ & \text { R9929 } \end{aligned}$ | 0611077A01 | RES CHIP JUMPER |
| R9930 | 0611077A52 | RES CHIP 1205 1/8W |
| R9931 | 0611077A50 | RES CHIP 1005 1/8W |
| RT9560 | 0680149M02 | THERMISTOR CHIP 100K OHM |
| U9850 | 5180110 E01 | MODE RF PWR 800MH 20W |
|  | 2280128P01 | PIN POLARIZING |
|  | 8480156P04 | BD CKT PA |

## Appendix A Secure Modules

## A. 1 Introduction

The secure modules are designed to digitally encrypt and decrypt voice and ASTRO data in ASTRO Digital Spectra and Digital Spectra Plus mobile radios. This section covers the following secure modules:

Table A-1. ASTRO Digital Spectra Secure Modules

| KIT NO. | ENCRYPTION TYPE |
| :--- | :--- |
| HLN1441 | DES, DES-XL, DES-OFB |
| NTN1158 | DVI-XL |
| NTN1147 | DVP-XL |
| NTN1369 | DES-XL, DVP-XL |
| NTN1565 | DES-OFB, DVP-XL |

Table A-2. ASTRO Digital Spectra Plus Secure Modules

| KIT NO. | ENCRYPTION TYPE |
| :--- | :--- |
| NNTN4024A | DES, DES-XL, DES-OFB |
| NNTN4025A | DVI-XL |
| NNTN4026A | DVP-XL |
| NNTN4027A | DES, DES-XL, DES-OFB <br> with DVP-XL Encryption Kit |

NOTE: The secure module is NOT serviceable. The information contained in this section is only meant to help determine whether a problem is due to the secure module or the radio itself.
The secure module uses a custom encryption integrated circuit (IC) and an encryption key variable to perform its encode/decode function. The encryption key variable is loaded into the secure module, via the radio's microphone connector, from a hand-held key-variable loader (KVL). The encryption IC corresponds to the particular encryption algorithm purchased. The encryption algorithms and their corresponding kit numbers are listed in the above table.
NOTE: When ordering replacement modules, make sure that the kit and board numbers of the module that is ordered are the same as those of the module that is being replaced.

## A. 2 Circuit Description

The secure module operates from three power supplies (UNSW_B+, SW_B+, and KG_BACKUP). The SW_B+ is turned on and off by the radio's on/off switch. The UNSW_B+ provides power to the secure module as long as the radio battery is in place.
Key variables are loaded into the secure module through connector P1, pin 15. Up to 16 keys (depending on the type of encryption module) can be stored in the module at a time. The key can be infinite key retention or 3 day key retention, depending on how the code plug is setup.

The radio's host processor communicates with the secure module on the Serial Peripheral Interface (SPI) bus. The host processor is the master on this bus, while the secure module is a slave on the bus. The SPI bus consists of five signal lines. Refer to Table A-1 for signal information. A communications failure between the host processor and the secure module will be indicated as an "ERROR 09/10" message on the radio display.

## A. 3 Troubleshooting Secure Operations

Refer to the ASTRO Digital Spectra and Digital Spectra Plus Mobile Radios Basic Service Manual (68P81076C20) for disassembly and reassembly information. A key-variable loader (KVL) and oscilloscope are needed to troubleshoot the secure module.
NOTE: The secure module itself is not serviceable. If the secure module is found to be defective, it must be replaced.

## A.3.1 Error 09/10, Error 09/90

The ASTRO Digital Spectra Mobile Radio automatically performs a self test on every power-up. Should the radio fail the self tests, the display will show "ERROR 09/10" or "ERROR 09/90" accompanied by a short beep. If the display shows "ERROR 09/10" or "ERROR 09/90," the radio failed the secure power-up tests and the host microcontroller was unable to communicate with the secure module via the SPI bus. Turn the radio off and back on. If the radio still does not pass the self tests, then a problem exists with the secure operations of the radio.

Troubleshooting information for "ERROR 09/10" is found in Troubleshooting Chart C.17, "09/10, Secure Hardware Failure," on page 5-13. For "ERROR 09/90," see Troubleshooting Chart C.18, "09/90, Secure Hardware Failure," on page 5-13.

## A.3.2 Keyload

When the keyloading cable is attached to the ASTRO Digital Spectra Mobile Radio and "KEYLOADING" is not displayed on the ASTRO Digital Spectra Mobile Radio display, then the radio has not gone into KEYLOAD mode. For troubleshooting a "KEYLOAD" failure, refer to Troubleshooting Chart C.21, "Key Load Fail," on page 5-16.

NOTE: ASTRO Digital Spectra Mobile Radios need a keyloader that has the ability to keyload a ASTRO Digital Spectra Mobile Radio. The keyloader must be either a KVL-3000 or later model key-variable loader.

## Appendix B Replacement Parts Ordering

## B. 1 Basic Ordering Information

When ordering replacement parts or equipment information, the complete identification number should be included. This applies to all components, kits, and chassis. If the component part number is not known, the order should include the number of the chassis or kit of which it is a part, and sufficient description of the desired component to identify it.

Crystal orders should specify the crystal type number, crystal and carrier frequency, and the model number in which the part is used.

## B. 2 Transceiver Board and VOCON Board Ordering Information

When ordering a replacement Transceiver Board or VOCON Board, refer to the applicable Model Chart in the front of this manual, read the Transceiver Board or VOCON Board note, and include the proper information with your order.

## B. 3 Motorola Online

Motorola Online users can access our online catalog at https://www.motorola.com/businessonline
To register for online access, please call 800-814-0601.

## B. 4 Mail Orders

Send written orders to the following addresses:

| Replacement Parts/ | Federal Government Orders: | International Orders: |
| :--- | :--- | :--- |
| Test Equipment/Manuals/ |  |  |
| Crystal Service Items: |  |  |
|  |  | Motorola Inc. |
| Motorola Inc. | Motorola Inc. | Customer Care and Services |
| Customer Care and Services | U.S. Federal Government | Division |
| Division | Markets Division | (United States and Canada) |
| (United States and Canada) | Attention: Order Processing | Attention: Order Processing |
| Attention: Order Processing | 7230 Parkway Drive | 1307 E. Algonquin Road |
| 1307 E. Algonquin Road | Landover, MD 21076 | Schaumburg, IL 60196 |
| Schaumburg, IL 60196 |  |  |

## B. 5 Telephone Orders

Customer Care and Services Division
(United States and Canada)
7:00 AM to 7:00 PM (Central Standard Time)
Monday through Friday (Chicago, U.S.A.)
1-800-422-4210
1-847-538-8023 (International Orders)
U.S. Federal Government Markets Division (USFGMD)

1-800-826-1913 Federal Government Parts - Credit Cards Only
8:30 AM to 5:00 PM (Eastern Standard Time)

## B. 6 Fax Orders

Customer Care and Services Division
(United States and Canada)
1-800-622-6210
847-576-3023 (International)
USFGMD
(Federal Government Orders)
1-800-526-8641 (For Parts and Equipment Purchase Orders)

## B. 7 Parts Identification

Customer Care and Services Division
(United States and Canada)
1-800-422-4210, menu 3

## B. 8 Product Customer Service

Customer Response Center
(Non-technical Issues)
1-800-247-2346
FAX:1-800-247-2347

## Glossary

This glossary contains an alphabetical listing of terms and their definitions that are applicable to ASTRO portable and mobile subscriber radio products.

Term

## A/D

| Abacus IC | A custom integrated circuit providing a digital receiver intermediate frequency (IF) backend. |
| :---: | :---: |
| ADC | See analog-to-digital converter. |
| ADDAG | See Analog-to-Digital, Digital-to-Analog and Glue. |
| ALC | See automatic level control. |
| analog | Refers to a continuously variable signal or a circuit or device designed to handle such signals. See also digital. |
| Analog-to-Digital, Digital-to-Analog and Glue | An integrated circuit designed to be an interface between the radio's DSP, which is digital, and the analog transmitter and receiver ICs. |
| analog-to-digital conversion | Conversion of an instantaneous dc voltage level to a corresponding digital value. See also D/A. |
| analog-to-digital converter | A device that converts analog signals into digital data. See also DAC. |
| automatic level control | A circuit in the transmit RF path that controls RF power amplifier output, provides leveling over frequency and voltage, and protects against high VSWR. |
| band | Frequencies allowed for a specific purpose. |
| BBP | See baseband interface port. |
| baseband interface port | Synchronous serial interface to the transceiver board used to transfer transmit and receive audio data. |
| BGA | See ball grid array. |
| ball grid array | A type of IC package characterized by solder balls arranged in a grid that are located on the underside of the package. |
| CODEC | See coder/decoder. |
| coder/decoder | A device that encodes or decodes a signal. |

See analog-to-digital conversion.
A custom integrated circuit providing a digital receiver intermediate frequency (IF) backend.

See analog-to-digital converter.
See Analog-to-Digital, Digital-to-Analog and Glue.
See automatic level control.
Refers to a continuously variable signal or a circuit or device designed to handle such signals. See also digital.

An integrated circuit designed to be an interface between the radio's DSP, which is digital, and the analog transmitter and receiver ICs.

Conversion of an instantaneous dc voltage level to a corresponding digital value. See also D/A.

A device that converts analog signals into digital data. See also DAC.

A circuit in the transmit RF path that controls RF power amplifier output, provides leveling over frequency and voltage, and protects against high VSWR.

Frequencies allowed for a specific purpose.
See baseband interface port.
Synchronous serial interface to the transceiver board used to transfer transmit and receive audio data.

See ball grid array.
A type of IC package characterized by solder balls arranged in a grid that are located on the underside of the package.

A device that encodes or decodes a signal.

## Term

## CPS

Customer Programming Software

## D/A

DAC
default
digital
digital-to-analog conversion
digital-to-analog converter

Digital Private Line

## See Customer Programming Software.

Software with a graphical user interface containing the feature set of an ASTRO radio. See also RSS.

See digital-to-analog conversion.
See digital-to-analog converter.
A pre-defined set of parameters.
Refers to data that is stored or transmitted as a sequence of discrete symbols from a finite set; most commonly this means binary data represented using electronic or electromagnetic signals. See also analog.

Conversion of a digital signal to a voltage that is proportional to the input value. See also $A / D$.

A device that converts digital data into analog signals. See also $A D C$.

A type of digital communications that utilizes privacy call, as well as memory channel and busy channel lock out to enhance communication efficiency.

A microcontroller specifically designed for performing the mathematics involved in manipulating analog information, such as sound, that has been converted into a digital form. DSP also implies the use of a data compression technique.

Object code executed by the Digital Signal Processor in an ASTRO subscriber radio. The DSP is responsible for computation-intensive tasks, such as decoding ASTRO signaling.

See Digital Private Line. See also PL.
See digital signal processor.
See digital signal processor code.
See dual tone multi-frequency.
The system used by touch-tone telephones. DTMF assigns a specific frequency, or tone, to each key so that it can easily be identified by a microprocessor.

Electrically Programmable Digital Potentiometer.
See Electrically Erasable Programmable Read-Only Memory.

## Term

## Definition

Electrically Erasable
Programmable
Read-Only Memory
FCC
firmware

FGU
flash

FLASHcode

FLASHport

## FMR

Florida Manual
Revision
frequency
frequency
generation unit

General-Purpose
Input/Output
GPIO
host code

IC See integrated circuit.
IF
IMBE power is turned off. from RAM.

See frequency generation unit. time.

See Florida Manual Revision. communication network.

Intermediate Frequency.

A special type of PROM that can be erased by exposing it to an electrical charge. An EEPROM retains its contents even when the

Federal Communications Commission.
Code executed by an embedded processor such as the Host or DSP in a subscriber radio. This type of code is typically resident in non-volatile memory and as such is more difficult to change than code executed

A non-volatile memory device similar to an EEPROM. Flash memory can be erased and reprogrammed in blocks instead of one byte at a

A 13-digit code which uniquely identifies the System Software Package and Software Revenue Options that are enabled in a particular subscriber radio. FLASHcodes are only applicable for radios which are upgradeable through the FLASHport process.

A Motorola term that describes the ability of a radio to change memory. Every FLASHport radio contains a FLASHport EEPROM memory chip that can be software written and rewritten to, again and again.

A publication that provides supplemental information for its parent publication before it is revised and reissued.

Number of times a complete electromagnetic-wave cycle occurs in a fixed unit of time (usually one second).

This unit generates ultra-stable, low-phase noise master clock and other derived synchronization clocks that are distributed throughout the

Pins whose function is programmable.

## See General-Purpose Input/Output.

Object code executed by the host processor in an ASTRO subscriber radio. The host is responsible for control-oriented tasks such as decoding and responding to user inputs.

A sub-band, voice-encoding algorithm used in ASTRO digital voice.

| Term | Definition |
| :---: | :---: |
| inbound signaling word | Data transmitted on the control channel from a subscriber unit to the central control unit. |
| integrated circuit | An assembly of interconnected components on a small semiconductor chip, usually made of silicon. One chip can contain millions of microscopic components and perform many functions. |
| ISW | See inbound signaling word. |
| key-variable loader | A device used to load encryption keys into a radio. |
| kHz | See kilohertz. |
| kilohertz | One thousand cycles per second. Used especially as a radio-frequency unit. |
| KVL | See key-variable loader. |
| LCD | See liquid-crystal display. |
| LED | See LED. |
| light emitting diode | An electronic device that lights up when electricity is passed through it. |
| liquid-crystal display | An LCD uses two sheets of polarizing material with a liquid-crystal solution between them. An electric current passed through the liquid causes the crystals to align so that light cannot pass through them. |
| LO | Local oscillator. |
| low-speed handshake | 150-baud digital data sent to the radio during trunked operation while receiving audio. |
| LSH | See low-speed handshake. |
| Master In Slave Out | SPI data line from a peripheral to the MCU. |
| Master Out Slave In | SPI data line from the MCU to a peripheral. |
| MCU | See microcontroller unit. |
| MDC | Motorola Digital Communications. |
| MDI | MCU/DSP Interface internal to the Patriot IC. |
| MHz | See Megahertz. |
| Megahertz | One million cycles per second. Used especially as a radio-frequency unit. |
| microcontroller unit | Also written as $\mu \mathrm{C}$. A microprocessor that contains RAM and ROM components, as well as communications and programming components and peripherals. |
| MISO | See Master In Slave Out. |

Term

| MOSI | See Master Out Slave In. |
| :---: | :---: |
| multiplexer | An electronic device that combines several signals for transmission on some shared medium (e.g., a telephone wire). |
| MUX | See multiplexer. |
| NiCd | Nickel-cadmium. |
| NiMH | Nickel-metal-hydride. |
| OMPAC | See over-molded pad-array carrier. |
| open architecture | A controller configuration that utilizes a microprocessor with extended ROM, RAM, and EEPROM. |
| oscillator | An electronic device that produces alternating electric current and commonly employs tuned circuits and amplifying components. |
| OSW | See outbound signaling word. |
| OTAR | See over-the-air rekeying. |
| outbound signaling word | Data transmitted on the control channel from the central controller to the subscriber unit. |
| over-molded padarray carrier | A Motorola custom IC package, distinguished by the presence of solder balls on the bottom pads. |
| over-the-air rekeying | Allows the dispatcher to remotely reprogram the encryption keys in the radio. |
| PA | Power amplifier. |
| paging | One-way communication that alerts the receiver to retrieve a message. |
| Patriot IC | A dual-core processor that contains an MCU and a DSP in one IC package. |
| PC Board | Printed Circuit Board. Also referred to as a PCB. |
| phase-locked loop | A circuit in which an oscillator is kept in phase with a reference, usually after passing through a frequency divider. |
| PL | See private-line tone squelch. |
| PLL | See phase-locked loop. |
| private-line tone squelch | A continuous sub-audible tone that is transmitted along with the carrier. See also DPL. |
| Programmable Read-Only Memory | A memory chip on which data can be written only once. Once data has been written onto a PROM, it remains there forever. |
| PROM | See Programmable Read-Only Memory. |

## Term <br> Definition

PTT
Push-to-Talk
radio frequency
radio frequency power amplifier

Radio Interface Box

Radio Service
Software
random access memory

RAM
read-only memory

| real-time clock | A module that keeps track of elapsed time even when a computer is <br> turned off. |
| :--- | :--- |
| receiver | Electronic device that amplifies RF signals. A receiver separates the <br> audio signal from the RF carrier, amplifies it, and converts it back to the <br> original sound waves. |
| registers | Short-term data-storage circuits within the microcontroller unit or <br> programmable logic IC. |
| repeater | Remote transmit/receive facility that re-transmits received signals in <br> order to improve communications range and coverage (conventional <br> operation). |
| repeater/talkaround | A conventional radio feature that permits communication through a <br> receive/transmit facility, which re-transmits received signals in order to <br> improve communication range and coverage. |
| RESET | Reset line: an input to the microcontroller that restarts execution. |
| RF | See radio frequency. |
| RF PA | See radio frequency power amplifier. |
| RIB | See Radio Interface Box. |

## Term

## Definition

| RPCIC | Regulator/power control IC. |
| :--- | :--- |
| RPT/TA | See repeater/talkaround. |
| RSS | See Radio Service Software. |
| RTC | See real-time clock. |
| RX | Receive. |
| RX DATA | Recovered digital data line. |
| SAP | See Serial Audio CODEC Port. |
| SCI IN | Serial Communications Interface Input line. |
| Serial Audio CODEC | SSI to and from the GCAP II IC CODEC used to transfer transmit and <br> receive audio data. |
| Sert | A full-duplex (receiver/transmitter) asynchronous serial interface. |
| Communication | See Serial Communication Interface Input Line. |
| Interface Input Line | How the microcontroller communicates to modules and ICs through the |
| SCI IN | CLOCK and DATA lines. |
| Serial Peripheral | An electrically transmitted electromagnetic wave. |
| Interface | An operating mode in which the radio is muted, but still continues to |
| signal | analyze receive data to determine RX signal type. |
| Signal Qualifier | See software potentiometer. |
| mode | Computer programs, procedures, rules, documentation, and data <br> softpot |
| pertaining to the operation of a system. |  |


| Term | Definition |
| :---: | :---: |
| Standby mode | An operating mode in which the radio is muted but still continues to monitor data. |
| static RAM | A type of memory used for volatile, program/data memory that does not need to be refreshed. |
| Synchronous Serial Interface | DSP interface to peripherals that consists of a clock signal line, a frame synchronization signal line, and a data line. |
| system central controllers | Main control unit of the trunked dispatch system; handles ISW and OSW messages to and from subscriber units (See ISW and OSW). |
| system select | The act of selecting the desired operating system with the system-select switch (also, the name given to this switch). |
| thin small-outline package | A type of dynamic random-access memory (DRAM) package that is commonly used in memory applications. |
| time-out timer | A timer that limits the length of a transmission. |
| TOT | See time-out timer. |
| transceiver | Transmitter-receiver. A device that both transmits and receives analog or digital signals. Also abbreviated as XCVR. |
| transmitter | Electronic equipment that generates and amplifies an RF carrier signal, modulates the signal, and then radiates it into space. |
| TSOP | See thin small-outline package. |
| TX | Transmit. |
| UART | See also Universal Asynchronous Receiver Transmitter. |
| UHF | Ultra-High Frequency. |
| Universal Asynchronous Receiver Transmitter | A microchip with programming that controls a computer's interface to its attached serial devices. |
| Universal Serial Bus | An external bus standard that supports data transfer rates of 12 Mbps . |
| USB | See Universal Serial Bus. |
| VCO | See voltage-controlled oscillator. |
| vector sum excited linear predictive coding | A voice-encoding technique used in ASTRO digital voice. |
| VHF | Very-High Frequency. |
| VIP | Vehicle Interface Port. |
| VOCON | See vocoder/controller. |


| Term | Definition |
| :--- | :--- |
| vocoder | An electronic device for synthesizing speech by implementing a <br> compression algorithm particular to voice. See also voice encoder. |
| vocoder/controller | A PC board that contains an ASTRO radio's microcontroller, DSP, <br> memory, audio and power functions, and interface support circuitry. |
| voice encoder | The DSP-based system for digitally processing analog signals, and <br> includes the capabilities of performing voice compression algorithms or <br> voice encoding. See also vocoder. |
| voltage-controlled | An oscillator in which the frequency of oscillation can be varied by <br> changing a control voltage. |
| oscillator |  |

## This Page Intentionally Left Blank

## M MOTOROLA

Motorola, Inc.
8000 West Sunrise Boulevard
Ft. Lauderdale, FL 33322
MOTOROLA, the Stylized M Logo, ASTRO, and Spectra are registered in the U.S. Patent and Trademark Office. All other product or service names are the property of their respective owners. © Motorola, Inc. 2002
All rights reserved. Printed in U.S.A.



[^0]:    X = Item Included
    _ = the latest version kit. When ordering a kit, refer to your specific kit for the suffix number.
    ${ }^{*}=$ kit not available. Order piece parts from the Accessories and Aftermarket Division.

[^1]:    X = Item Included
    = the latest version kit. When ordering a kit, refer to your specific kit for the suffix number.
    ${ }^{\star}=$ kit not available. Order piece parts from the Accessories and Aftermarket Division.

[^2]:    $X=$ Item Included
    _ = the latest version kit. When ordering a kit, refer to your specific kit for the suffix number.

    * $=$ kit not available. Order piece parts from the Accessories and Aftermarket Division.

[^3]:    $X=$ Item Included
    = the latest version kit. When ordering a kit, refer to your specific kit for the suffix number.

    * $=$ kit not available. Order piece parts from the Accessories and Aftermarket Division.

[^4]:    X = Item Included
    _ = the latest version kit. When ordering a kit, refer to your specific kit for the suffix number.
    ${ }^{*}=$ kit not available. Order piece parts from the Accessories and Aftermarket Division.

[^5]:    $X=$ Item Included
    _ = the latest version kit. When ordering a kit, refer to your specific kit for the suffix number.

    * $=$ kit not available. Order piece parts from the Accessories and Aftermarket Division.

[^6]:    $X=$ Item Included
    _ = the latest version kit. When ordering a kit, refer to your specific kit for the suffix number.

    * $=$ kit not available. Order piece parts from the Accessories and Aftermarket Division.

[^7]:    $X=$ Item Included
    O = Optional item
    = the latest version kit. When ordering a kit, refer to your specific kit for the suffix number.
    $\bar{*}=$ kit not available. Order piece parts from the Accessories and Aftermarket Division.

[^8]:    $X=$ Item Included
    O = Optional item

    - = the latest version kit. When ordering a kit, refer to your specific kit for the suffix number.
    ${ }^{*}=$ kit not available. Order piece parts from the Accessories and Aftermarket Division.

[^9]:    X = Item Included
    O = Optional
    _ = the latest version kit. When ordering a kit, refer to your specific kit for the suffix number.

    * $=$ kit not available. Order piece parts from the Accessories and Aftermarket Division.

[^10]:    X = Item Included
    O = Optional
    _ = the latest version kit. When ordering a kit, refer to your specific kit for the suffix number.

    * $=$ kit not available. Order piece parts from the Accessories and Aftermarket Division.

